

System TRAIN-Z: memory address decoder Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
TITLE: MMU_V101r4	
Document Number:	REV: 4
Date: 3/15/13 11:51 AM	Sheet: 1/4

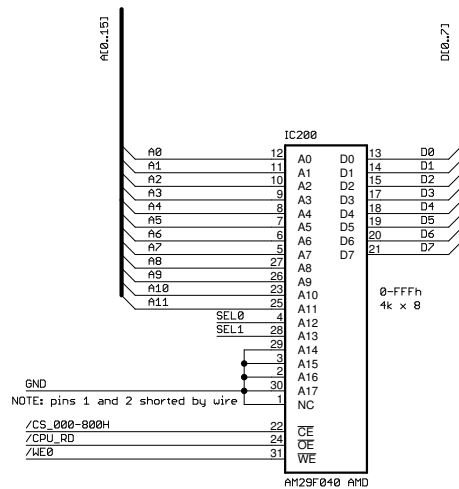
## BOOT PROM

## USER PROM

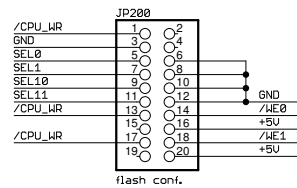
## static RAM

various device mounting possible  
address ranges must not overlap !  
see static RAM section !

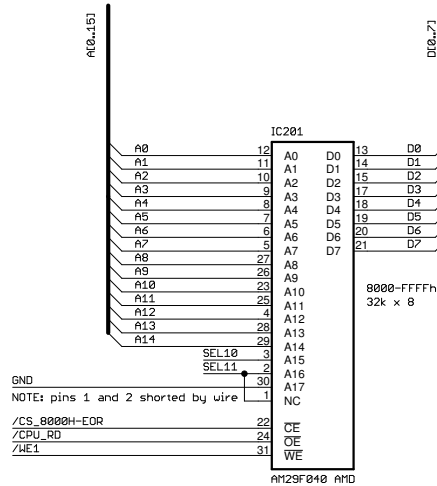
various device mounting possible  
address ranges must not overlap !  
see user PROM section !



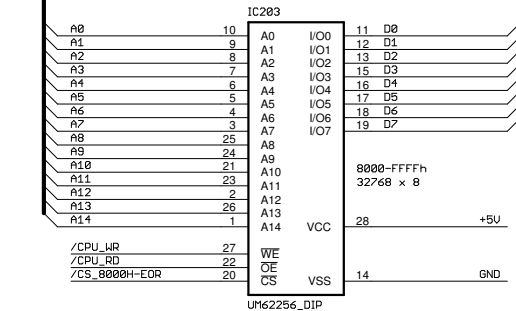
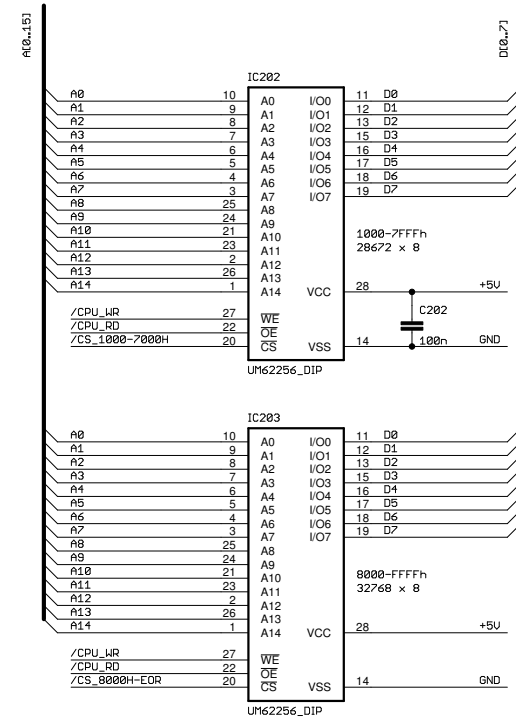
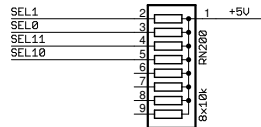
NOTE: IC200 may be AM29F040 (AMD)  
or W29C020 (WINBOND) or equivalent



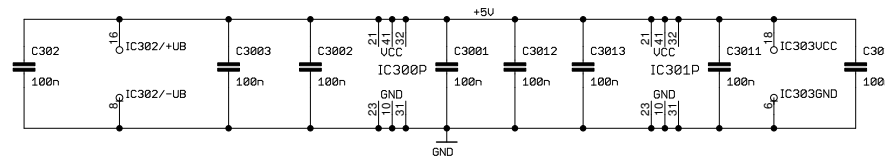
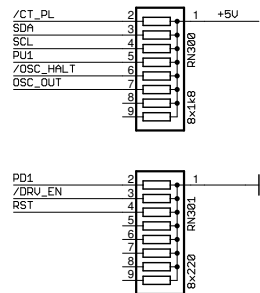
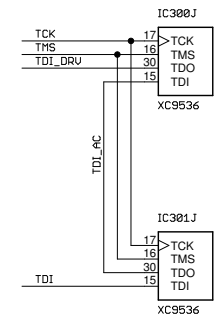
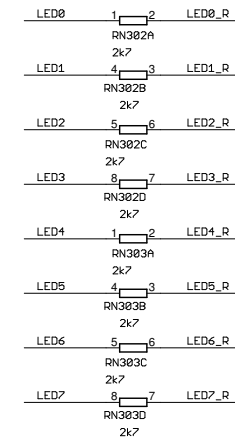
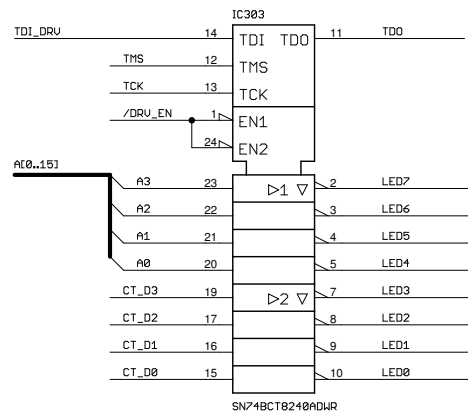
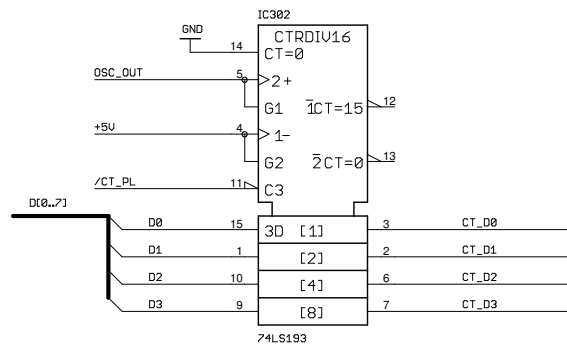
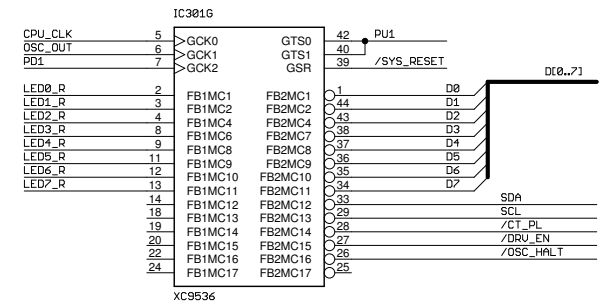
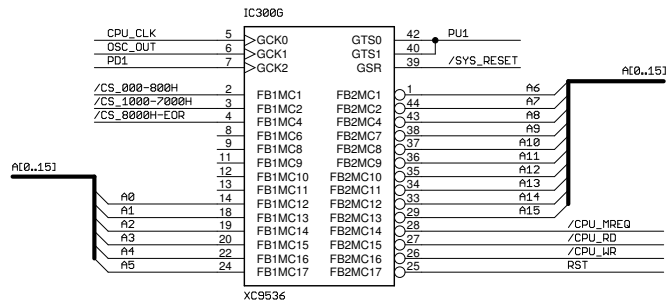
flash conf.  
external write signal at 1-2  
close 5-6 / 7-8 for boot flash bank select  
close 9-10 / 11-12 for user flash bank select  
close 13-14 for boot flash write enabled  
close 14-16 for boot flash write protect  
close 17-18 for user flash write enabled  
close 18-20 for user flash write protect



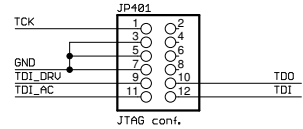
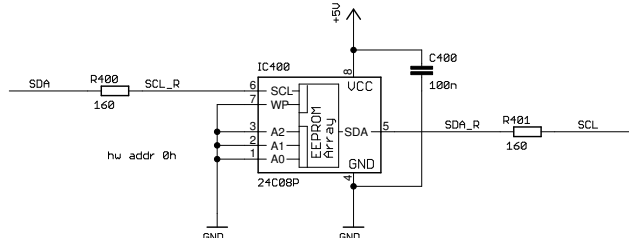
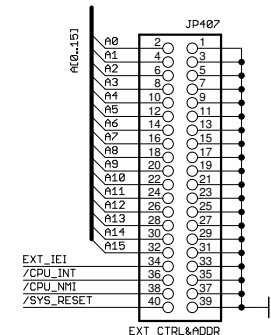
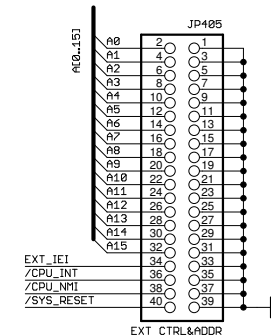
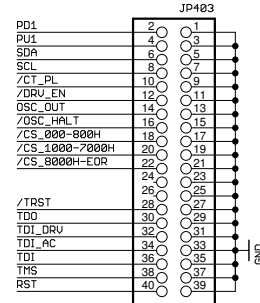
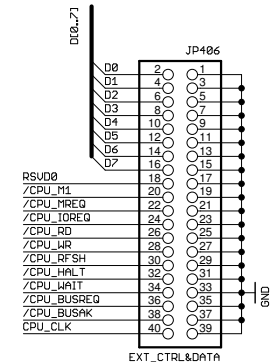
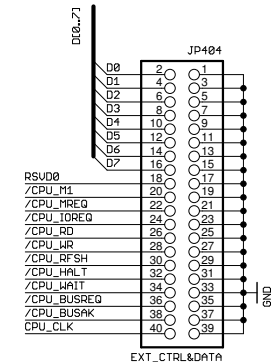
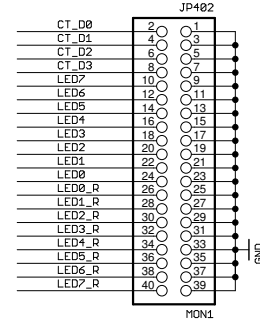
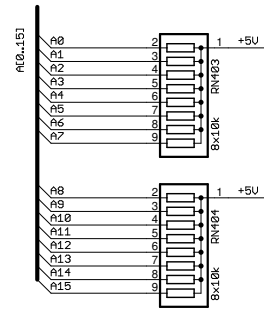
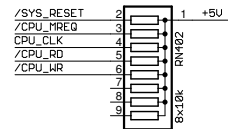
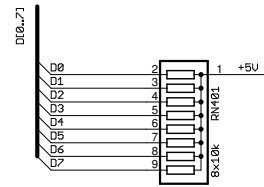
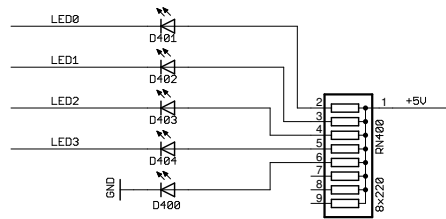
NOTE: IC201 may be AM29F040 (AMD)  
or W29C020 (WINBOND) or equivalent  
Please read manual for supported FLASH-device !



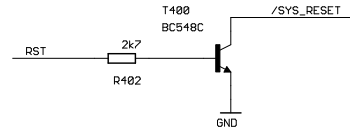
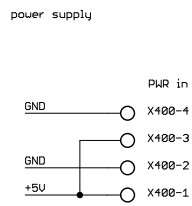
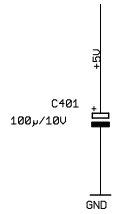
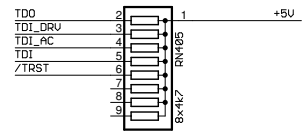
FLASH-EEPROM, static RAM Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
TITLE: MMU_V101r4	
Document Number:	REV: 4
Date: 3/15/13 11:51 AM	Sheet: 2/4



CPLDs, counter, driver, JTAG Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
TITLE: MMU_V101r4	
Document Number:	REV: 4
Date: 3/15/13 11:51 AM	Sheet: 3/4



use pin 1 for TCK monitoring  
close 9-10 / 11-12 for alternative scan chain



power, connectors, LEDs, JTAG Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
TITLE: MMU_V101r4	
Document Number:	REV: 4
Date: 3/15/13 11:51 AM	Sheet: 4/4