J1
From Teensy Board

+13V8	1	2	+13V8	
+3V3	3	4	+3V3	
SCL	5	6	Pin5	
SDA	7	8	Pin6	
Rx1	9	10	Tx1	
Rx2	11	12	Tx2	
AREF	13	14		
A10	15	16	A11	
Pin22	17	18	A14_DAC	
	19	20		

GNDD   GNDD

J2
Level 1 interface

+13V8	1	2	+13V8	
+3V3	3	4	+3V3	
SCL	5	6	Pin5	
SDA	7	8	Pin6	
Rx1	9	10	Tx1	
Rx2	11	12	Tx2	
AREF	13	14		
A10	15	16	A11	
Pin22	17	18	A14_DAC	
	19	20		

GNDD   GNDD

J3
Level 2 interface

+13V8	1	2	+13V8	
+3V3	3	4	+3V3	
SCL	5	6	Pin5	
SDA	7	8	Pin6	
Rx1	9	10	Tx1	
Rx2	11	12	Tx2	
AREF	13	14		
A10	15	16	A11	
Pin22	17	18	A14_DAC	
	19	20		

GNDD   GNDD

I2C pull-ups (if required).

+3V3

R1
R_Small

R2
R_Small

SCL

SDA

Notes:
A14_DAC is used as S meter input for Simple VFO Plus Code.
Pull-ups are present on Si5351 module so no need to install.
Only populate the headers you require, unless you intend to expand the system.
to use +13V8 external supply — fit regulator on Simple VFO Plus processor PCB.
+3V3 output from Teensy, so don't overload this supply line.

Sheet: /
File: Simnple_VFO_Plus_Backplane.sch

Title:

Size: A4   Date:

KiCad E.D.A. kicad (5.1.4-0)

Rev:

Id: 1/1