Time Control	1	14 us			14100 ns			14200 ns			14300 1	DS .		14400 ns				14500 ns	1 1 1	14	1600 ns			1470	O IIS		
clk			1				Г	+		7					1					\neg			$\overline{}$				\exists
rst																											
Input																											
insn[31:0]	34-	+ X 33C0	0000			X 37C6000	10	X 4065200	0			X 11	DC21900	X 45	C32086	X39C000	100				X 000000	000			X 40	0652000	
Registers																											
lr[31:0]	000	000011																									
pc[31:0]	0+	X 00000	013			X 0000001	.0	(0000001	1			X 00	000100	X 00	000101	X 000001	02				X 000001	.03			χ00	000011	
sp[31:0]		000000																									
st[31:0]	000	000000																									
Pins	, _	1400																									
pins0[31:0]				Viron																							
pins1[31:0]	J 98	1		1597				-																			