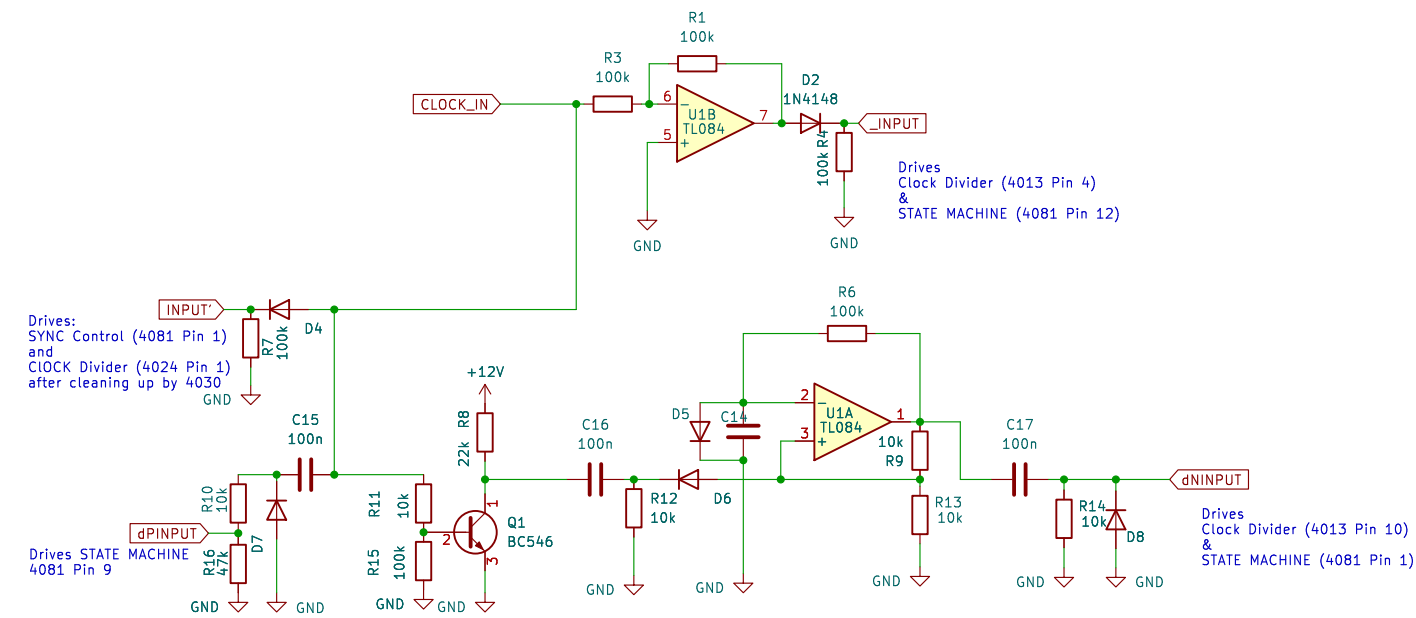
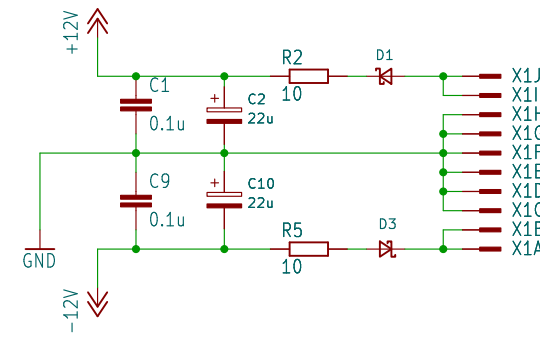


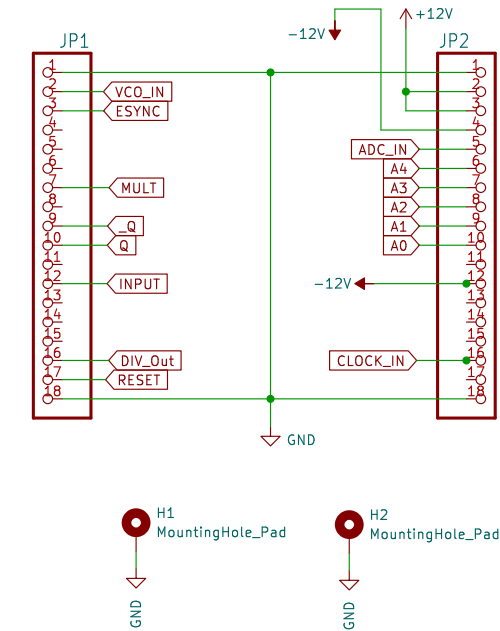
CLOCK PROCESSING



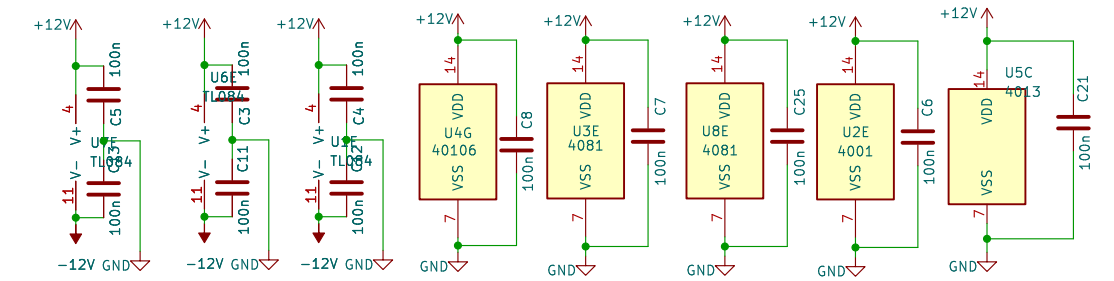
EURORACK POWER



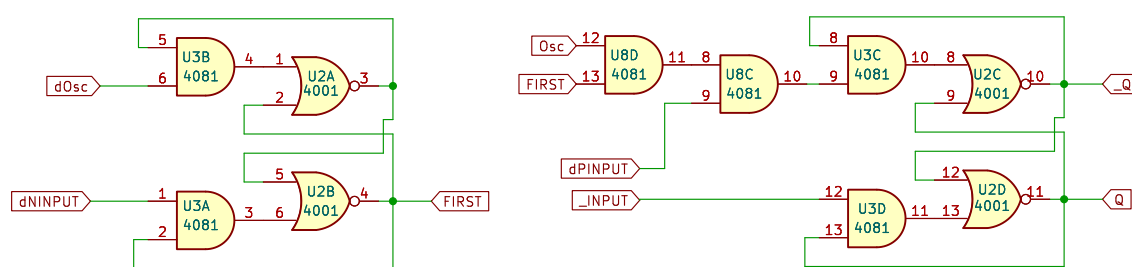
BOARD INTERFACE



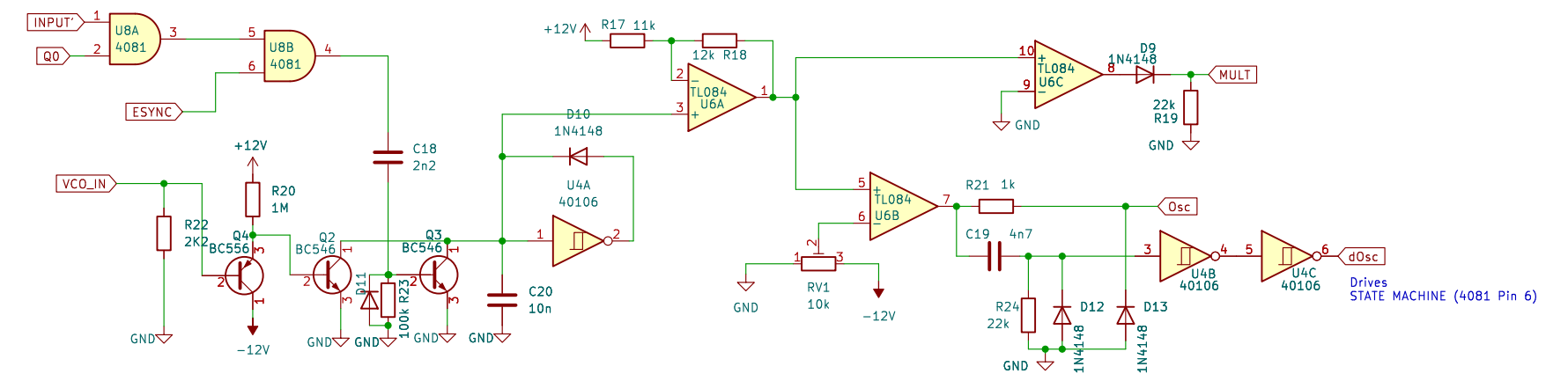
DECOUPLING & POWER DISTRIBUTION



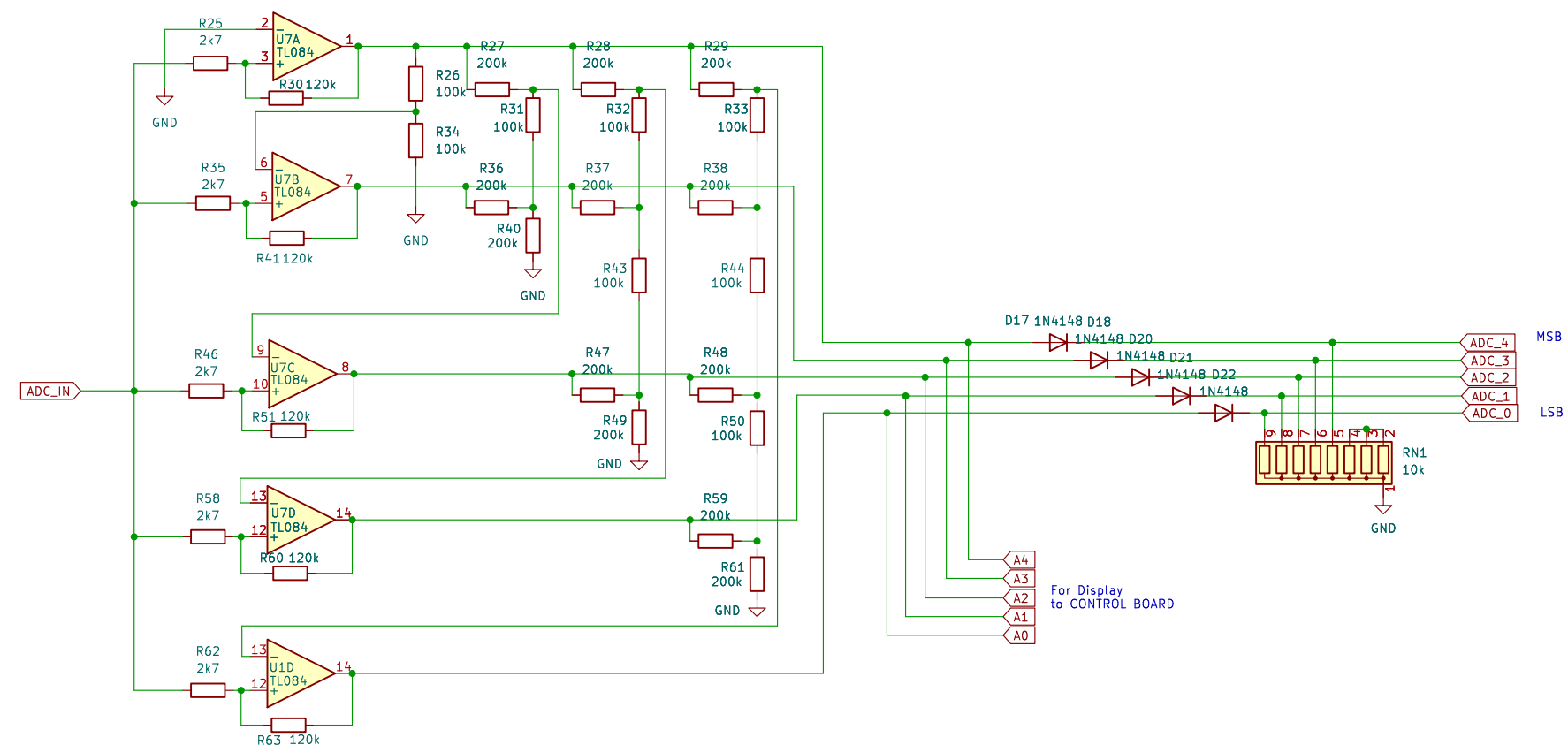
STATE MACHINE



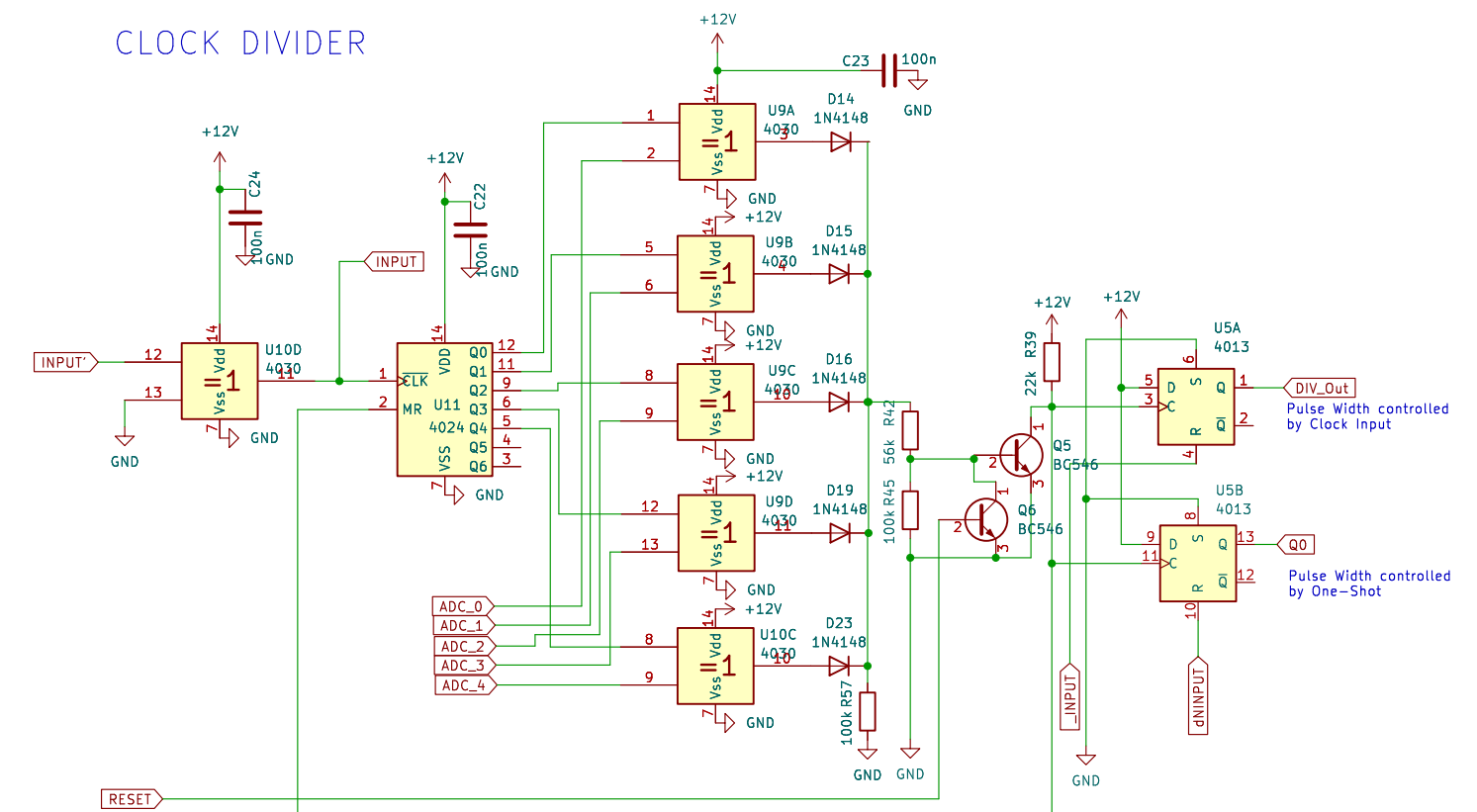
INTERNAL OSCILLATOR



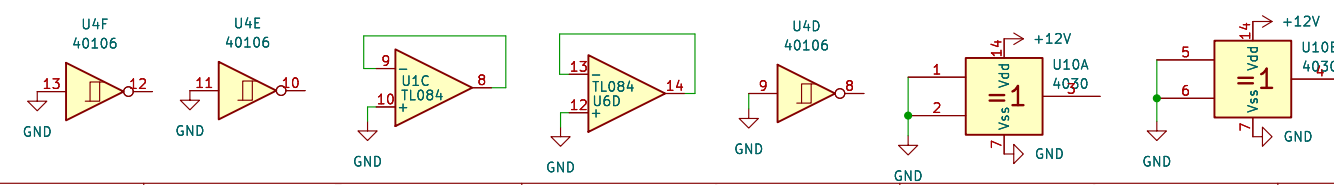
CLOCK DIVIDER A->D Converter



CLOCK DIVIDER



UNUSED GATES & STAGES



See: <https://github.com/m0xpd/EUCLIDplus>
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Sheet: /
File: Euclid+ Main Board.kicad_sch

Title: m0xpd EUCLID+ Main B

Size: A2	Date: 2024-03-26	Rev: 2.0
KiCad E.D.A. kicad 7.0.9		Id: 1/1