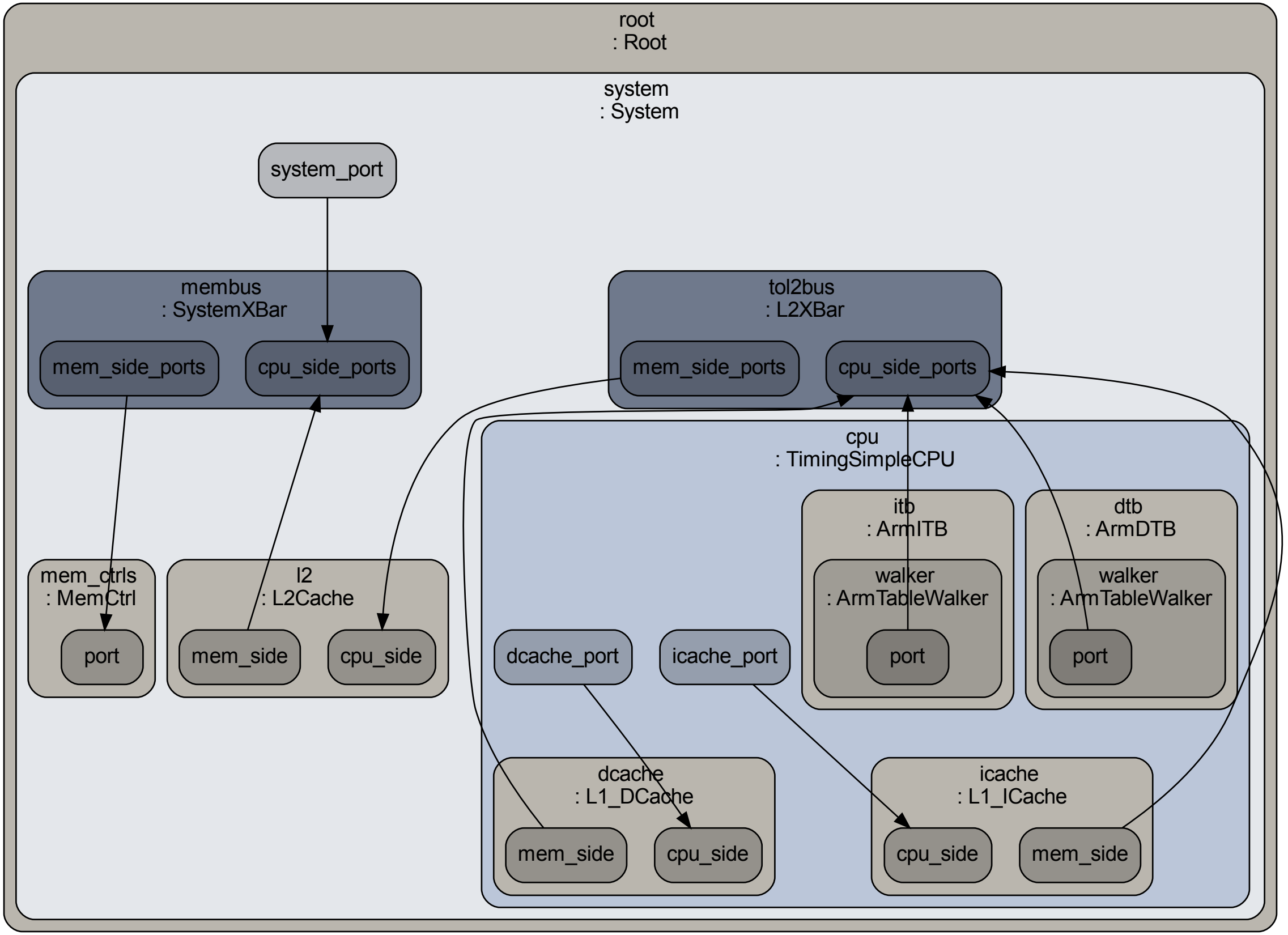
root
: Root
system
: System
system_port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
tol2bus
: L2XBar
mem_side_ports
cpu_side_ports
cpu
: TimingSimpleCPU
itb
: ArmITB
walker
: ArmTableWalker
port
dtb
: ArmDTB
walker
: ArmTableWalker
port
mem_ctrls
: MemCtrl
port
l2
: L2Cache
mem_side
cpu_side
dcache_port
icache_port
dcache
: L1_DCache
mem_side
cpu_side
icache
: L1_ICache
cpu_side
mem_side