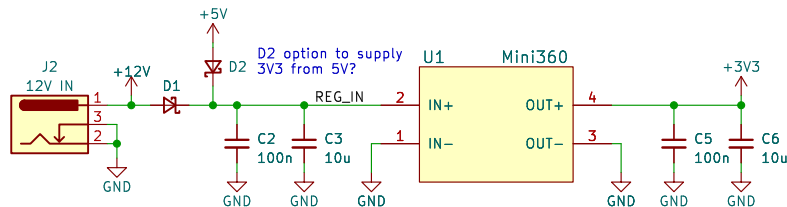
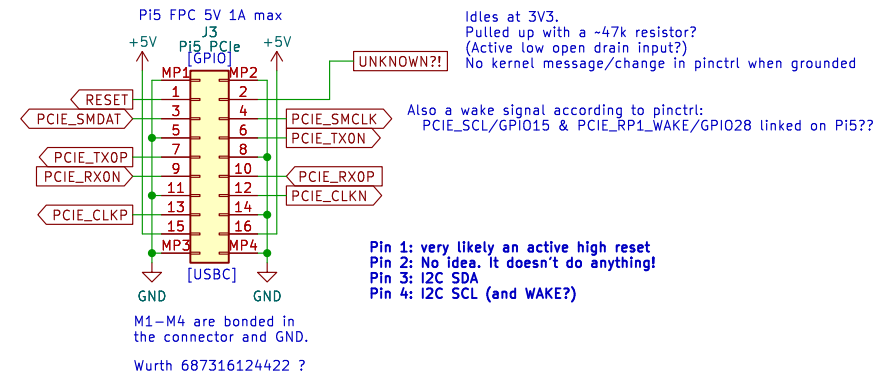
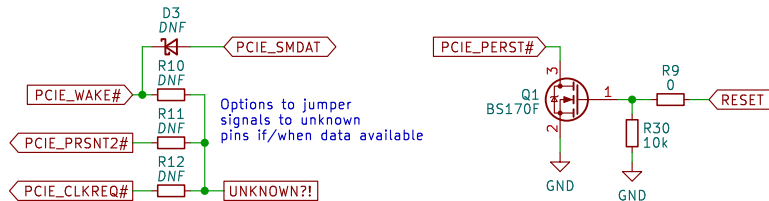


THIS SCHEMATIC IS A REVERSE ENGINEERING WITH GUESSING! IT WORKED WITH 3 PCIe CARDS ON 1 PI5. DO NOT USE FOR ANYTHING SERIOUS!

PCIe EMS R4.0 V1.0, §1.5:

- Reference clock (REFCLK-/REFCLK+), must be supplied by the system (see Section 2.1.1)
- Add-in Card presence detect pins (PRSENT1# and PRSENT2#), required
- PERST#, required
- CLKREQ#, optional
- JTAG, optional
- SMBus, optional
- Wake (WAKE#), required only if the device/system supports wakeup and/or the Optimized Buffer Flush/Fill (OBBF) mechanism
- Power Brake (PWRBRK#), optional
- +3.3 Vaux, optional

See also: https://en.wikipedia.org/wiki/PCI_Express

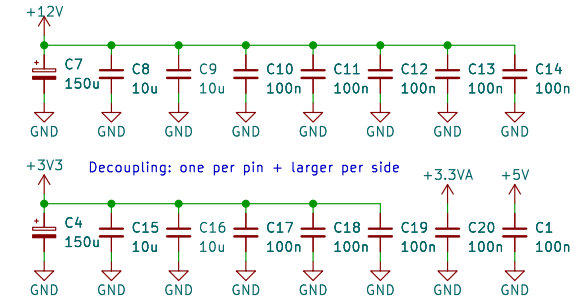
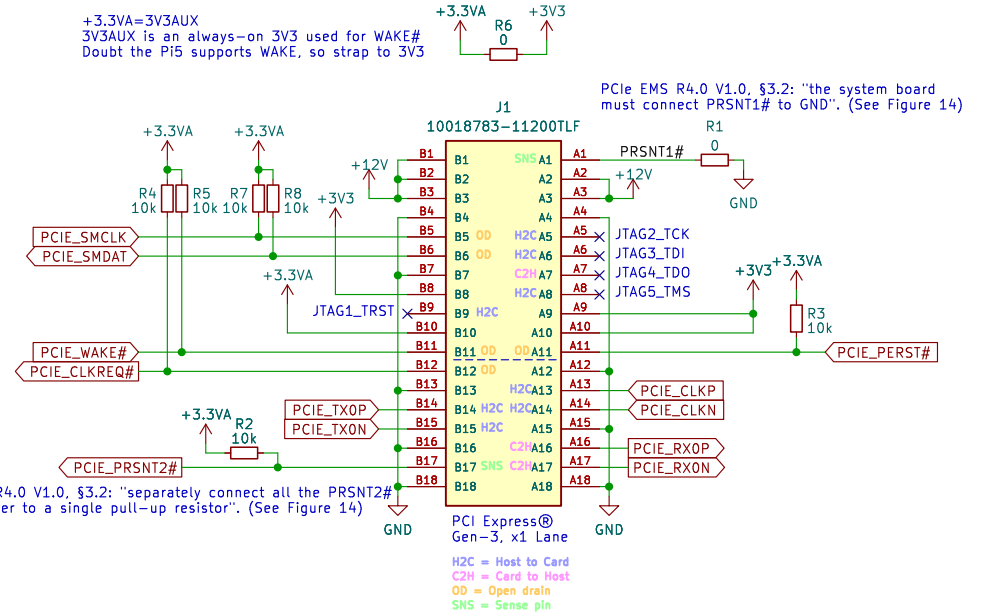


Spec for 75W Power:

+3V3: 3.0A / 1000uF max
+12V0: 5.5A / 2000uF max
+3V3A: 0.4A / 150uF max

Notes:

Watch 12V current on connector
Pi5 only supplies 5V 1A: 5W max



https://github.com/m1geo/Pi5_PcIe

<https://www.george-smart.co.uk>

George M1GEO, Chris G80CV

Sheet: /

File: Pi5_PcIe.kicad_sch

Title: Raspberry Pi 5 PCIe Breakout

Size: A4

Date: 2023-11-09

KiCad E.D.A. kicad 7.0.8-7.0.8-ubuntu22.04.1

Rev: 0.3

Id: 1/1