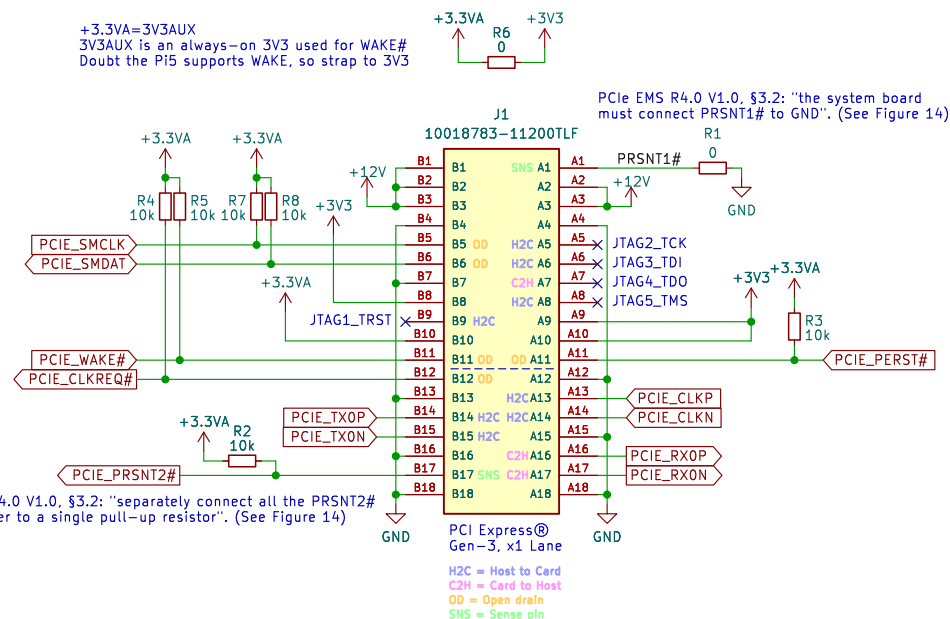


PCIe EMS R4.0 V1.0, §1.5:

- Reference clock (REFCLK-/REFCLK+), must be supplied by the system (see Section 2.1.1)
- Add-in Card presence detect pins (PRST1# and PRST2#), required
- PERST#, required
- CLKREQ#, optional
- JTAG, optional
- SMBus, optional
- Wake (WAKE#), required only if the device/system supports wakeup and/or the Optimized Buffer Flush/Fill (OBFF) mechanism
- Power Brake (PWRBRK#), optional
- +3.3 Vaux, optional

See: https://en.wikipedia.org/wiki/PCI_Express

+3.3VA=3V3AUX
3V3AUX is an always-on 3V3 used for WAKE#
Doubt the Pi5 supports WAKE, so strap to 3V3

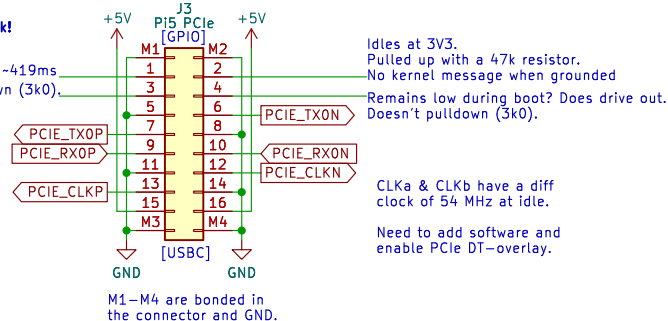


Connector on Pi5: (maybe?) Wurth 687316124422

Pi5 FPC 5V 1A max

Pins 1-4 are sidebands signals are a complete guess.
If the card is ready and powered at Pi5 boot, it'll likely work!

Drives (into 220R, so not pullup?) to 3V3 during reboot for ~419ms
Remains low during boot? Does drive out. Doesn't pulldown (3k0).

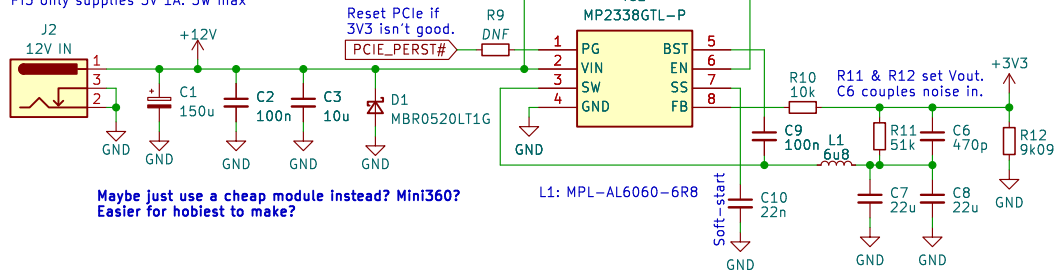


Power (75W req.)

+3V3: 3.0A / 1000uF max
+12V0: 5.5A / 2000uF max
+3V3A: 0.4A / 150uF max

Watch 12V current on connector

Pi5 only supplies 5V 1A: 5W max



Maybe just use a cheap module instead? Mini360?
Easier for hobiest to make?

THIS SCHEMATIC IS A REVERSE ENGINEERING WITH
GUESSING! IT WORKED WITH 3 PCIe CARDS ON 1 Pi5.
DO NOT USE FOR ANYTHING SERIOUS!

https://github.com/m1geo/Pi5_PcIe
<https://www.george-smart.co.uk>

George M1GEO, Chris G8OCV

Sheet: /
File: Pi5_PcIe.kicad_sch

Title: Raspberry Pi 5 PCIe Breakout

Size: A4 Date: 2023-11-05

KiCad E.D.A. kicad (7.0.0)

Rev: 0.01A

Id: 1/1