

Congestion Prediction in Integrated Circuit Design Using Graph Neural Network

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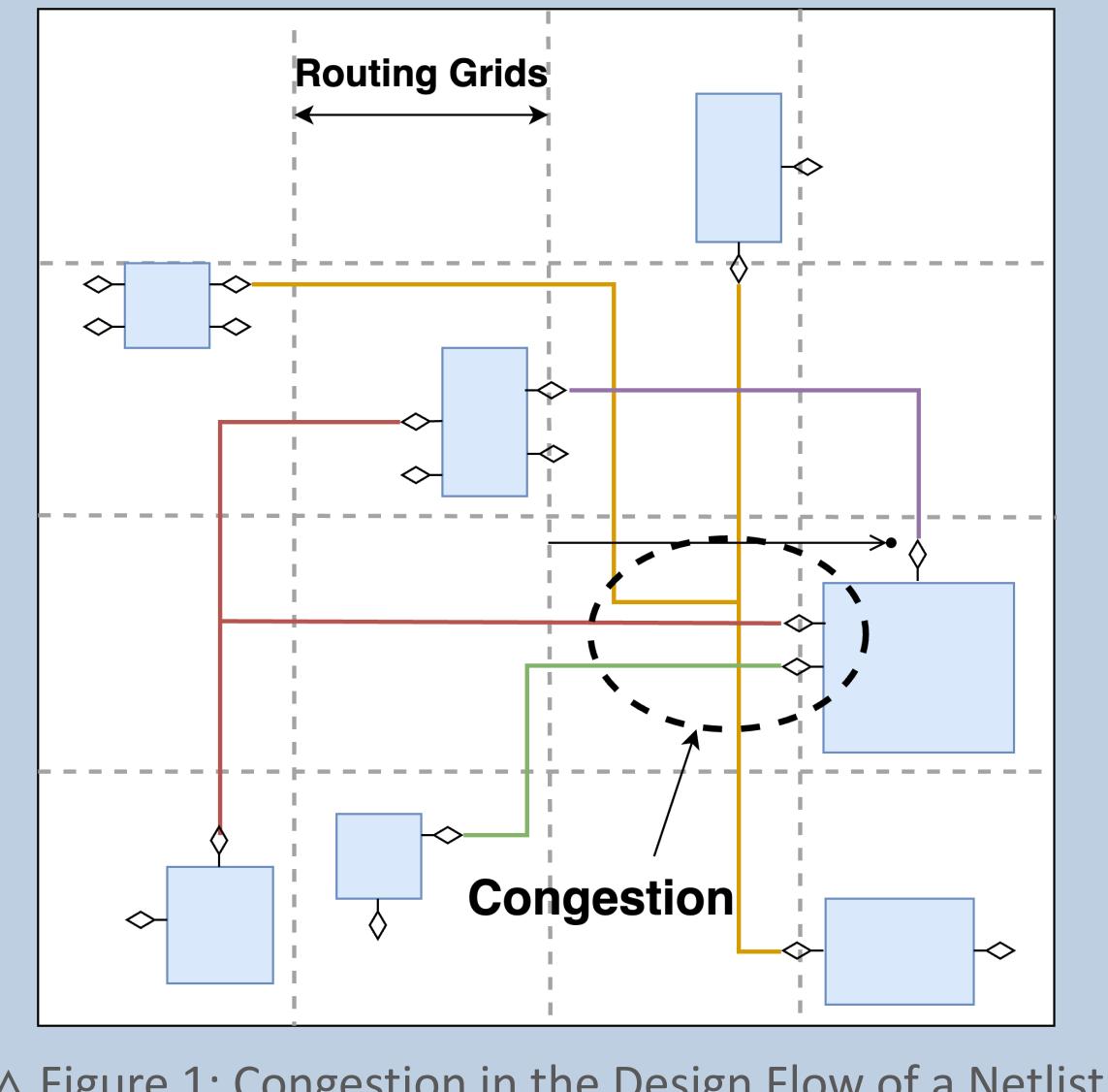


Project Website

Introduction

Chip design is crucial for technological advancement, for it directly influences the performance of electronic devices.

A key component of chip design is the **netlist**, which can be seen as a giant graph that connects different logic functions to each other, guiding the assembly of circuits. However, as more logic functions are added, the **demand** for more connections exceeds the **capacity** available, which leads to a major challenge in chip design called **congestion**, and this can impede chip manufacturing.

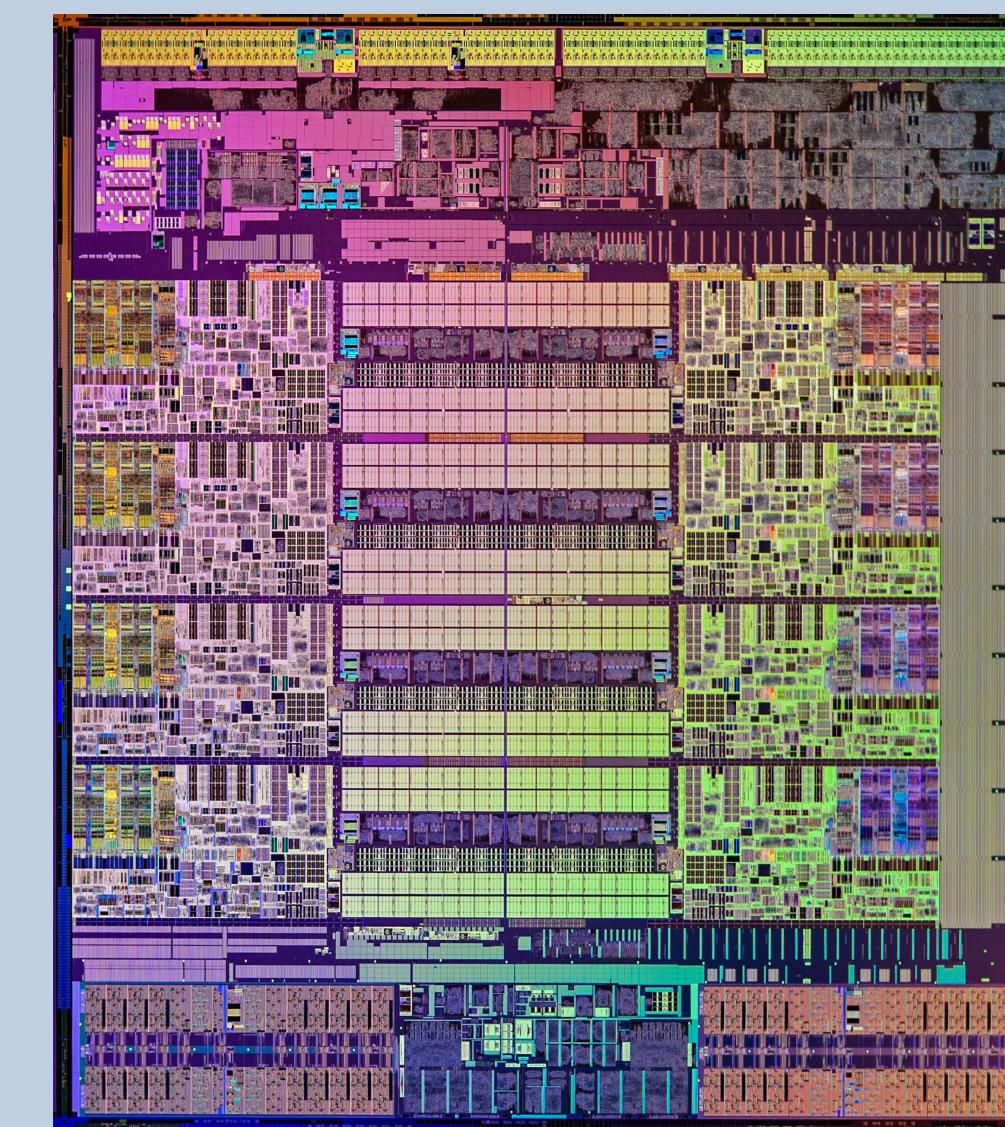


△ Figure 1: Congestion in the Design Flow of a Netlist

Motivation

This project aims to **predict congestion** in Integrated Circuit (IC) Design using **Graph Neural Networks** (GNNs).

GNNs can model the intricate, non-Euclidean relationships between circuit components represented as graphs. This allows for effective learning and forecasting of congestion by capturing the complex dependencies inherent in IC layouts.



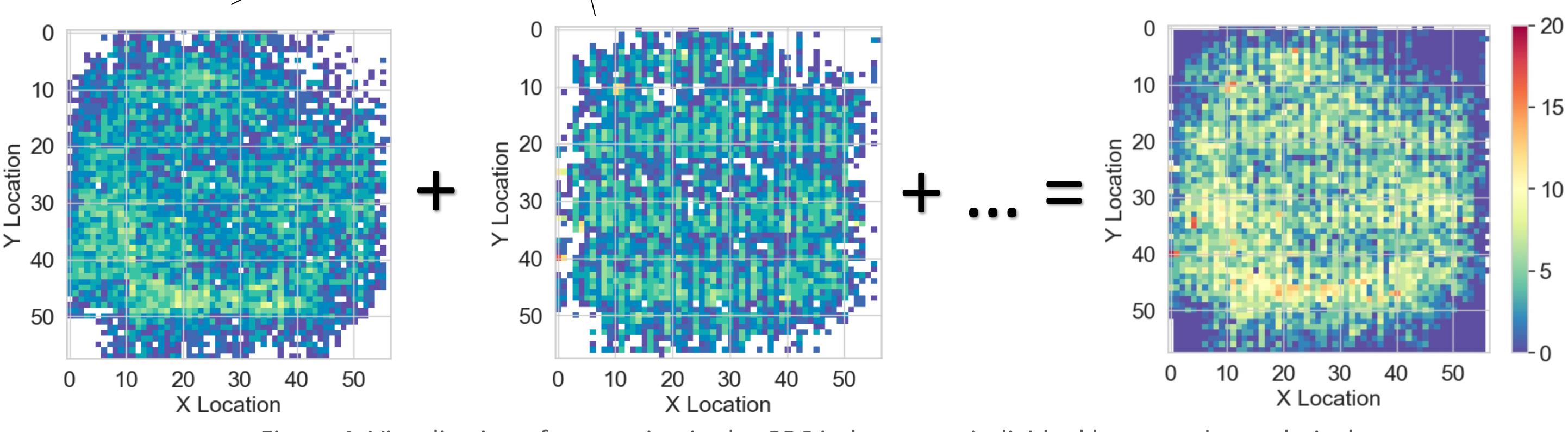
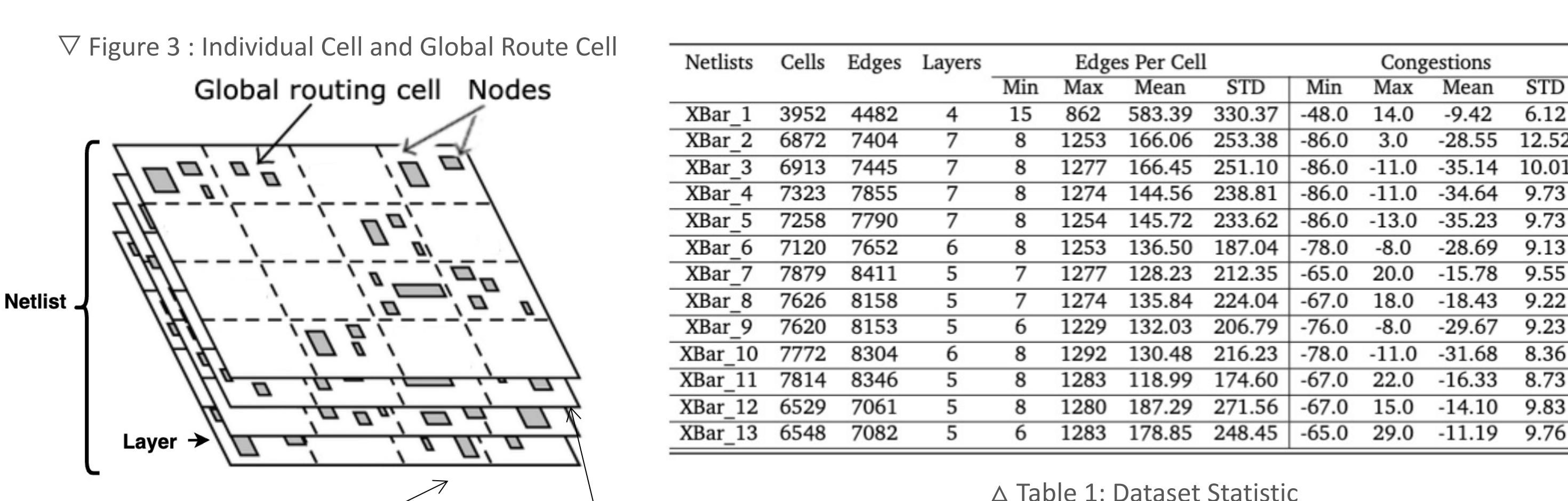
△ Figure 2: Complexity of Modern chips

Dataset

We utilized the NCSU-DigIC-GraphData, which comprises 13 netlists, each exhibiting unique **placement** and **congestion** characteristics.

For each netlist, the dataset contains **node features**, **node connectivity**, and Global Route Cell (GRC) based **congestion** (demand minus capacity).

Node features and node connectivity refer to the characteristics and interconnections of individual elements within the netlist. GRC-based congestion calculates congestion at a GRC index with multiple nodes.



△ Figure 4: Visualization of congestion in the GRC index across individual layers and cumulatively.

Methodology

Features

- **xloc** and **yloc**, **width** and **height**: Represent the location and size of the nodes and can be used to discern congestion and elemental spatial occurrence.
- **orient**: Might indicate the preferred direction of routing, potentially revealing congestion-prone areas.
- **edge index**: Information obtained from the adjacency matrix. Helps the model learn relationships and pathways within the graph structure.

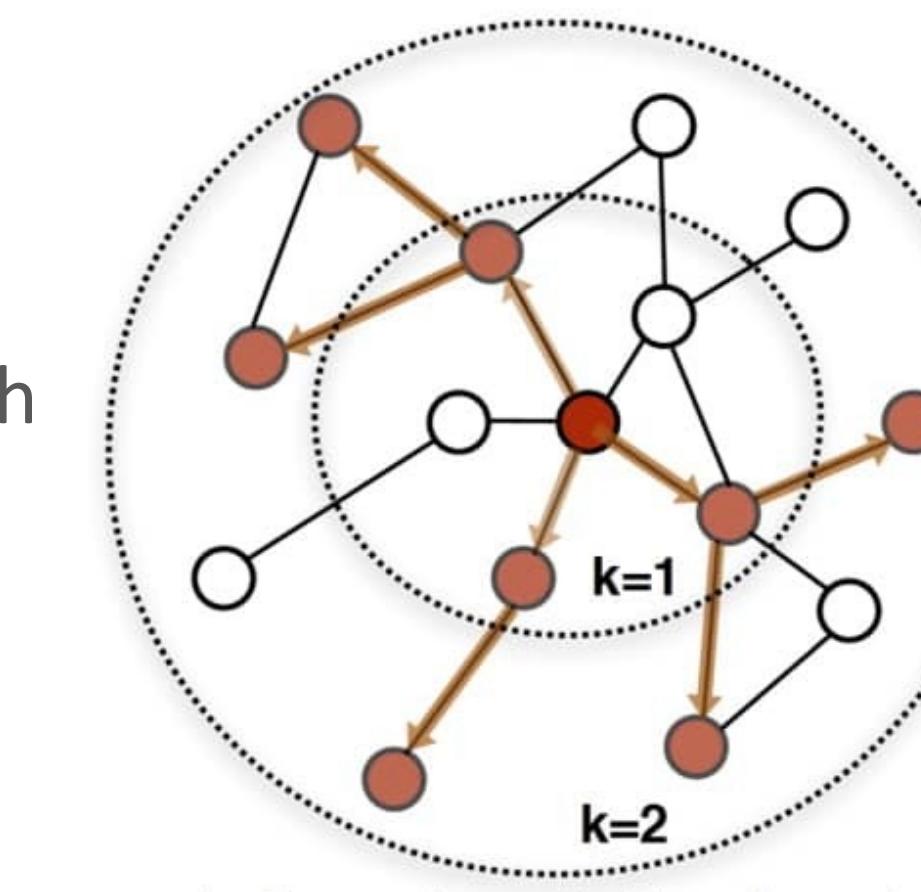
Positional data and target congestion is normalized so that the models generalize across designs with differing dimensions

xloc	yloc	width	height	orient	congestion
0	41984	44544	2176	1536	0
649	62336	72192	896	1536	6

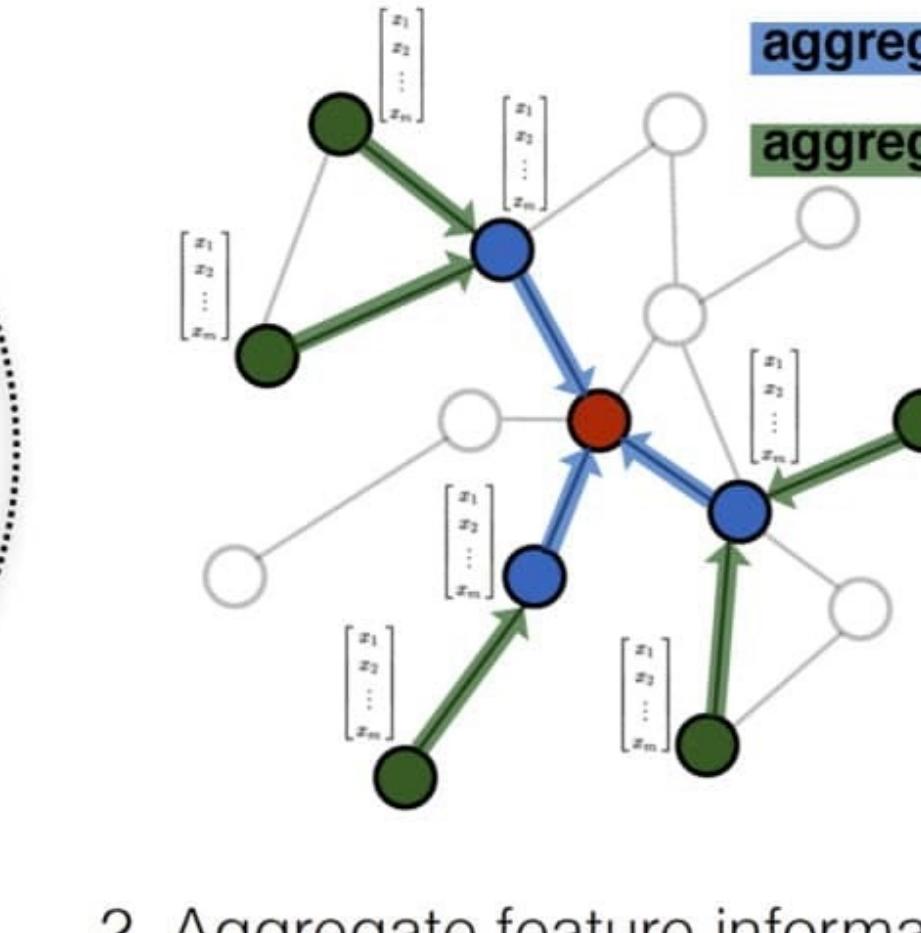
△ Table 2: Node Features

Model Selection

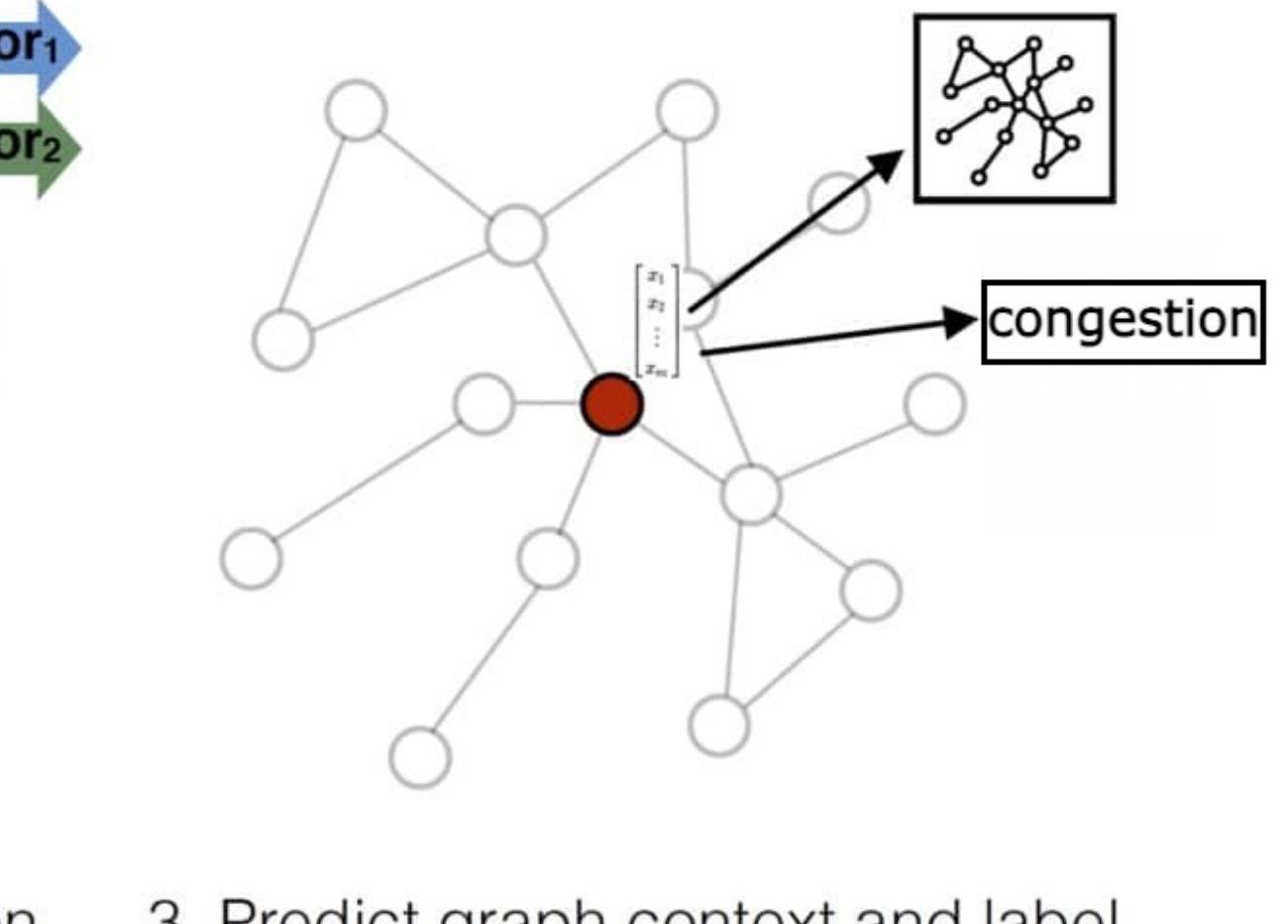
We use GNNs to predict congestion, given that netlists can be represented as graphs. Each node represents a cell, and an edge exists when two cells are connected by a route.



1. Sample neighborhood



2. Aggregate feature information from neighbors



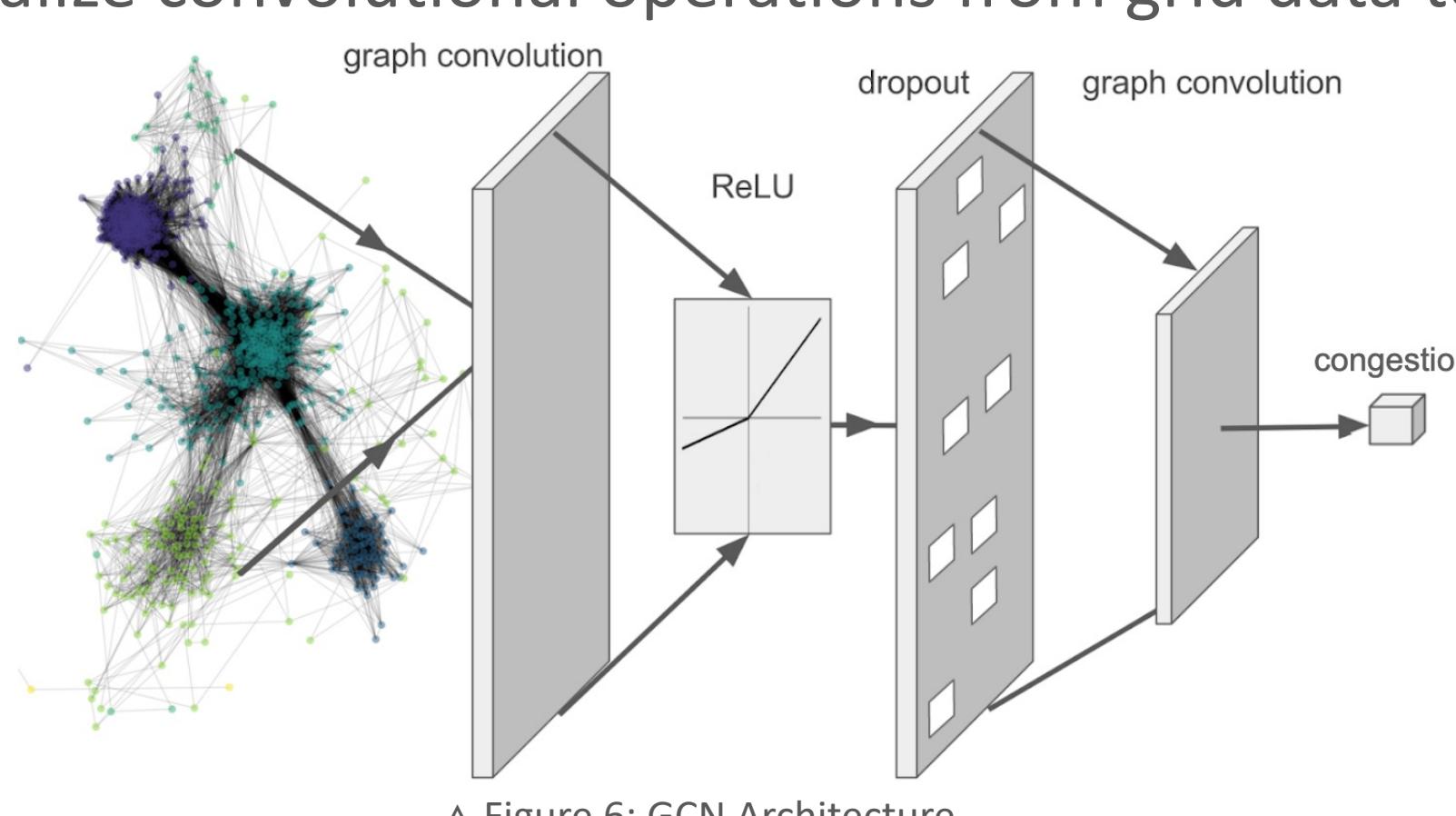
△ Figure 5: Graph Neural Network Flowchart

Graph Neural Networks

Model Architectures

Graph Convolutional Networks (GCN):

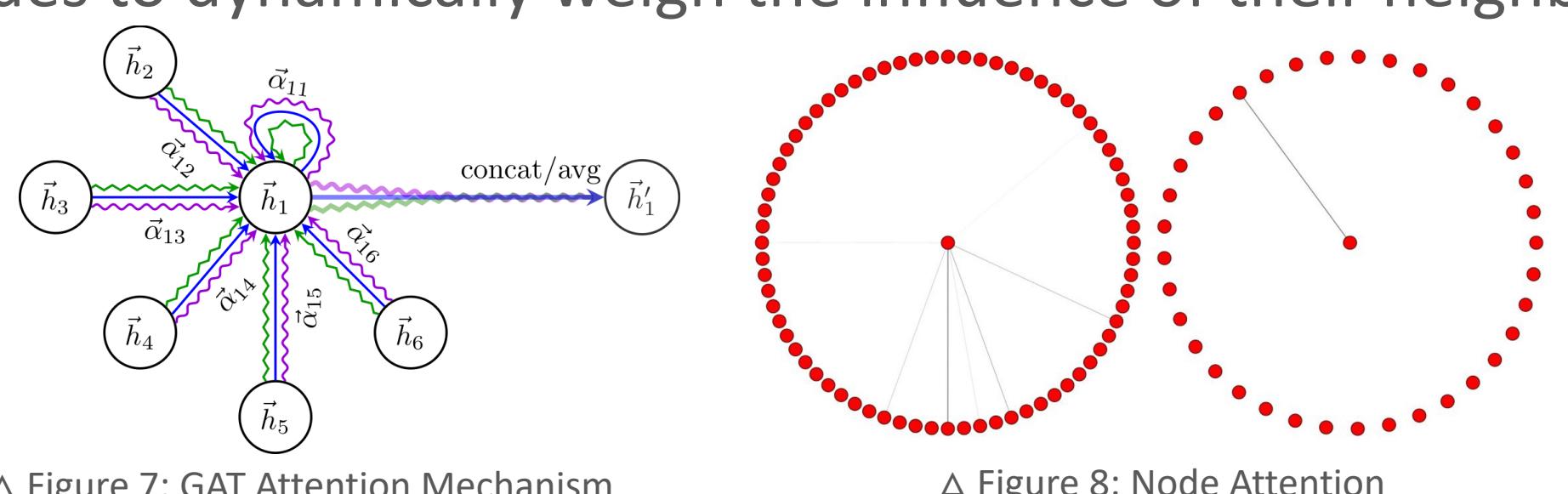
- Generalize convolutional operations from grid data to graph data



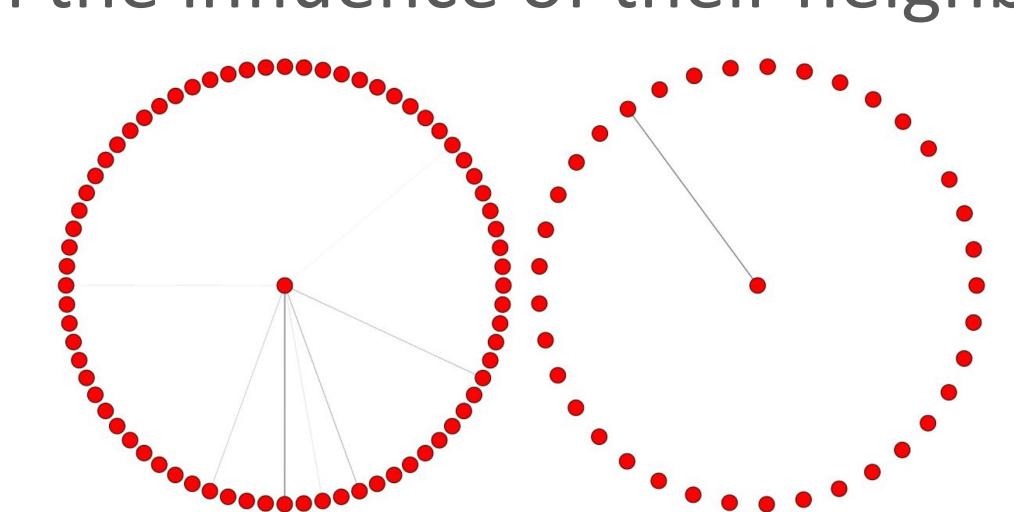
△ Figure 6: GCN Architecture

Graph Attention Networks (GAT):

- Enhance GCN by incorporating attention mechanisms, allowing nodes to dynamically weigh the influence of their neighbors



△ Figure 7: GAT Attention Mechanism



△ Figure 8: Node Attention

Model Training

Data balancing

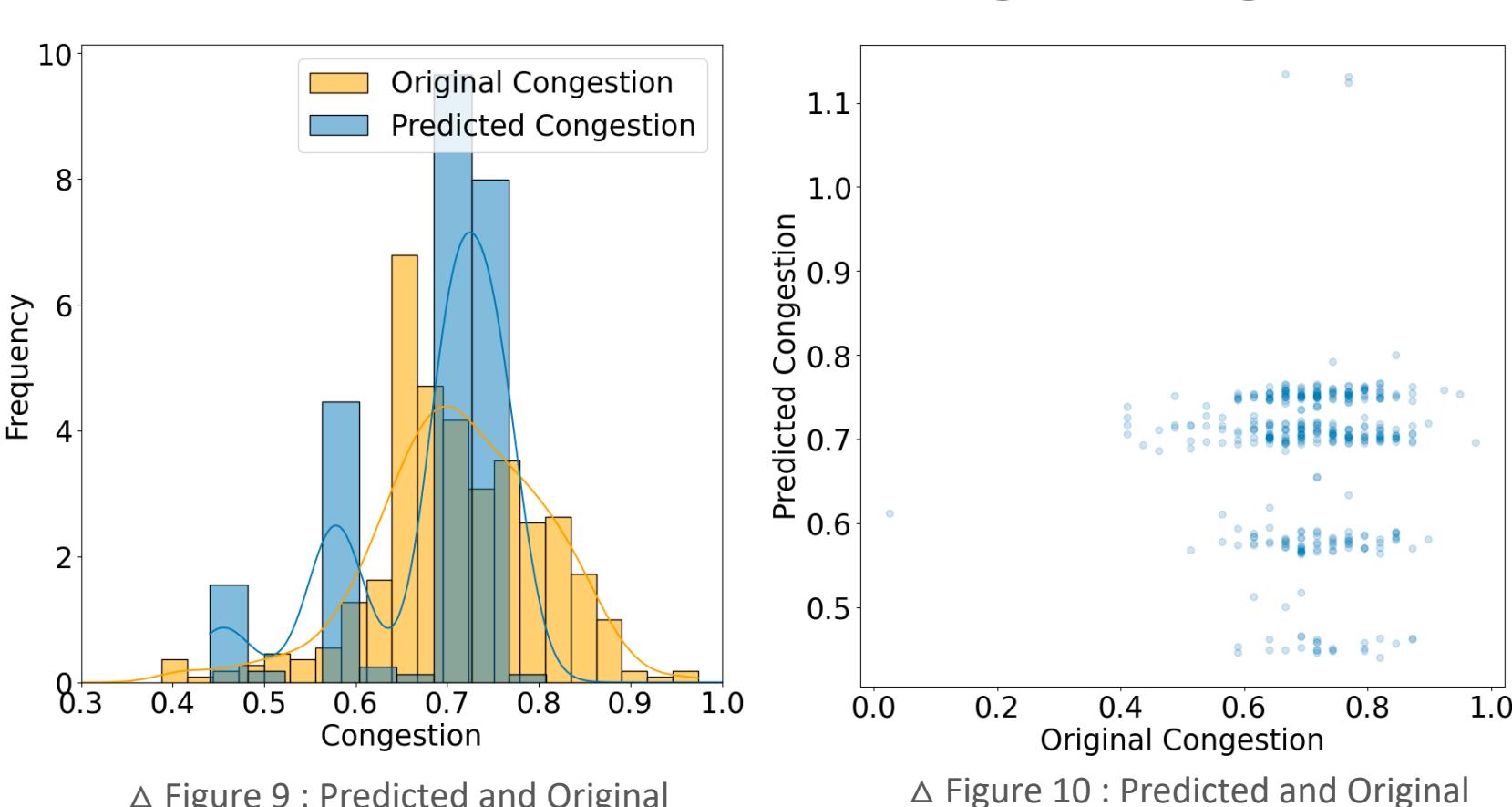
- To solve the lack of variance of predicted congestion, we implemented a **weighted loss criterion**, where our targets are split into bins and their weight is the inverse frequency of their respective bin.

Train Test Split

- **Training within a single design**. We randomly pick 80%/10%/10% of nodes within one netlist to serve as our training, validation and testing set.

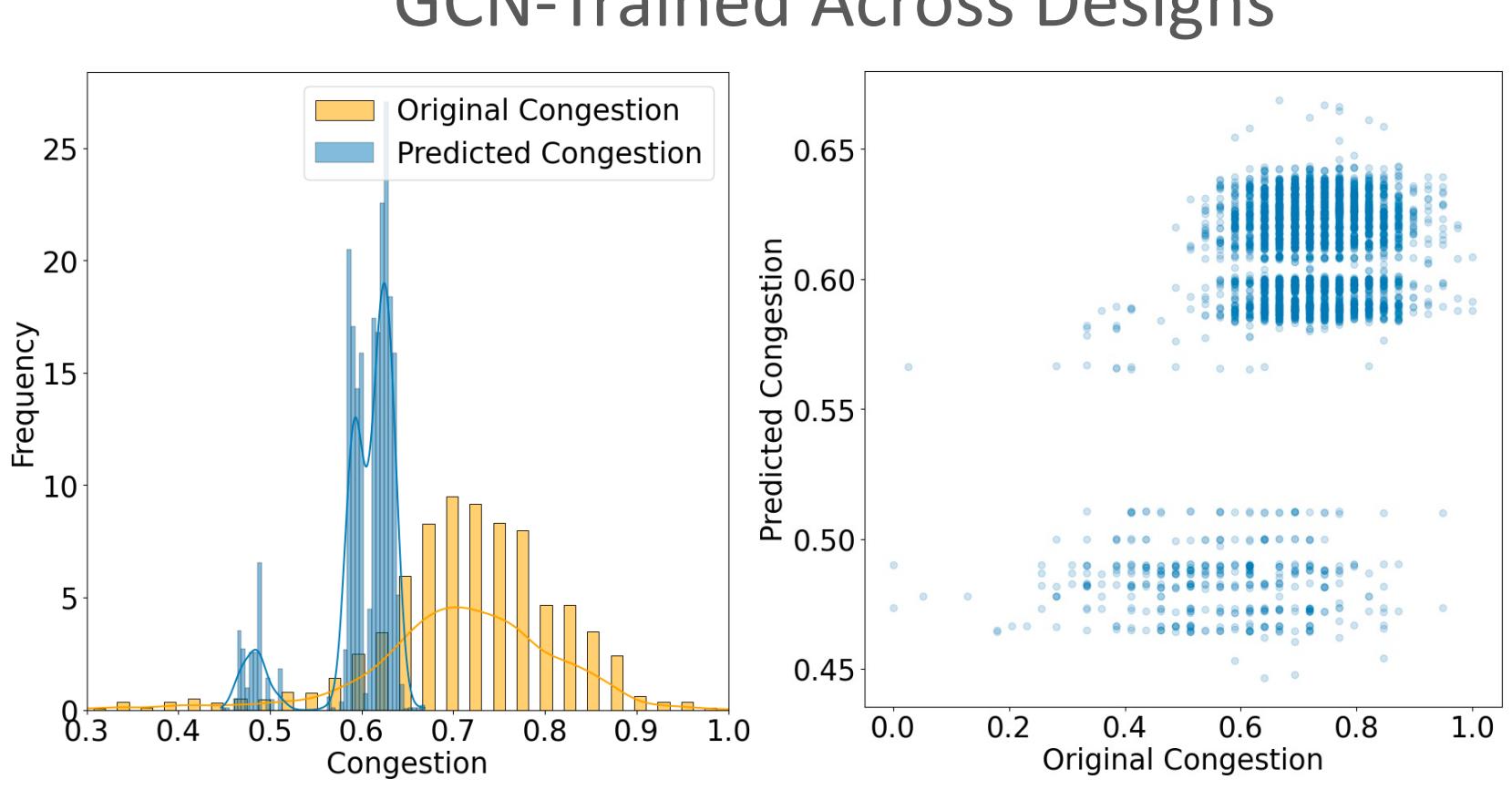
- **Training across designs**. We leave one netlist out and utilize the other 12 netlists as our training set. This cross-files' training method broadens the scope of our model's learning to a wider array of data variations, which enhance its generalization capabilities.

GCN-Trained on a Single Design



△ Figure 9: Predicted and Original Congestion Histogram

GCN-Trained Across Designs



△ Figure 11: Predicted and Original Congestion Histogram

Insights

Based on the results in table 3, we found that GNNs tend to predict mean values for the congestion in the NCSU datasets, which are relatively smaller by the netlist sizes. Even though we conducted hyperparameter tuning on the models to experiment

with different numbers of layers, numbers of hidden channels, and so on, we failed to improve the models' performance significantly better than a naïve mean predictor.

Therefore, we believe GNNs in general may require more training data to understand the connectivity of a graph.

Moreover, we noticed that GATs tend to perform better than GCNs, even though GATs take longer time to train.

Model	Single-Design		Cross-Design	
	RSME	Pearson	RSME	Pearson
Cell-Based	0.140	-0.015	0.144	0.445
GAT	0.010	-0.128	0.151	0.463

△ Table 3: Model Performance

Future Work

Given our suspicion on the scaling issue of GNNs, we want to test our model on the larger netlists from the SuperBlue datasets, which contains hundreds of thousands of nodes.

Furthermore, we are interested in trying other ML models like XGBoost and random forest regressor for predicting congestion.

More node features, such as the layer information of nodes and routing information, could be collected and included in the model..