



**Department of Electrical
& Computer Engineering**
Faculty of Engineering & Architectural Science

Course Title:	Software Design Architecture
Course Number:	COE692
Semester/Year:	W2022

Instructor:	Dr. Faezeh Ensan
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Assignment/Lab Number:	Lab 5
Assignment/Lab Title:	Lab 5 Part 3

Submission Date:	TBD
Due Date:	TBD

Last Name:	First Name:	Student ID:	Section:	*Signature:
Faisal	Mahir	500896206	01	MF
Patel	Dhaval	500901575	01	DP

*By signing above you attest that you have contributed to this written lab report and confirm that all work you have contributed to this lab report is your own work. Any suspicion of copying or plagiarism in this work will result in an investigation of Academic Misconduct and may result in a "0" on the work, an "F" in the course, or possibly more severe penalties, as well as a Disciplinary Notice on your academic record under the Student Code of Academic Conduct, which can be found online at: <http://www.ryerson.ca/senate/current/pol60.pdf>

COE692 Lab 5 Part 3

NAME	READY	STATUS	RESTARTS	AGE
additem-64478f54c7-2626j	1/1	Running	0	35m
additemdb-59bcf76669-hq1gm	1/1	Running	0	35m
frontend-67b54449b-wql9d	1/1	Running	0	35m
searchdb-bdbf7577-2dssd	1/1	Running	0	35m
searchitem-6d564b45bf-2stb8	1/1	Running	0	35m

NAME	TYPE	CLUSTER-IP	EXTERNAL-IP	PORT(S)	AGE
frontend	LoadBalancer	10.60.12.231	34.95.31.249	80:31434/TCP	36m

34.95.31.249/frontend/

34.95.31.249/frontend/

Login into the system to be able to purchase items.

Username

Password

Login

Search Items

Search item name

Submit Query

