

```
4  parameter M = 8;
5
6  wire [(N-1):0] output_arguments_tb;
7  wire start_target_current_state_machine_tb;
8
9  wire finish_a_tb;
10 wire finish_b_tb;
11 wire reset_start_request_a_tb;
12 wire reset_start_request_b_tb;
13 wire [(M-1):0] received_data_a_tb;
14 wire [(M-1):0] received_data_b_tb;
15
16 reg sm_clk_tb;
```