

M5Stack Module 4-20mA I2C Protocol																	V1 (FW Version)	
																	2023/1/30	
REG MAP (Addr:0x55)		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	note
Raw ADC (12Bits)	0x00 R	ADC1-L	ADC1-H															
Raw ADC (8Bits)	0x10 R	ADC1																
Current	0x20 R	Current 1-L	Current 1-H															
Firmware Version	0xF0 R															Version		Version: firmware version number
I2C Address	0xF0 R																Address	Address: I2C Address