M5Stack Module HMI I2C Protocol

REG MAP (Addr:0x41)		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	note
Counter Value (accumulate)	0x00 R/W	Cnt-byte0	Cnt-byte1	Cnt-byte2	Cnt-byte3													Cnt: -2147483648-2147483647 (will be reset after set reg 0x40)
Increment Value	0x10 R	Inc-byte0	Inc-byte1	Inc-byte2	Inc-byte3													Cnt: -2147483648-2147483647 (will be reset after get)
Button Value	0x20 R	Btn-S	Btn-1	Btn-2														BNT: 0~1
LED	0x30 R/W	LED-1	LED-2															LED: 1, LED on; 0, LED off
Counter Reset	0x40 W	Cnt-RST																RST: write 1 to reset counter
Firmware Version	0xF0 R															Version		Version: firmware version number
I2C Address	0xF0 R/W																Address	Address: 1~127