

INSTITUTE OF INFORMATION TECHNOLOGY JAHANGIRNAGAR UNIVERSITY

Assignment : 02

Submission Date : 05/12/2021

Course Tittle : Computer Architecture

Course Code : ICT - 2207

Submitted To

Md Biplob Hosen

Lecture

IIT-JU

Submitted By

Md. Shakil Hossain

Roll - 2023

2nd year 2st Semester

IIT - JU

Answer to the question no -1

Given that,

Ü FiFo

	2	3	4	2_	į	3	7	5	·		
FI	2	2	2	2	1	1	1	1,	1 /	1	1.
F2		3	3	2	0	-	<u>'</u>	<u>-</u> _	9	4	-
F3				-		3	+	7	7	3	
13			4	4	4	4	4	5	5	5	
				Hit		Hit					

Hit = 2Mis = 8

: Hit ratio =
$$\frac{\text{Hit}}{\text{Hit+Mis}} \times 100$$
$$= \frac{2}{2+8} \times 100$$
$$= 20\% \text{ Am}.$$

2	3	ч	2	1	3	7	5	4	2
2	2	2	2	2	2	7	7	7	2
	3	3	3	1	1	,	5	5	5
		4	4	4	3	3	3	4	4

: Hit natio =
$$\frac{\text{Hit}}{\text{Hit+Mis}} \times 100$$
 | hene.
= $\frac{1}{1+9} \times 100$ | Mis = 9
= 10% Ans.

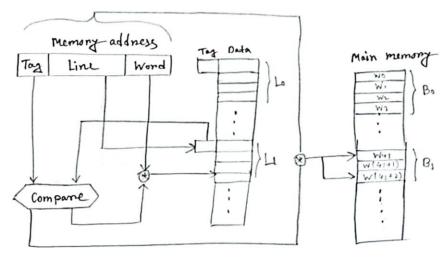
(iii) LFU

1-	2	3	4	2	1	3	7	5	u	2
,	2	2	2	2	2	2	2	2	2	12
2		3	3	3	1	1	7	7	4	4
			4	4	4	3	3	5	5	7

: Hit ratio =
$$\frac{\text{Hit}}{\text{Hit} + \text{Mis}} \times 100$$
 | here $\frac{1}{\text{Hit}} = 1$ | $\frac{1}{1+9} \times 100$ | $\frac{1}{1+9} \times 1000$ | $\frac{$

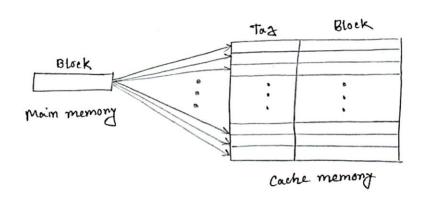
Answer to the question no-2

(i) Direct Mapping: Direct mapping is a Procedure used to assign each memory block in the main memory to a Particular line in the cache. If a line is already filled with a memory block and a new block needs to be loaded then the old block is discarded from the cache.



Dinect mapping divides an address into three Pants
Tag, line, word. The word are the least
Significant bits that identify the specific word
within a block of memory.

(ii) Associative Mapping. In the associative mapping function any block of main memony can Probably Consist of any cache block Position. It breaks the main memony address into two Pant. the word ID and a tag as shown in the figure. To check for a block Stoned in the memony the tag is Pulled from the memory address the tag is Pulled from the memory address and a search is Penformed through all of the lines of the cache to see if the block is Present.



This method of searching for a block mithin a cache appears like it might be a slow Process but it is not. Each line of the cache has its compane circuitry which can quickly analyze whether or not the block is contained at that line.

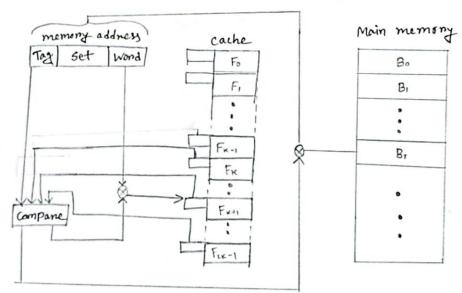
With all of the lines Pertonning this companison Process in Parallel the connect line is identified quickly.

This mapping technique is designed to solve a Problem that exists with direct mapping where to active blocks of main memory could map to the same line of the cache. When this happens neither block of memory is allowed to stay in the cache as it is neplaced quickly by the competing block. This leads to a condition that is neferned to as thrashing

In throashing a line in the cache goes back and forth between two or more blocks usually and forth between two or more blocks usually neplacing a block even before the Processor goes through it. Thrashing can be avoided by allowing a block of memory to map to any line of the cache.

(iii) Set Associative Mapping. Set associative mapping combines direct mapping with fully associative by arrangement lines of a cache into sets.

The Sets are Pensistent using a direct mapping scheme. However the lines within each Set scheme theated as a small fully associative cache are treated as a small fully associative cache where any block that can save in the set where any block that can save in the set. Can be stoned to any line inside the set.



A set-associative cache that includes k lines Pen set is known as a k way set associative cache. Because the mapping approach uses the memory address only like direct mapping does the number of lines include in a set should be similar to an integer Power of two.

THE END