



***INSTITUTE OF INFORMATION TECHNOLOGY***  
***JAHANGIRNAGAR UNIVERSITY***

**Number of Assignment : 02**

**Name of Assignment : Designing Combinational Logic Circuits.**

**Course Title : Digital Logic Design**

**Course Code : ICT – 2103**

**Submission Date : 5/11/2020**

**Submitted To**

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Roll – 2023

2<sup>nd</sup> year 1<sup>st</sup> Semester

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Date-05-11-2020

