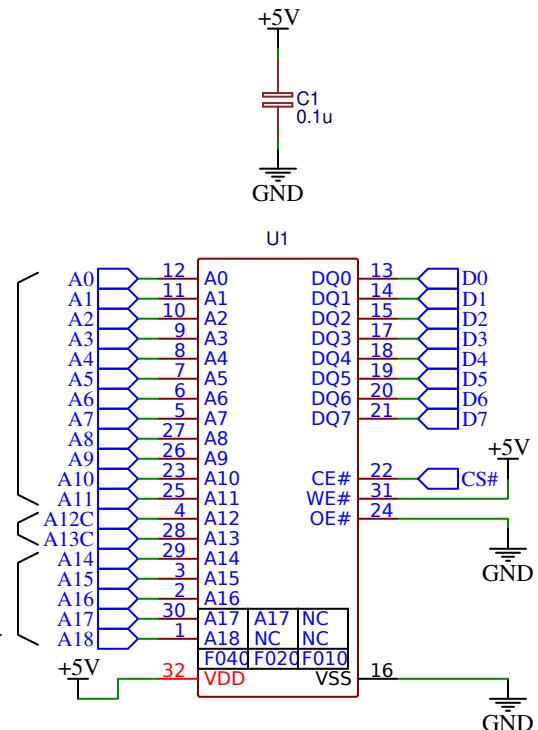


The lower 12 bits of the address are set by the P2000 via J1.

Address lines A12C and A13C are computed by U2.

The upper 5 bits select a block of 16k via dipswitches SW1.



SST39SF010: 1Mb/128kB (8×16kB) flash
SST39SF020: 2Mb/256kB (16×16kB) flash
SST39SF040: 4Mb/512kB (32×16kB) flash

TITLE: P2000 Multi ROM Cartridge		REV: 1.0
Erix Collectables	Company: Erix Collectables	Sheet: 1/1
Date: 2026-02-09	Drawn By: Maarten Pennings	