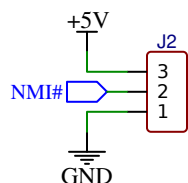
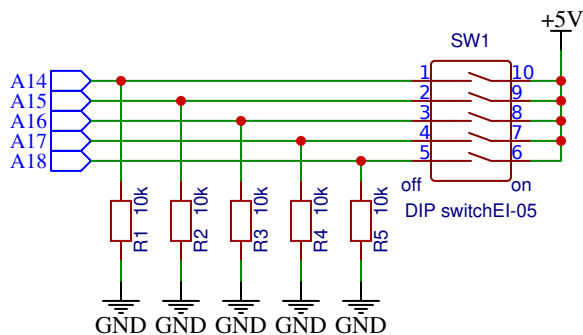


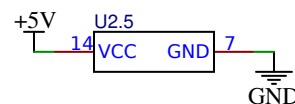
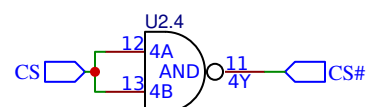
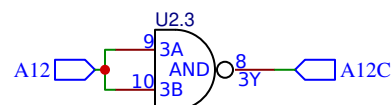
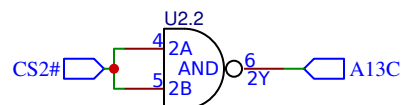
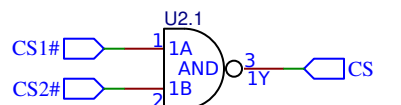
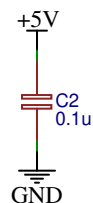
"b" side is front



Debug connector



ROM select (F040:5, F020:4, F010:3)



MC74HC00AN

Quad NAND for addressing logic

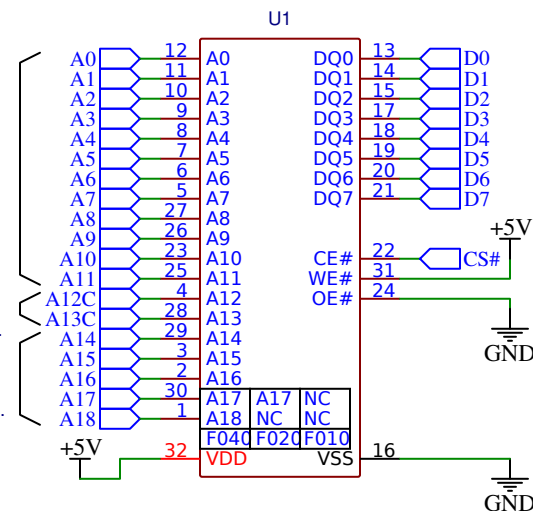
The two 8kB cartridges are mapped to 1000..4FFF.
U2 remaps that to 0000..3FFF.

addr range	Block	A15	A14	A13	A12	CS2#	CS1#	A13C	A12C
..FFFF	Other	1	1	1	1	0	0		
6000	Other	0	1	1	0	0	0		
5000..5FFF	Video	0	1	0	1	0	0		
4000..4FFF	Cartridge 2	0	1	0	0	0	1	1	1
3000..3FFF	Cartridge 2	0	0	1	1	0	1	1	0
2000..2FFF	Cartridge 1	0	0	1	0	1	0	0	1
1000..1FFF	Cartridge 1	0	0	0	1	1	0	0	0
0000..0FFF	Monitor	0	0	0	0	0	0		

The lower 12 bits of the address are set by the P2000 via J1.

Address lines A12C and A13C are computed by U2.

The upper 5 bits select a block of 16k via dipswitches SW1.



SST39SF010: 1Mb/128kB (8×16kB) flash
SST39SF020: 2Mb/256kB (16×16kB) flash
SST39SF040: 4Mb/512kB (32×16kB) flash

TITLE: P2000 Multi ROM Cartridge		REV: 1.0
Company: Erix Collectables		Sheet: 1/1
Date: 2026-02-09	Drawn By: Maarten Pennings	