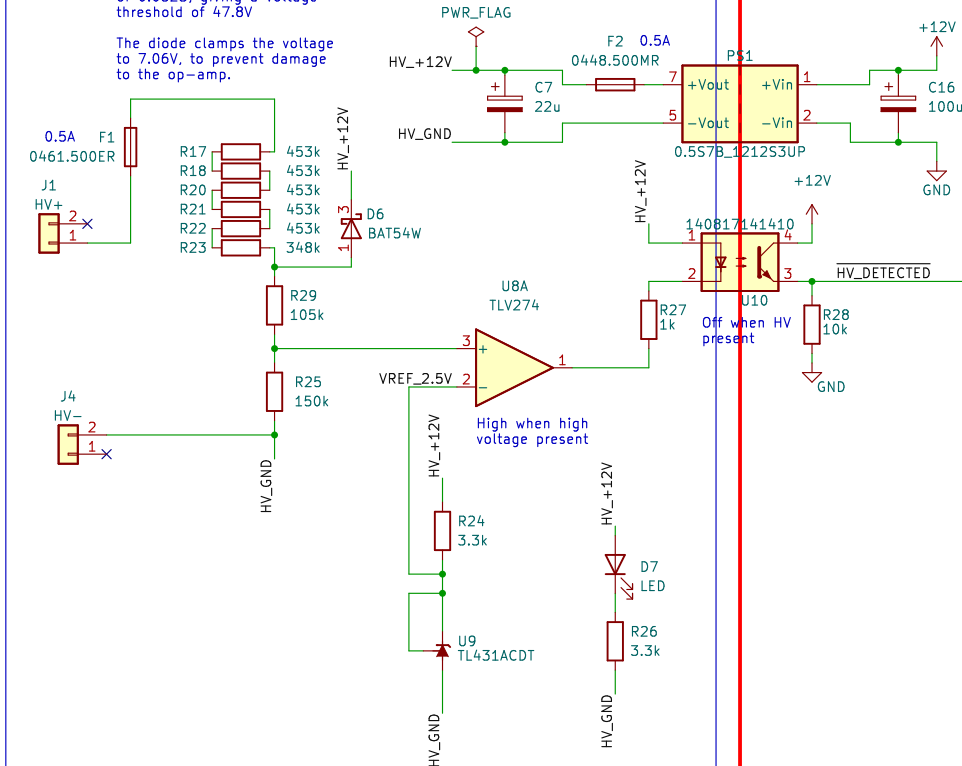


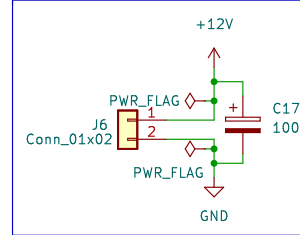
## HV Detection

For low input voltages, the diode is reverse biased and there is an attenuation ratio of 0.0523, giving a voltage threshold of 47.8V

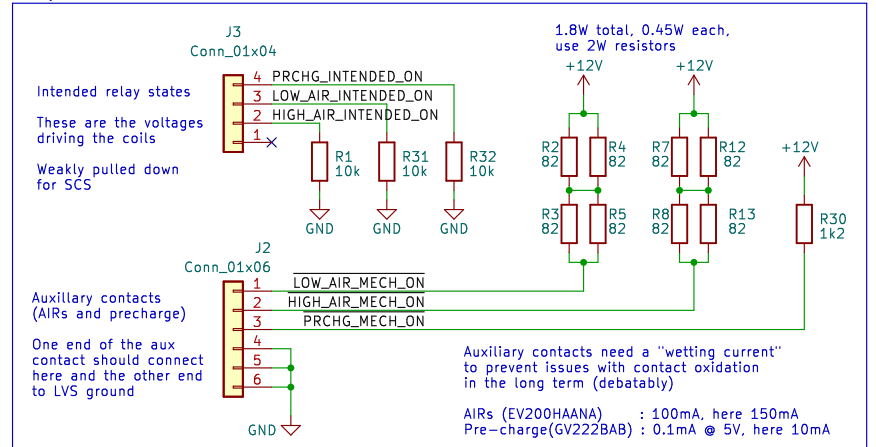
The diode clamps the voltage to 7.06V, to prevent damage to the op-amp.



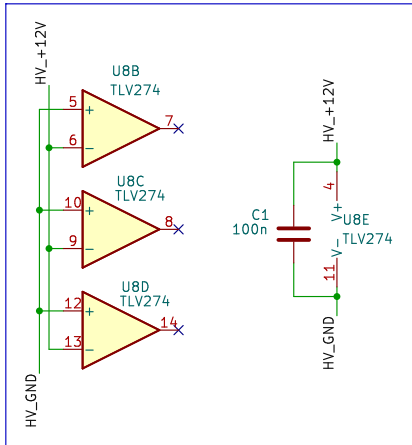
## LV Power



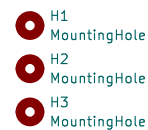
## Relay Connections



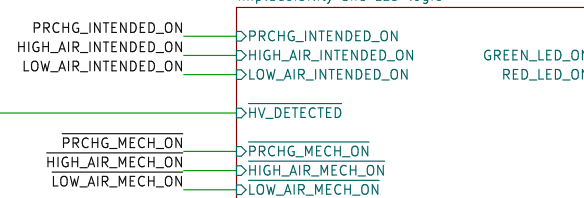
## IC Power and Unused



## Mounting Holes



## Implausibility and LED logic



File: logic.kicad\_sch

PCB located in junction box, not accumulator

Vehicle: STAG 9

Drawn By: Joe Pater, Tim Brewis

Checked By: Marek Frodyma

CAD Part:

**SUFST** – Southampton University Formula Student Team

Sheet: /

File: hv\_sense.kicad\_sch

**Title: HV Sense**

Size: A4 Date: 2023-05-11

KiCad E.D.A. kicad 7.0.1-0

**Rev: 1.1.1**

Id: 1/2



