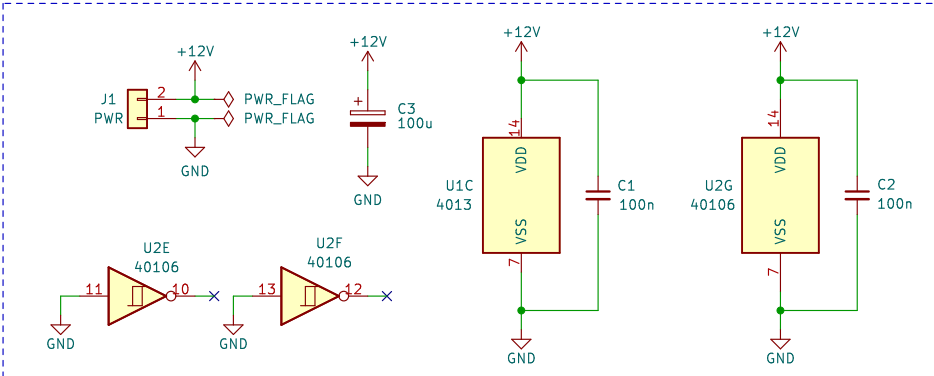
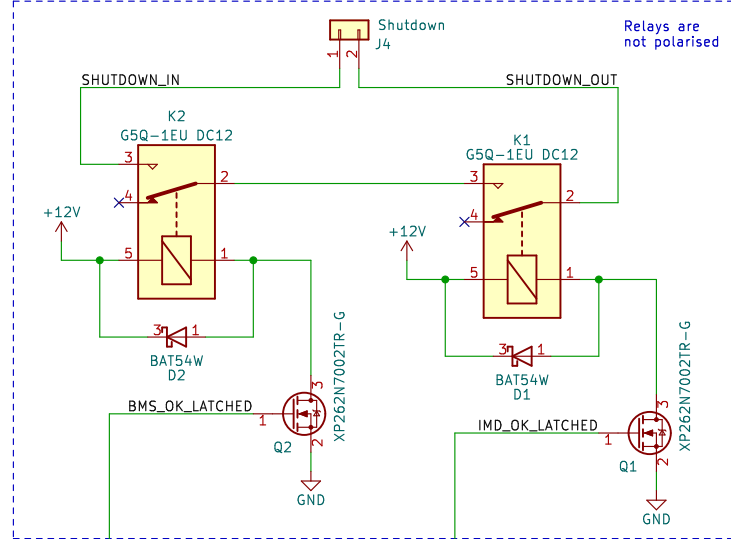


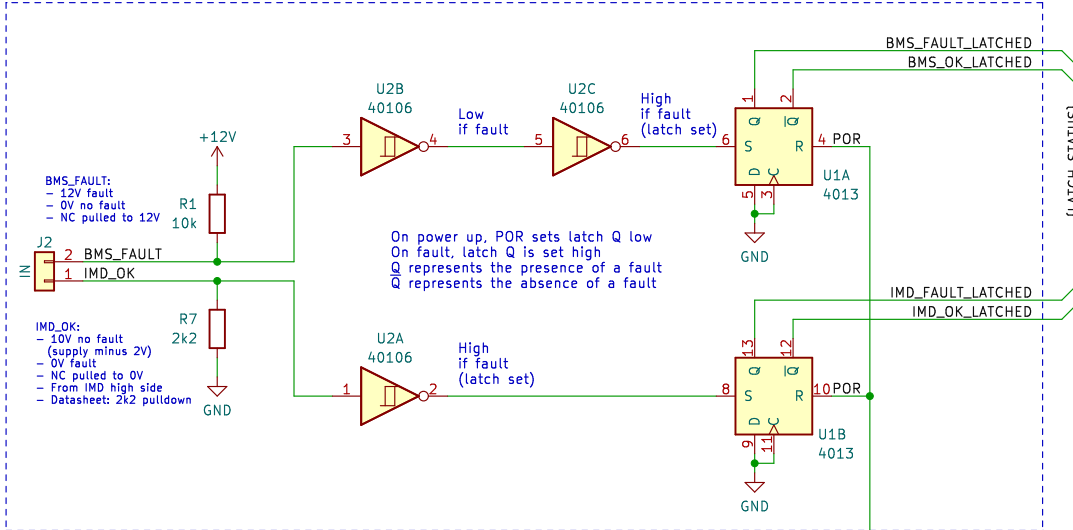
# LVS and IC Power, Unused Gates



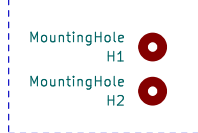
# Shutdown Relays



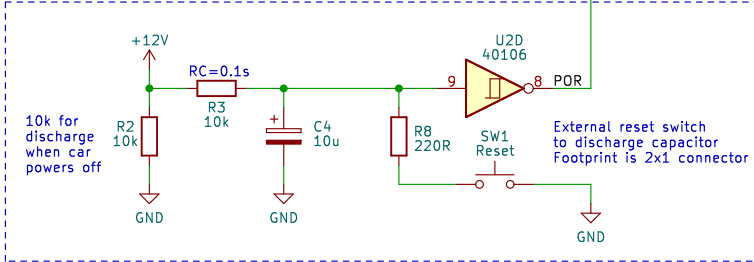
# Fault Detection and Latching



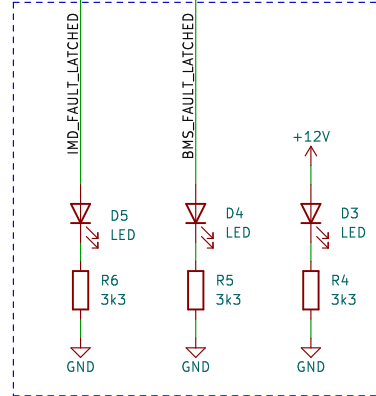
# Mounting Holes



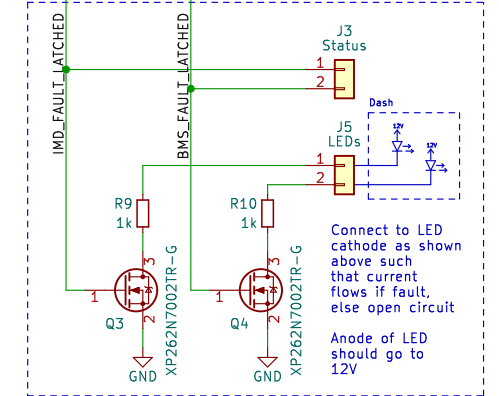
# Power-On Reset



# Debug LEDs



# External Connections



Vehicle: STAG 9  
 Drawn By: Tim Brewis  
 Checked By: Marek Frodyma, Max O'Brien  
 CAD Part:

**SUFST**

Sheet: /  
 File: latching.kicad\_sch

**Title: Latch Circuit**

Size: A4  
 Date: 2023-04-04  
 KiCad E.D.A. kicad (6.0.11-0)

**Rev: 1.1.1**  
 Id: 1/1