Top Level Schematic

Rom_LinearTransform

IN /8

clk

LUT

Zin

data /26

reset /1

en /1

mod-m-counter

delay

max_tick /1

LRShifter

en /1

reset /1

out /16

lr
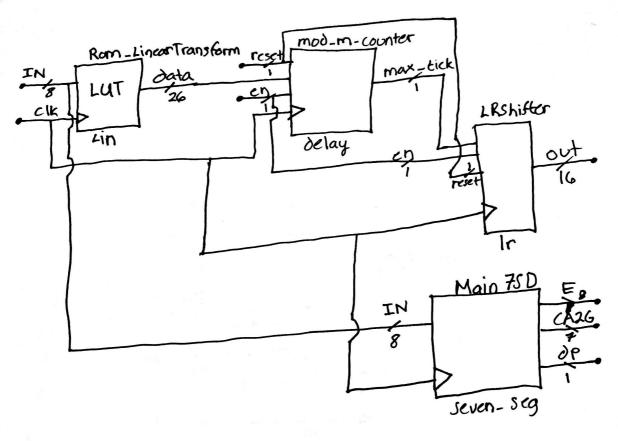
Main 7SD

IN /8

seven-seg

E /8

CA2G /7

dp /1

Top level inputs

clk, reset, en,
[7:0] in

Top level outputs

[15:0] out, [7:0] E,
[6:0] CA2G, dp