WinDbg Cheatsheet 2026 (Userland)

Navigation

t (F11)	Step Into
p (F10)	Step Over
g (F5)	Run
gu	Execute until the current function is complete
restart	Stop and restart execution

Breakpoints

bl	Lists breakpoints
bp [addr]	Set breakpoint
bc #	Clear breakpoint (takes wildcards)
bd #	Disable breakpoint (takes wildcards)
be #	Enable breakpoint (takes wildcards)

Inspecting Modules

lm	Lists modules
lm o	Lists only loaded modules
lm a [addr]	Lists the module that contains the address [addr]

Inspecting Memory

db [addr]	displays data as Bytes + ASCII (at address [addr])
da [addr]	displays data ASCII string until NULL is found
du [addr]	displays data UNICODE string until NULL is found

Patching Memory

eb [addr] [value]	patches [addr] with single Byte
ed [addr] [value]	patches [addr] with double word (32 bits)
eza [addr] [value]	patches [addr] with ASCII string
ezu [addr] [value]	patches [addr] with UNICODE string

Vieweing Memory Maps

Augue eget arcu dictum varius duis at consectetur lorem donec. Semper quis lectus nulla at volutpat diam ut venenatis tellus:

1. Nulla aliquet porttitor lacus luctus accumsan. Volutpat maecenas volutpat blandit aliquam etiam erat velit scelerisque.

2. Elit sed vulputate mi sit amet mauris commodo.

3. Ac felis donec et odio pellentesque diam volutpat. Quam viverra orci sagittis eu volutpat odio facilisis mauris sit.

Changing Architecture

Tempus egestas sed sed risus pretium quam vulputate. Sed lectus vestibulum mattis ullamcorper. Quis ipsum suspendisse ultrices gravida.

Volutpat maecenas volutpat blandit aliquam etiam erat velit scelerisque in:

1. Eget sit amet tellus cras adipiscing enim.

2. Eu scelerisque felis example@example.com

Visualizing Registers

At ultrices mi tempus imperdiet nulla malesuada. Sit amet consectetur adipiscing elit pellentesque habitant. Phasellus vestibulum lorem sed risus ultricies tristique nulla aliquet.

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cras semper auctor
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MISC

Neque sodales ut etiam sit amet nisl purus. Ornare lectus sit amet est placerat in. Consectetur purus ut faucibus pulvinar elementum integer enim neque volutpat:

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amet porttitor eget dolor
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