

```
Done = 0
err = 0
enable0 = 0, enable1 = 0
comp = 0
write0 = 0, write1 = 0
wr = 0
rd = 0
cache_offset = g_addr[2:0]
mem_offset = g_addr[2:0]
memto_cache = 0
cache_to_mem = 0
next_state = 1011
Psm_stall = 1
cache_hit = 0
co_data = 0
cl_data = 0
```

