	FETCH (F)	DECODE (D)	EXECUTE (E)	MEMORY (M)	WRITE BACK (WB)													
xorl %eax, %eax	F	D	E	M	WB													
andl %edx, %edx		F	D	E	M	WB												
jle Done			F	D	E	M	WB											
mrmovl (%ebx), %esi				F	D	E	M	WB										
rmmovl %esi, (%ecx)																		
andl %esi, %esi				F	D	E	M	WB										
jle Npos					F	D	E	M	WB									
irmovl $1, %edi						F	D	E	M	WB								
addl %edi, %eax							F	D	E	M	WB							
irmovl $1, %edi								F	D	E	M	WB						
subl %edi, %edx									F	D	E	M	WB					
irmovl $4, %edi										F	D	E	M	WB				
addl %edi, %ebx											F	D	E	M	WB			
addl %edi, %ecx												F	D	E	M	WB		
andl %edx, %edx													F	D	E	M	WB	
jg Loop														F	D	E	M	WB

Q1.   This goes on for 24 cycles

Q2.   This is significantly lower than the CPE of 16.44 in archlab