

Bit offset	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0	Source Port																Destination Port															
32	Sequence Number																															
64	Acknowledgement Number																															
96	Checksum																Padding															
128+	Packet Information																															

State Transition Diagram:

