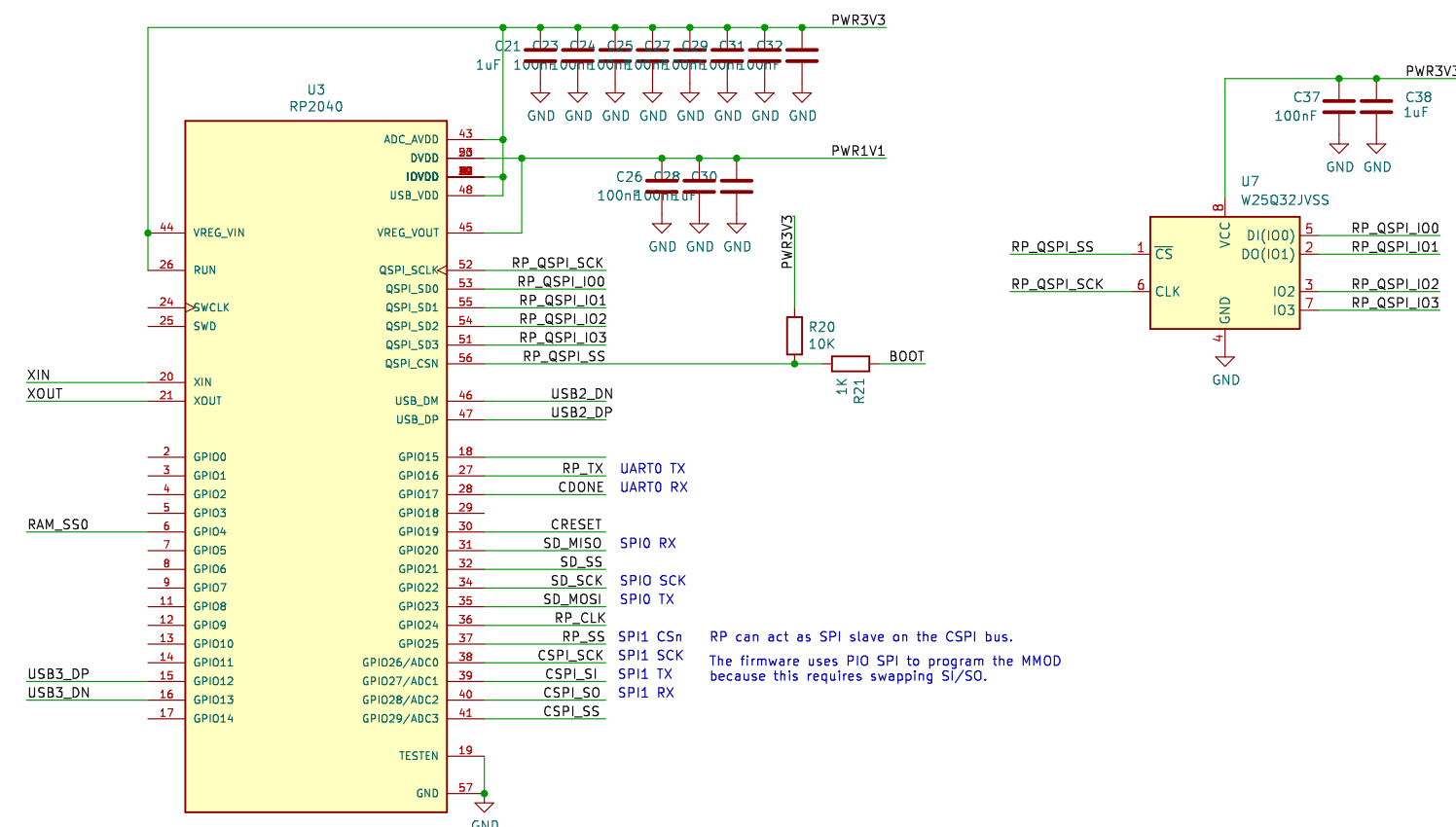
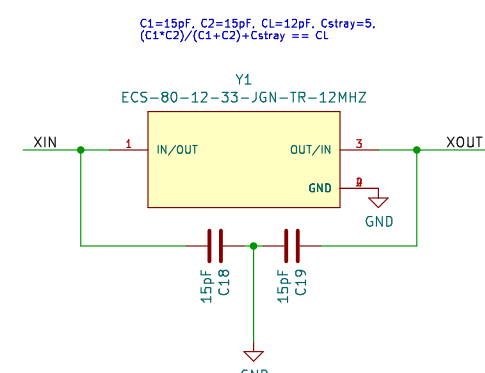
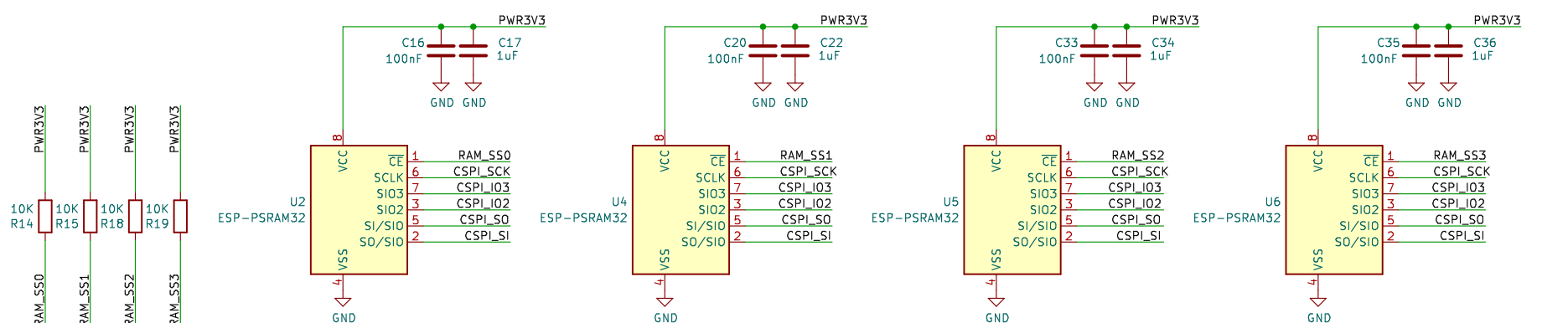
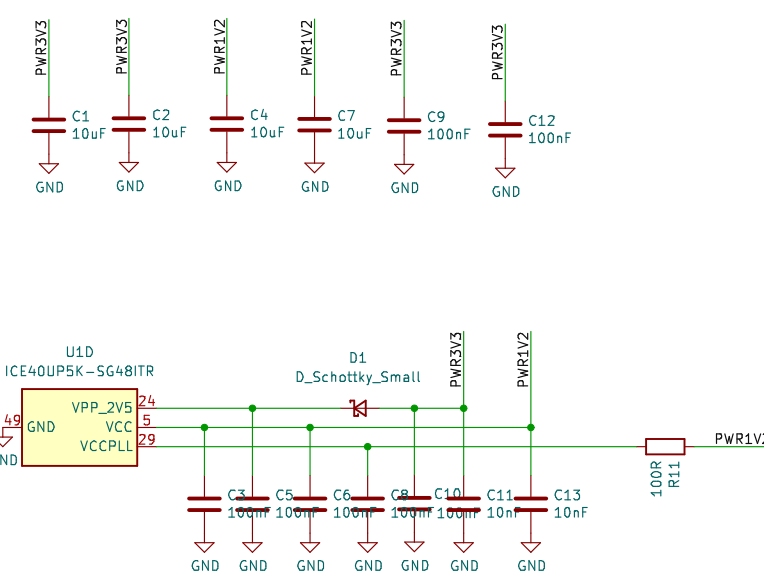
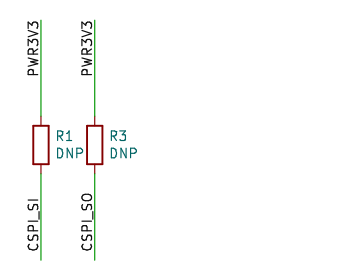


CSPL_SS is pulled up by the MMIO or the firmware



The firmware uses PIO SPI to program the MMIO because this module supports *CSPI*.

