"Differential Data Multiple Interface"

CDONE is also RP_RX (FPGA_TX)

CSPI_SS is pulled up by the MMOD or the firmware

C1=15pF, C2=15pF, CL=12pF, Cstray=5.
(C1*C2)/(C1+C2)+Cstray == CL

RP can act as SPI slave on the CSPI bus.
The firmware uses PIO SPI to program the MMOD because this requires swapping SI/SO.

USB1: FPGA USB DEVICE & POWER

USB2: RP2040 USB DEVICE/DEBUG

USB3: RP2040 USB HOST (PIO)

BONBON	
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Sheets: /	
File: idt.kicad_sch	
Title: Bonbon Computer	
Size: A1	Date: 2022-07-28
KiCad E.D.A. kicad 6.0.5-a6ca702e91-116~ubuntu20.04.1	Rev: V1
	Id: 1/1