6502 Instruction Opcode Table

Opcode	imp	imm	zp	zpx	zpy	izx	izy	abs	abx	aby	ind	rel	Function	N	V	B	D	I	Z	C
ORA		0x09	0x05	0x15		0x01	0x11	0x0D	0x1D	0x19			A:=A or adr	*					*	
AND		0x29	0x25	0x35		0x21	0x31	0x2D	0x3D	0x39			A:=A&adr	*					*	
EOR		0x49	0x45	0x55		0x41	0x51	0x4D	0x5D	0x59			A:=A exor adr	*					*	
ADC		0x69	0x65	0x75		0x61	0x71	0x6D	0x7D	0x79			A:=A+adr	*	*				*	*
SBC		0xE9	0xE5	0xF5		0xE1	0xF1	0xED	0xFD	0xF9			A:=A-adr	*	*				*	*
CMP		0xC9	0xC5	0xD5		0xC1	0xD1	0xCD	0xDD	0xD9			A-adr	*					*	*
CPX		0xE0	0xE4					0xEC					X-adr	*					*	*
CPY		0xC0	0xC4					0xCC					Y-adr	*					*	*
DEC			0xC6	0xD6				0xCE	0xDE				adr:=adr-1	*					*	
DEX	0xCA												X:=X-1	*					*	
DEY	0x88												Y:=Y-1	*					*	
INC			0xE6	0xF6				0xEE	0xFE				adr:=adr+1	*					*	
INX	0xE8												X:=X+1	*					*	
INY	0xC8												Y:=Y+1	*					*	
ASL	0x0A		0x06	0x16				0x0E	0x1E				adr:=adr*2	*					*	*
ROL	0x2A		0x26	0x36				0x2E	0x3E				adr:=adr*2+C	*					*	*
LSR	0x4A		0x46	0x56				0x4E	0x5E				adr:=adr/2	*					*	*
ROR	0x6A		0x66	0x76				0x6E	0x7E				adr:=adr/2+C*128	*					*	*

Table 1: Instruktionen geordnet nach Funktionsgruppen
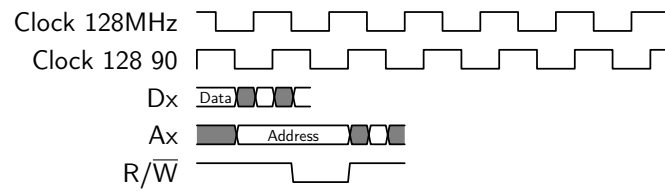
Instruction Mnemonic	Addressing Mode	Assembler Format	Operation	opcode	Bytes
LDA	Immediate	LDA #oper	$\# \to A$	A9	2
	Zeropage	LDA addr	MEM[00addr]$\to$A	A5	2
	Zeropage,X	LDA addr,X	MEM[00|addr+X]$\to$ A	B5	2
	Absolute	LDA ADDR	MEM[ADDR]$\to$A	AD	3
	Absolute,X	LDA ADDR,X	MEM[ADDR+X]$\to$ A	BD	3
	Absolute,Y	LDA ADDR,Y	MEM[ADDR+Y]$\to$ A	B9	3
	(Indirect,X)	LDA (addr,X)	MEM[MEM[addr+X-1|addr+X]]$\to$A	A1	2
	(Indirect),Y	LDA (addr),Y	MEM[MEM[addr+1]+Y]$\to$A	B1	2

Table 2: 6502 Opcode table