Pin no.	Symbol	Name	Direction	Function
14,37,5,83 77,15,71	A4-A7 A13-A15	Address bus	In	Address Bus line inputs from Z80
39,25,17,31,32,19,35,21	D0-D7	Data bus	In/Out	Data Bus inputs/outputs from/to Z80
2,72,80,52,11	MA14-MA18	Memory address bus	Out	Address Bus line outputs to memory
54	CLK16	16 MHz clock input	In	Clock input 16 MHz
26	CLK_CPU	CPU clock	Out	Clock to Z80 CPU 4 MHz (normal) or 8 MHz (turbo) depending on jumper configuration
40	CLK_FDC	FDC clock	Out	Clock to WD1772 8 MHz (DD) or 16 MHz (HD) depending on jumper configuration
33	CLK4	Expansion bus clock	Out	Clock 4 Mhz to Expansion Bus
1	/PwrOn	Power-on reset	In	Active low resets the system and forces configuration based on jumper settings
66	/RESET	Reset system	In	Active low forces the boot mode and initializes interface output register and set RESET for serial controller
43	RESET	Reset for serial controller	Out	Active high output to reset serial controller
76	IROM		In	Active high disables all on-board ROM
44	/BUSGE		In	Active low disconnects all on-board chips from the CPU buses
74	/RFSH	Refresh	In	Active low indicates that the lower 7 bits of the address bus contain a refresh address for dynamic memories
48	/IORQ	I/O Request	In	Active low indicates that the lower half of the address bus holds a valid I/O address for a I/O read or write operation
9	/MREQ	Memory Request	In	Active low indicates that the address bus holds a valid address for a memory read or write operation
12	/RD	Read to CPU	In	Active low indicates that the CPU want to read data from memory or I/O device
10	/WR	Write from CPU	In	Active low indicates that the CPU data bus holds valid data to be stored in the addressed memory or I/O device
18	/MixDrive	Mix Drive mode select jumper	In	Shorted enabled Mix Drive mode
84	/Turbo	Turbo mode select jumper	In	Shorted enabled Turbo mode at power on
56	/HD	HD mode select jumper	In	Shorted enabled HD mode at power on
81	/UINT	UART Interrupt Enable jumper	In	Shorted enabled UART interrupts
34	/FlashWE	Flash Write Enable jumper	In	Shorted allows to write Flash memory
75	INTRQ	FDC Interrupt	In	Active high indicates that the FDC has finished executing any command or resets the reading of the FDC status register
79,67	TxRDY.A TxRDY.B	UART Tx Ready Interrupt	In	Active high indicate that UART channel is ready to accept a new data character
3,6	TxE.A TxE.B	UART Tx End Interupt	In	Active high indicate that UART channel transmitter has no new characters to send and is waiting in an idle state
69,57	RxRDY.A RxRDY.B	UART Rx Ready Interrupt	In	Active high indicate that UART channel contains a data character that is ready to be input
45,68	BRKDET.A BRKDET.B	UART Break Detected Interrupt	In	Active high indicates that the receiver for UART channel has detected a break condition
7	/ROMCS	ROM Chip Select	Out	To activate ROM chip
4	/RAMCS	RAM Chip Select	Out	To activate RAM chip
82	/OUT	Interface Output Select	Out	To activate interface output register
36	/IN	Interface Input Select	Out	To activate interface input
13	/FDC	FDC Chip Select	Out	To activate FDC chip
47	/DDEN	DD Mode Select	Out	Active low selects double (MFM) density in FDC
41	/INT	Interrupt Request to CPU	Out	Active low is generated by I/O devices
20,23,24	/RS1-/RS3	WD2123 Chip Select	Out	To activate UART chip
50,53,65,62	/DRV0-/DRV3	Drive Select	Out	Active low selects one of 4 drives
61	/Side1	Side1 Select	Out	Active low selects side 1 of double sided disks
55	/InUse	Drive In Use	Out	Active low indicate the selected drive is in use
63	LED_Turbo	Turbo mode LED indicator	Out	Active high indicate CPU is in Turbo mode (8 MHz clock)
70	LED_HD	HD mode LED indicator	Out	Active high indicate FDC is in HD mode (16 MHz clock)
46	LED_InUse	Drive In Use LED indicator	Out	Active high indicate the selected drive is in use
51,58	Tie1-Tie2	Programmable Ground	In	Must be connected to the ground
22,38,64,73,78	Vcc	+5V Power	Power	
8,16,27,42,49,60	GND	Ground	Power	
28,29,30,59	TDI/TMS/TCK/TDO	CPLD programming pins	In/Out	CPLD programming pins