

# Jesse Zhang

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<b>EDUCATION</b>	<i>Bachelor of Science</i> , Computer Engineering University of Illinois at Urbana-Champaign Relevant Coursework: ECE 411 Computer Organization & Design, ECE 391 Computer Systems Engineering, CS 423 Operating Systems Design, ECE 408 Applied Parallel Programming, CS 225 Data Structures
<b>PUBLICATIONS</b>	Kim, M., <b>Zhang, X.</b> , Milenkovic, O. (2016). MetaCRAM: an integrated pipeline for metagenomic taxonomy identification and compression. <i>BMC Bioinformatics</i> . 17:94.
<b>WORK EXPERIENCE</b>	<b>Apple - Software Engineering Intern</b> Summer 2016 <ul style="list-style-type: none"><li>Developed a proof-of-concept iOS app for retail store inventory management</li><li>Interacted with SAP systems on the backend</li><li>Participated in designing the service calls and the overall program flow</li></ul> <b>Fulcrum GT – Software Engineering Intern</b> Summer 2015 <ul style="list-style-type: none"><li>Launched Epoch, a legal time entry solution, at ILTA 2015</li><li>Primary iOS backend developer – responsible for designing and implementing the data model in Core Data for Epoch</li><li>Explored location and physical activity tracking, as well as geo-fencing, using Core Location and Core Motion frameworks</li><li>Designed overall program flow for asynchronous activities using NSNotificationCenter, libdispatch, delegates, and closures</li></ul> <b>ECE 391 – Course Staff</b> August 2015 - Now
	<b>Coordinated Science Lab – Research Intern</b> May 2014 - August 2015 <ul style="list-style-type: none"><li>Automate parallelized DNA compression and maximize DNA compression ratio</li><li>Developed the Extended Golomb Code compression scheme adapted for DNA read-specific statistical distributions</li></ul>
<b>PROJECTS</b>	<b>ECE 411 – SystemVerilog</b> WenMeiCraw – Pipelined LC-3 CPU with L1, L2 Caches <ul style="list-style-type: none"><li>Implemented basic structure of the pipelined CPU</li><li>Implemented L1 and L2 caches<ul style="list-style-type: none"><li>Multicycle 4-way set associative L2 with true LRU replacement policy</li><li>Eviction Write Buffer, Victim Cache, and Hardware Prefetching in the memory hierarchy</li></ul></li></ul> <b>ECE 391 – x86 Assembly, C</b> Za Big New OS – Linux-like operating system <ul style="list-style-type: none"><li>Implemented the PIC configuration code and developed the interrupt handlers for the keyboard and RTC</li><li>Implemented the Linux ext2 file system with both read and write functionality</li><li>Developed the system calls for device and file I/O as well as the execution and halting of a task</li><li>Implemented the C Standard Library as well as C runtime in conjunction with the native runtime</li></ul> <b>BoilerMake – C, Java, Objective-C</b> HackedReality – virtual reality using Google Cardboard ( <i>Winning project 2014</i> ) <ul style="list-style-type: none"><li>Developed a driver for a DDR Dancepad to mimic the omni-directional treadmill and implemented dynamic remapping of the dancepad buttons</li><li>Used the magnetometer in Android phone to track the user's orientation</li><li>Used the Pebble smartwatch to track the user's body motions</li></ul>
<b>TECHNICAL SKILLS</b>	Languages: C, Assembly, Swift, SystemVerilog, C++, Perl, Java, Objective-C