IPC is used to synchronize cores

Core 0 performs a part of SVD
and scheduling for one SB

| System Initialization | Channel Initialization | Channel Cache WB | SVD for $\frac{K}{8}$ channels | Cache WB | Scheduler Algorithm | Idle task | Core 0 |

| System Initialization | Waiting for Interrupt | SVD for $\frac{K}{8}$ channels | Cache WB | Idle task | Core 1 |

Core 1 – Core 7 shares SVD Load

| System Initialization | Waiting for Interrupt | SVD for $\frac{K}{8}$ channels | Cache WB | Idle task | Core 7 |
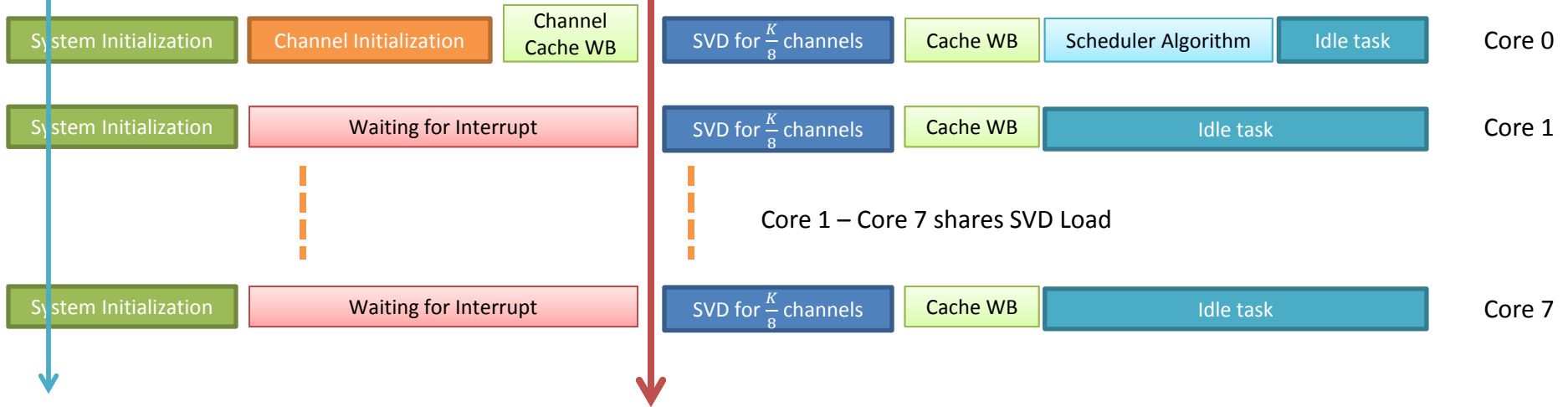
Interrupt from Core 0 on the updated channel state information