



# JITHENDRA.S

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## Objective

To pursue a challenging career in the industry to further enhance my technical abilities and to contribute my skills, knowledge for the fulfilment of organizational objectives through determination, hard work, and continuous learning.

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## Education

- **BMSIT &M** 2022  
Electronics and communication ENGINEERING  
7.05cgpa
- **KVT polytechnic** 2019  
DIPLOMA  
80.36%
- **Nalanda High School** 2016  
SSLC  
83.04%

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## Experience

- **VLSIGURU Training Institute** Jan 2023 - November 2023  
Trainee as Functional Verification Engineer
- **TCR Innovation** July 2021 - August 2021  
Internship on Front End Developer
- **Naveen Eletricals and Rewinding works** November 2018 - December 2018  
Implant Training
- **KAUSHALYA Technical Training** December 2018 - January 2019  
Intership On Internet Of Things

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## Skills

- Programming Language:Verilog, System Verilog , UVM, microcontroller (8051)
- Tools :Xilinx, Cadence, Kicad, Matlab, Multisim,Questasim
- The Arduino Platform(uno, Nano,micro) and Resberry pi

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## Achievements & Awards

- Awarded "Second prize in open day –Project Based Learning at BMSIT
- "Best Student Award" 2019 at KVT Polytechnic
- Qualified for district, division level kabaddi tournaments
- Qualified for "Quiz" computations at school level
- Nominated for "SMART INDIA Hackathon" conducted at national level

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## Projects

- **Memory control using Verilog and SV**

Designed Memory(DUT) in Verilog and verified using Verilog Front door and Back door methods and also verified using System Verilog test bench architecture

- **Ambulance Rescue System And Vehicle Accident Detection And Monitoring Using IOT**

Whenever Crash takes place GPS system sends location and picture of Victim to ambulance

- **"IOT BASED ANTI-THEFT FLOOR MAT SYSTEM USING RASPBERRY PI**

sensor based security system device which captures photo of a person steps on sensor

- **Heartbeat Monitoring System with Serial Communication**

This system is a biomedical device capable of monitoring and recording the patient health

- **Automatic water level controller**

It's an Electrical project where the water level is measured by the sensor and controlled by motor

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## Publications

- **Ambulance Rescue System**

Paper published in IRJMETS on "ACCIDENT DETECTION, RESCUE AND MONITORING SYSTEM"

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## Interests

- Hardware Description language:- Verilog HDL, System Verilog, UVM
  - Analog Circuit Design
  - Advance Digital Design
  - Micro-controllers and Micro-processors, Embedded systems
  - Arduino (uno, mega ) , Raspberry Pi
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## Activities

- Attended NATIONAL level "SMART INDIA Hackathon"
  - Attended research and development center at Bangalore as a school visitor
  - Vande Bharatam (As a part of AICTE ACTIVITY)
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## Languages

- KANNADA , ENGLISH, HINDI, TELUGU
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## Hobby

- Sports, Following Social Media, Traveling, Newspaper Reading, Solving logical problem And Sudoku.