

REV Description BY DATE Madison Traynhan 03/04/2015 Α1 Initial Layout PAGE NO. SCHEMATIC PAGE COVER PAGE 1 2 Processor ATmega 2560 3 Real Time Clock 4 microSD 5 Thermistors 6 Thermocouples PIC16F913 Heart Rate Monitor 8 Esp8266 WiFi Module 9 Accelerometer 10 Power Regulation 11 Programming Interface UNCC USAFRL HEAT Control Vest Friday, March 20, 2015



















