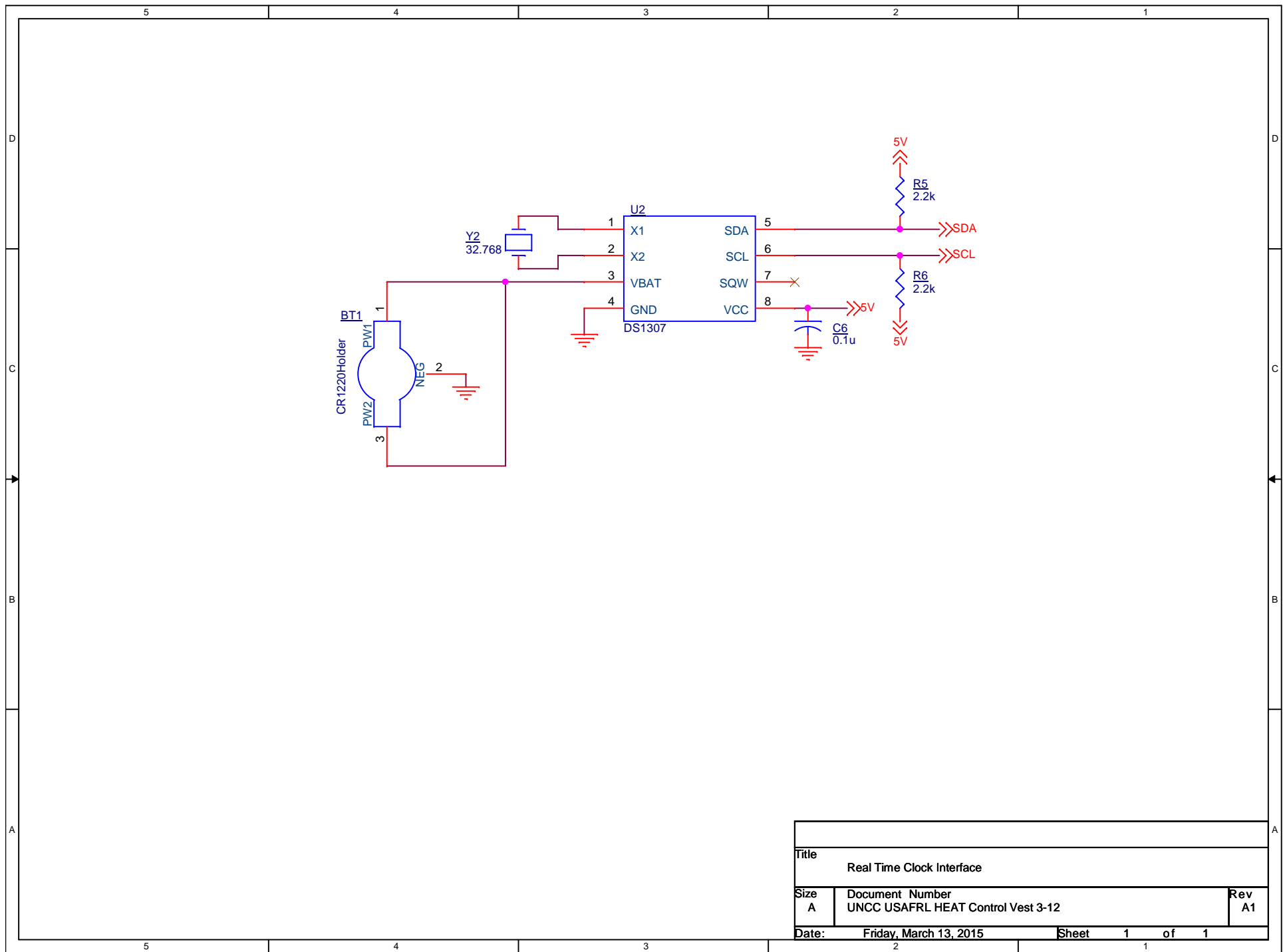
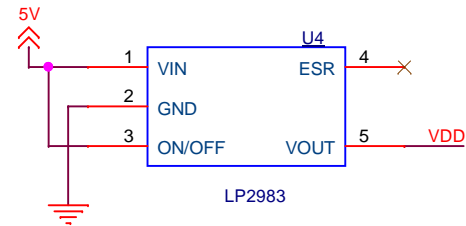
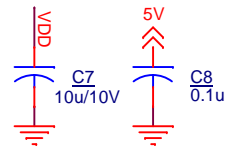
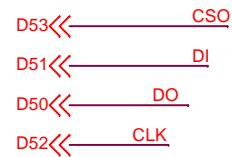
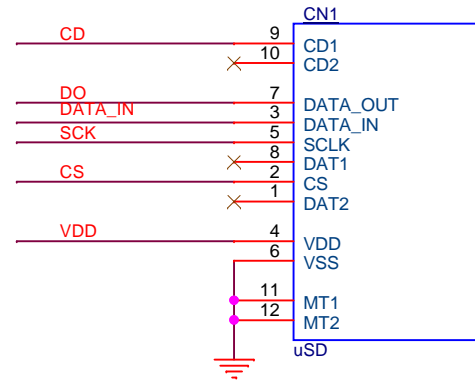
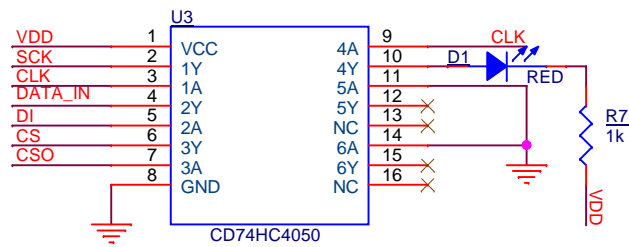


REV	Description	DATE	BY
A1	Initial Layout	03/04/2015	Madison Traynham

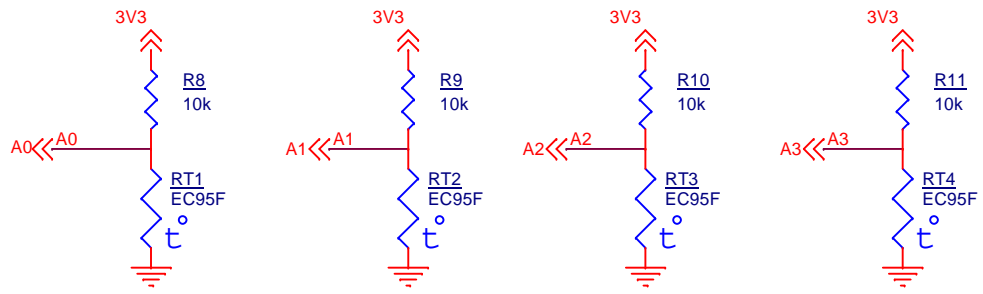
PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	Processor ATmega 2560
3	Real Time Clock
4	microSD
5	Thermistors
6	Thermocouples
7	PIC16F913 Heart Rate Monitor
8	Esp8266 WiFi Module
9	Accelerometer
10	Power Regulation
11	Programming Interface

Title			
UNCC USAFRL HEAT Control Vest			
Size	Document	Number	Rev
B	1-12		A1
Date:	Friday, March 13, 2015		Sheet 1 of 1

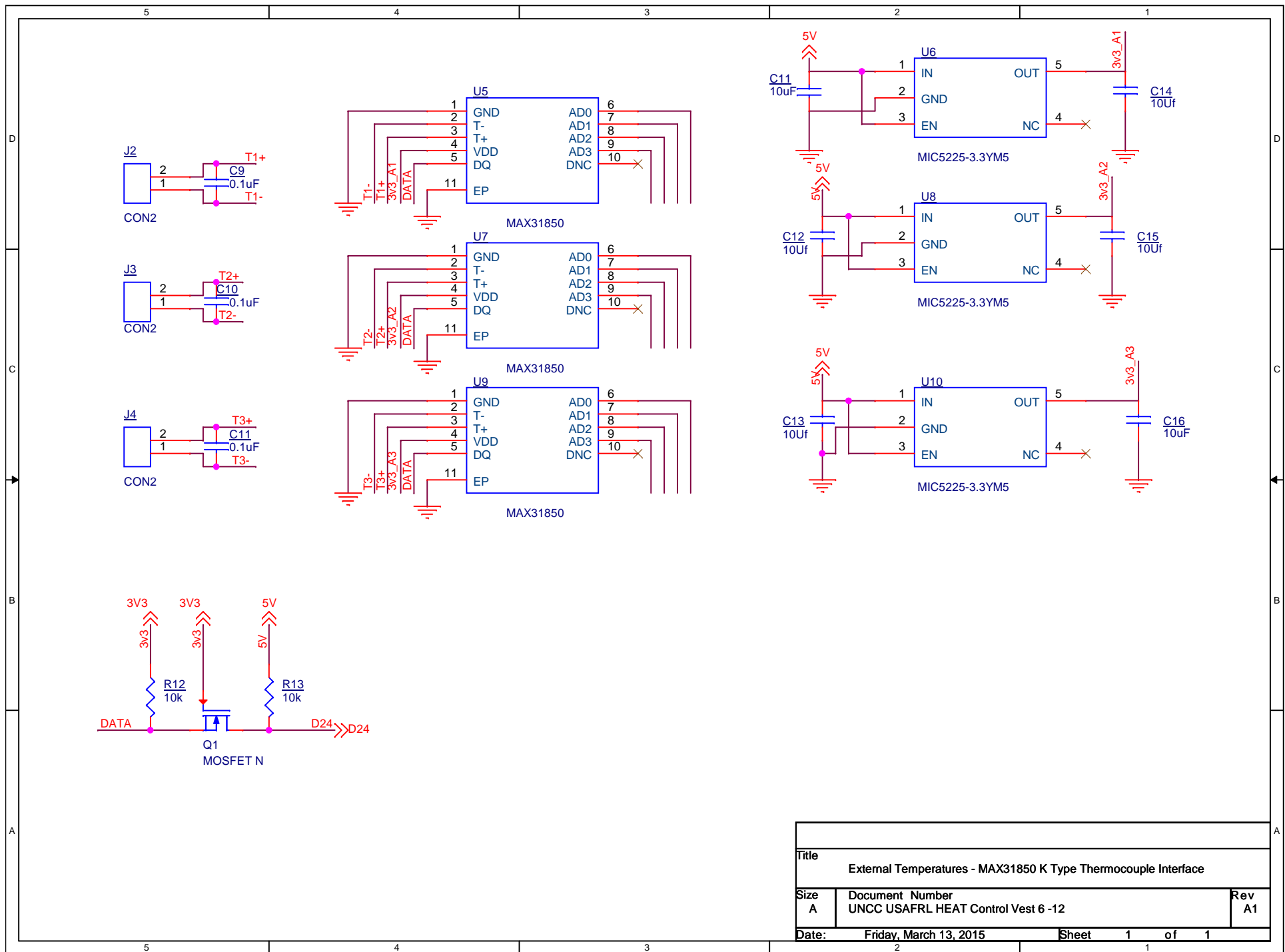




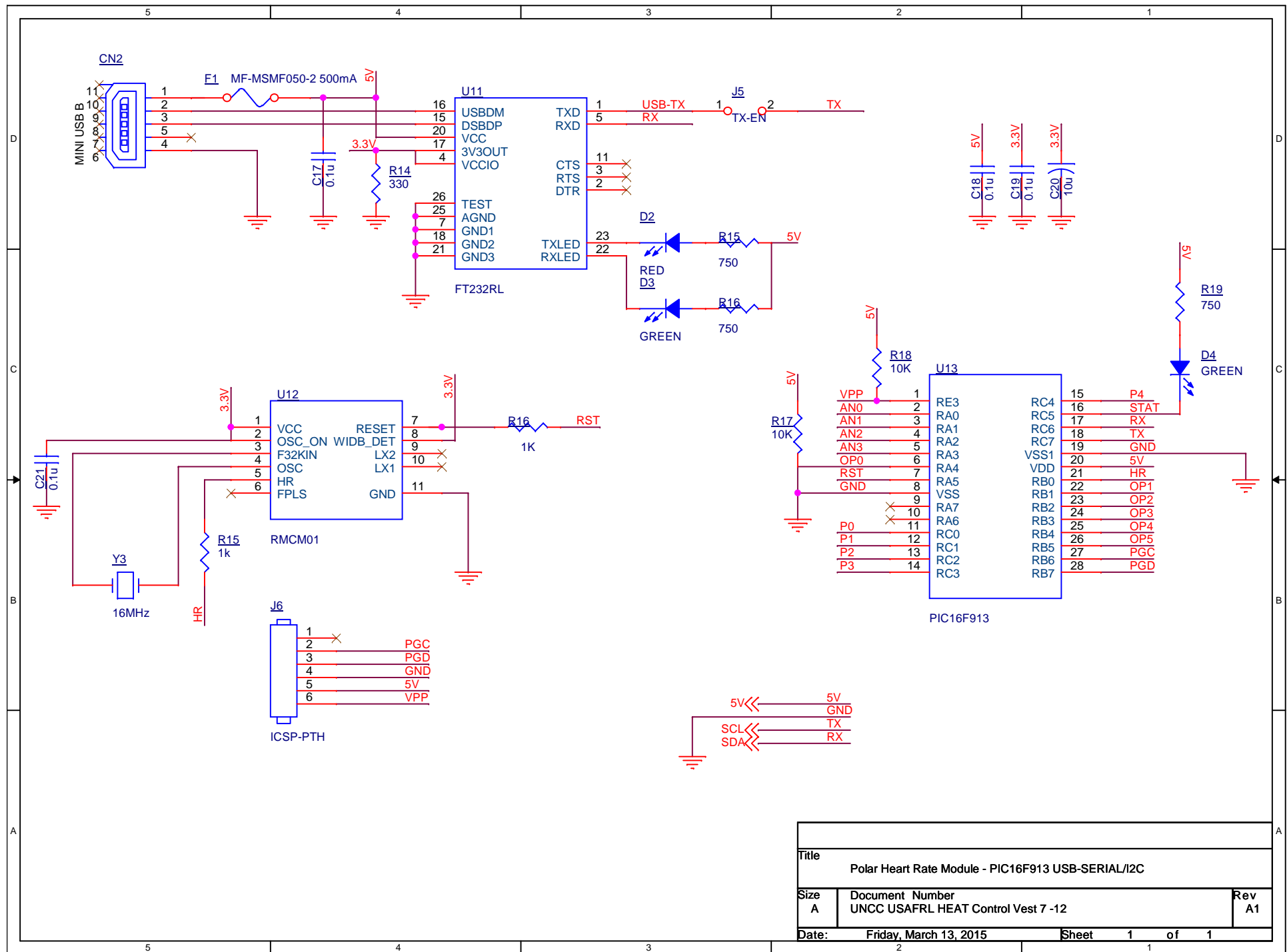
Title		
microSD interface with transflash and spi support		
Size A	Document Number UNCC USAFRL HEAT Control Vest 4-12	Rev A1
Date: Friday, March 13, 2015	Sheet 1 of 1	



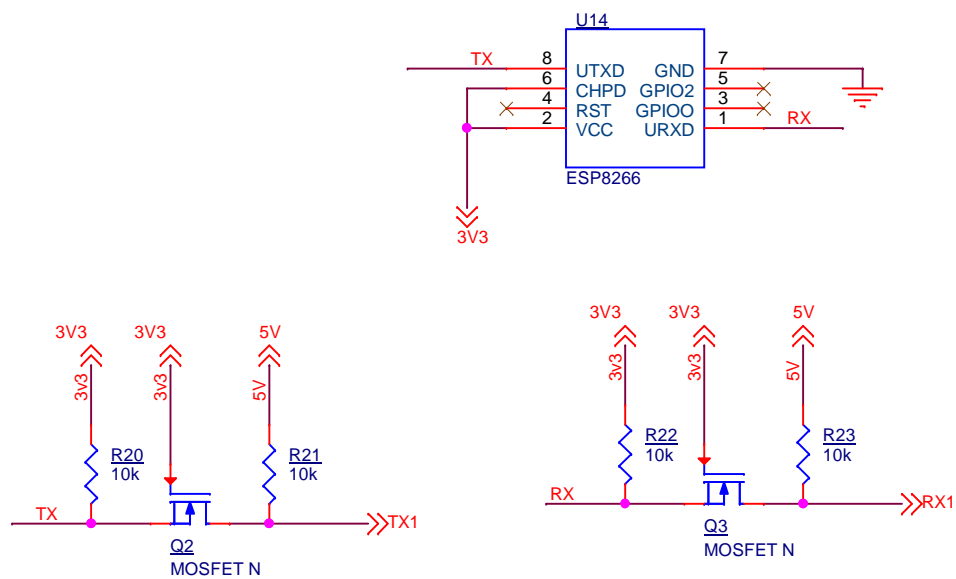
Title		
Body Heat Interface-EC95F 10k Thermistors		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 5-12	A1
Date: Friday, March 13, 2015		
Sheet 1 of 1		



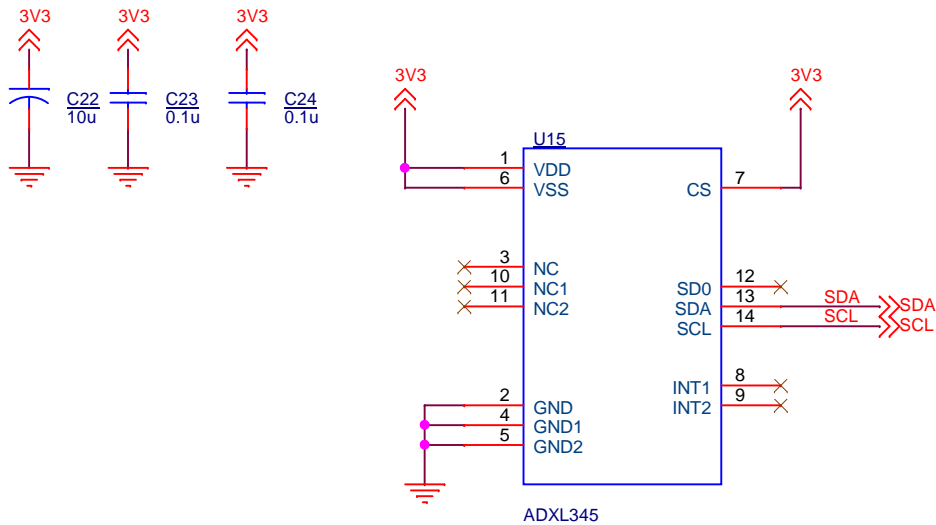
Title		
External Temperatures - MAX31850 K Type Thermocouple Interface		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 6 -12	A1
Date:	Friday, March 13, 2015	Sheet 1 of 1



Title		
Polar Heart Rate Module - PIC16F913 USB-SERIAL/I2C		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 7 -12	A1
Date:	Friday, March 13, 2015	Sheet 1 of 1

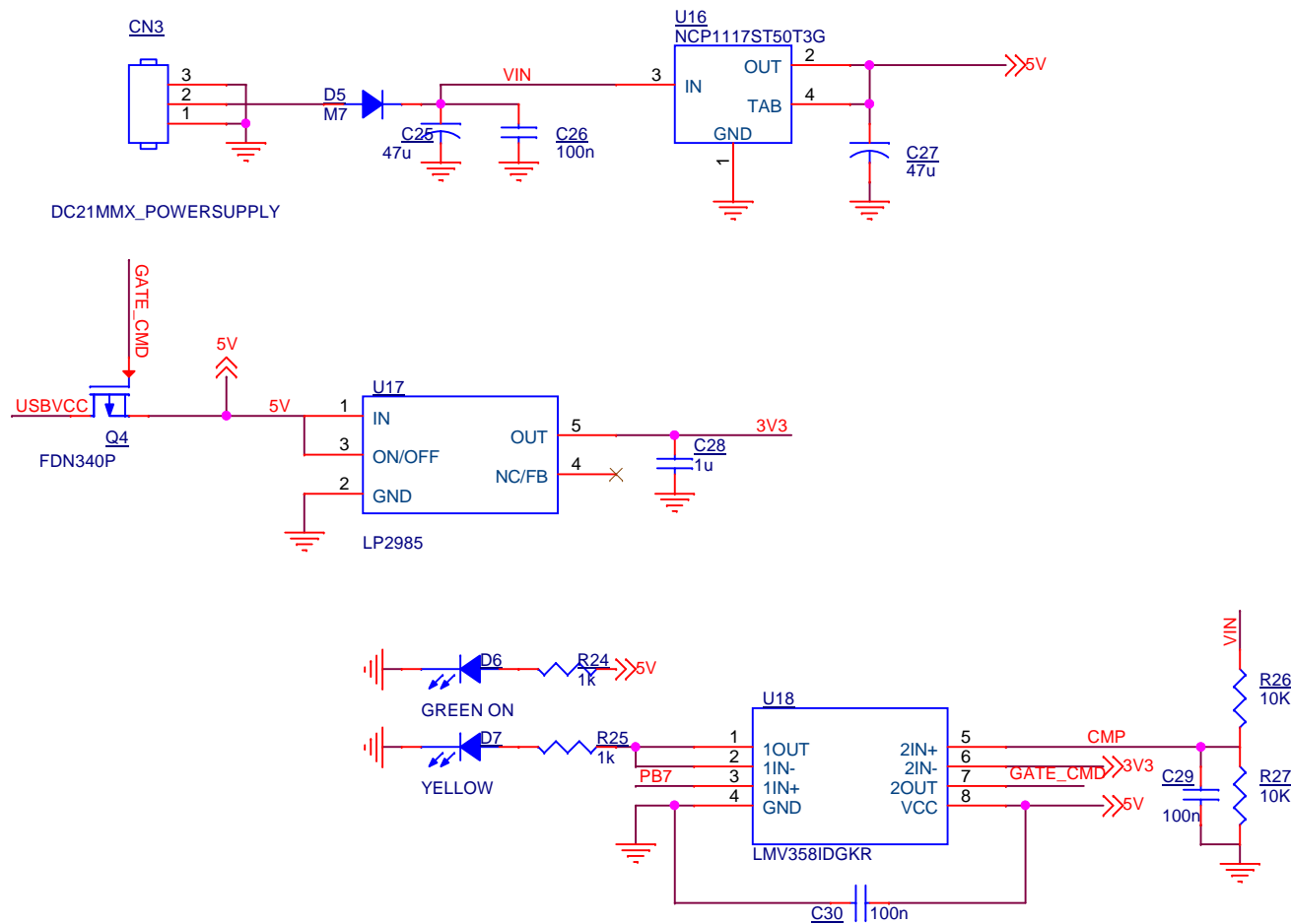


Title		
Datalink - Serial to WiFi ESP8266		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 8-12	A1
Date:	Friday, March 13, 2015	Sheet 1 of 1

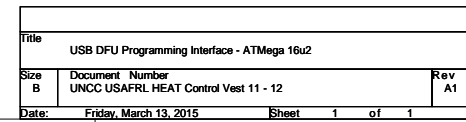


analog sda (A4) and scl (A5)

Title		
Tilt Sensor - ADXL345		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 9 - 12	A1
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C24, C25 should be a Electrolytic footprint not ceramic		
Title		
Power Regulation - 3V3 amd 5V LDO		
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A	UNCC USAFRL HEAT Control Vest 10-12	A1
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Title		
MultiRPM Compressor Control - Digital Potentiometer and ADJ LDO Reg		
Size	Document Number	Rev
A	UNCC USAFRL HEAT Control Vest 12-12	A0
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