Operating
Systems:
Internals
and Design
Principles

# Chapter 7 Memory Management

Seventh Edition
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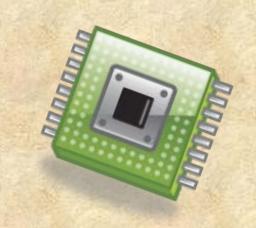
## Operating Systems: Internals and Design Principles

I cannot guarantee that I carry all the facts in my mind. Intense mental concentration has a curious way of blotting out what has passed. Each of my cases displaces the last, and Mlle. Carère has blurred my recollection of Baskerville Hall. Tomorrow some other little problem may be submitted to my notice which will in turn dispossess the fair French lady and the infamous Upwood.



— THE HOUND OF THE BASKERVILLES,
Arthur Conan Doyle

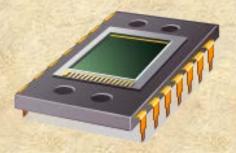
## Memory Management Terms



	Frame	A fixed-length block of main memory.
		A fixed-length block of data that resides in secondary memory (such as disk). A page of data may temporarily be copied into a frame of main memory.
Section of the second section of	Segment	A variable-length block of data that resides in secondary memory. An entire segment may temporarily be copied into an available region of main memory (segmentation) or the segment may be divided into pages which can be individually copied into main memory (combined segmentation and paging).

## Memory Management Requirements

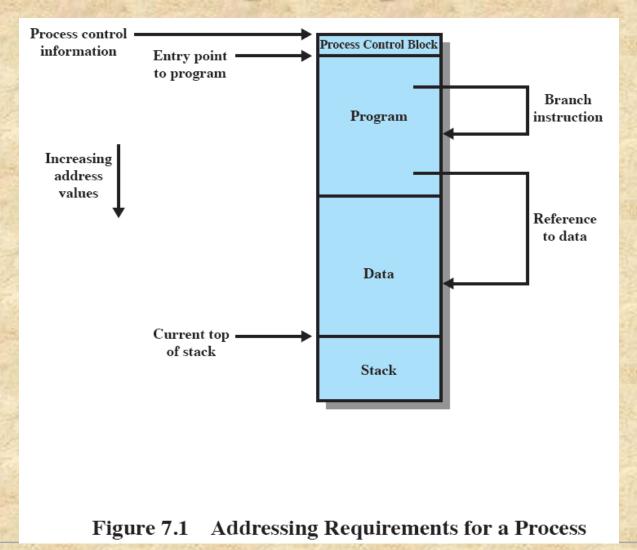
- Memory management is intended to satisfy the following requirements:
  - Relocation
  - Protection
  - Sharing
  - Logical organization
  - Physical organization



### Relocation

- Programmers typically do not know in advance which other programs will be resident in main memory at the time of execution of their program
- Active processes need to be able to be swapped in and out of main memory in order to maximize processor utilization
- Specifying that a process must be placed in the same memory region when it is swapped back in would be limiting
  - may need to *relocate* the process to a different area of memory

## Addressing Requirements



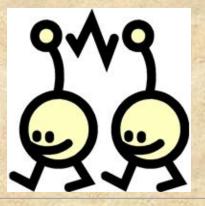
### Protection

- Processes need to acquire permission to reference memory locations for reading or writing purposes
- Location of a program in main memory is unpredictable
- Memory references generated by a process must be checked at run time
- Mechanisms that support relocation also support protection



## Sharing

- Advantageous to allow each process access to the same copy of the program rather than have their own separate copy
- Memory management must allow controlled access to shared areas of memory without compromising protection
- Mechanisms used to support relocation support sharing capabilities



## Logical Organization

Memory is organized as linear

#### Programs are written in modules

- modules can be written and compiled independently
- different degrees of protection given to modules (read-only, execute-only)
- sharing on a module level corresponds to the user's way of viewing the problem
- Segmentation is the tool that most readily satisfies requirements

## Physical Organization

Cannot leave the programmer with the responsibility to manage memory

Memory available for a program plus its data may be insufficient

Programmer does not know how much space will be available

overlaying allows various modules to be assigned the same region of memory but is time consuming to program

## Memory Partitioning

- Memory management brings processes into main memory for execution by the processor
  - involves virtual memory
  - based on segmentation and paging
- Partitioning
  - used in several variations in some now-obsolete operating systems
  - does not involve virtual memory

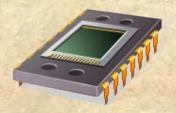


Table 7.2
Memory
Management
Techniques

Technique	Description	Strengths	Weaknesses
Fixed Partitioning	Main memory is divided into a number of static partitions at system generation time. A process may be loaded into a partition of equal or greater size.	Simple to implement; little operating system overhead.	Inefficient use of memory due to internal fragmentation; maximum number of active processes is fixed.
Dynamic Partitioning	Partitions are created dynamically, so that each process is loaded into a partition of exactly the same size as that process.	No internal fragmentation; more efficient use of main memory.	Inefficient use of processor due to the need for compaction to counter external fragmentation.
Simple Paging	Main memory is divided into a number of equal-size frames. Each process is divided into a number of equal-size pages of the same length as frames. A process is loaded by loading all of its pages into available, not necessarily contiguous, frames.	No external fragmentation.	A small amount of internal fragmentation.
Simple Segmentation	Each process is divided into a number of segments. A process is loaded by loading all of its segments into dynamic partitions that need not be contiguous.	No internal fragmentation; improved memory utilization and reduced overhead compared to dynamic partitioning.	External fragmentation.
Virtual Memory Paging	As with simple paging, except that it is not necessary to load all of the pages of a process. Nonresident pages that are needed are brought in later automatically.	No external fragmentation; higher degree of multiprogramming; large virtual address space.	Overhead of complex memory management.
Virtual Memory Segmentation	As with simple segmentation, except that it is not necessary to load all of the segments of a process. Nonresident segments that are needed are brought in later automatically.	No internal fragmentation, higher degree of multiprogramming; large virtual address space; protection and sharing support.	Overhead of complex memory management.

## **Fixed Partitioning**

- Equal-size partitions
  - any process whose size is less than or equal to the partition size can be loaded into an available partition
- The operating system can swap out a process if all partitions are full and no process is in the Ready or Running state

Operating System 8M SM 8M 8M SM 8M SM

(a) Equal-size partitions

## Disadvantages

- A program may be too big to fit in a partition
  - program needs to be designed with the use of overlays
- Main memory utilization is inefficient
  - any program, regardless of size, occupies an entire partition
  - internal fragmentation
    - wasted space due to the block of data loaded being smaller than the partition

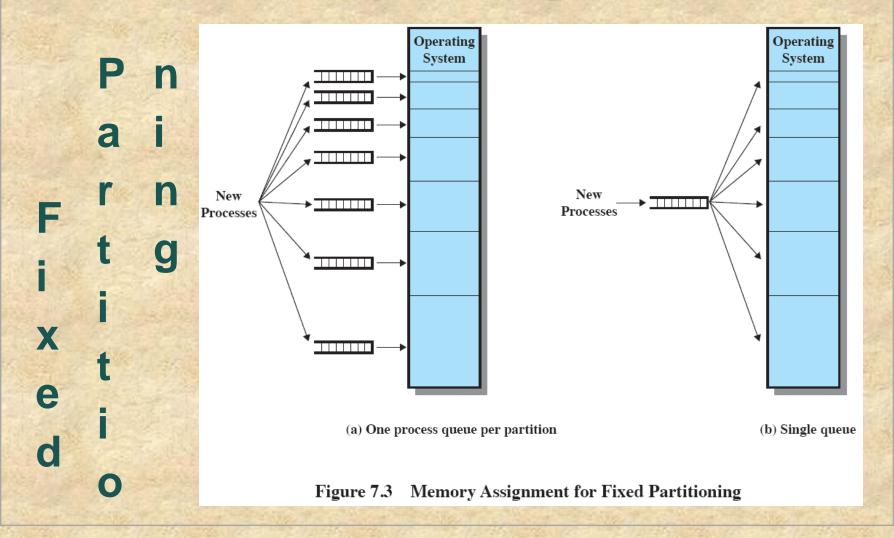
## Unequal Size Partitions

- Using unequal size partitions helps lessen the problems
  - programs up to 16M can be accommodated without overlays
  - partitions smaller than 8M allow smaller programs to be accommodated with less internal fragmentation

Operating System 2M4M6M8M8M12M16M

(b) Unequal-size partitions

## Memory Assignment



## Disadvantages

- The number of partitions specified at system generation time limits the number of active processes in the system
- Small jobs will not utilize partition space efficiently

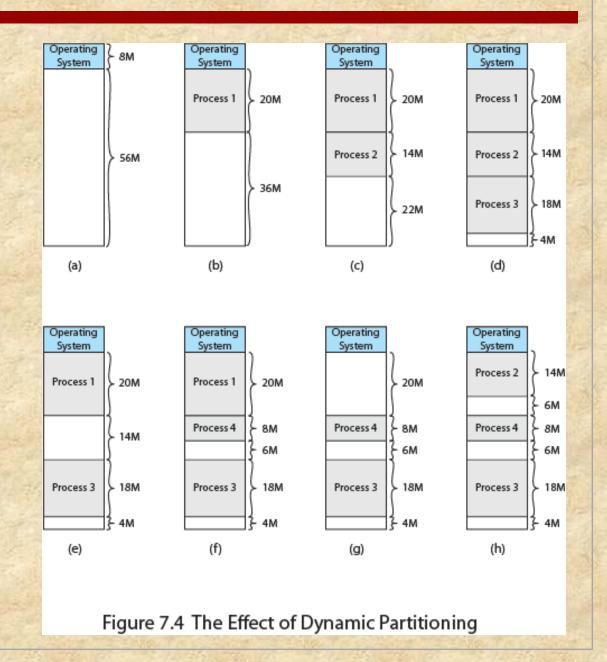


## Dynamic Partitioning

- Partitions are of variable length and number
- Process is allocated exactly as much memory as it requires
- This technique was used by IBM's mainframe operating system, OS/MVT



## Effect of Dynamic Partitioning



## **Dynamic Partitioning**

#### External Fragmentation

- memory becomes more and more fragmented
- memory utilization declines

#### Compaction

- technique for overcoming external fragmentation
- OS shifts processes so that they are contiguous
- free memory is together in one block
- time consuming and wastes CPU time

## Placement Algorithms

#### **Best-fit**

 chooses the block that is closest in size to the request

#### First-fit

 begins to scan memory from the beginning and chooses the first available block that is large enough

#### Next-fit

 begins to scan memory from the location of the last placement and chooses the next available block that is large enough

## Memory Configuration Example

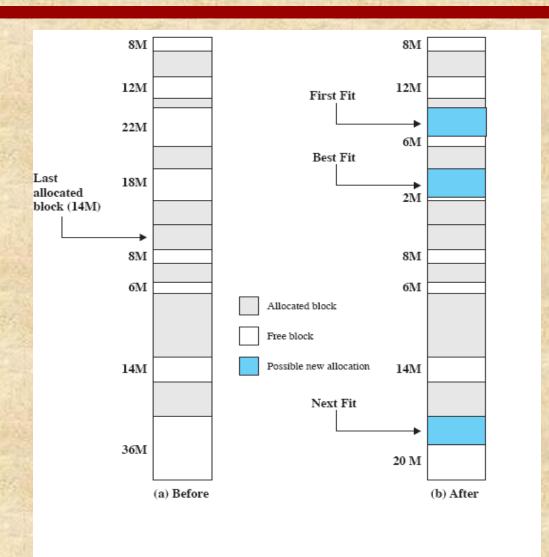


Figure 7.5 Example Memory Configuration before and after Allocation of 16-Mbyte Block

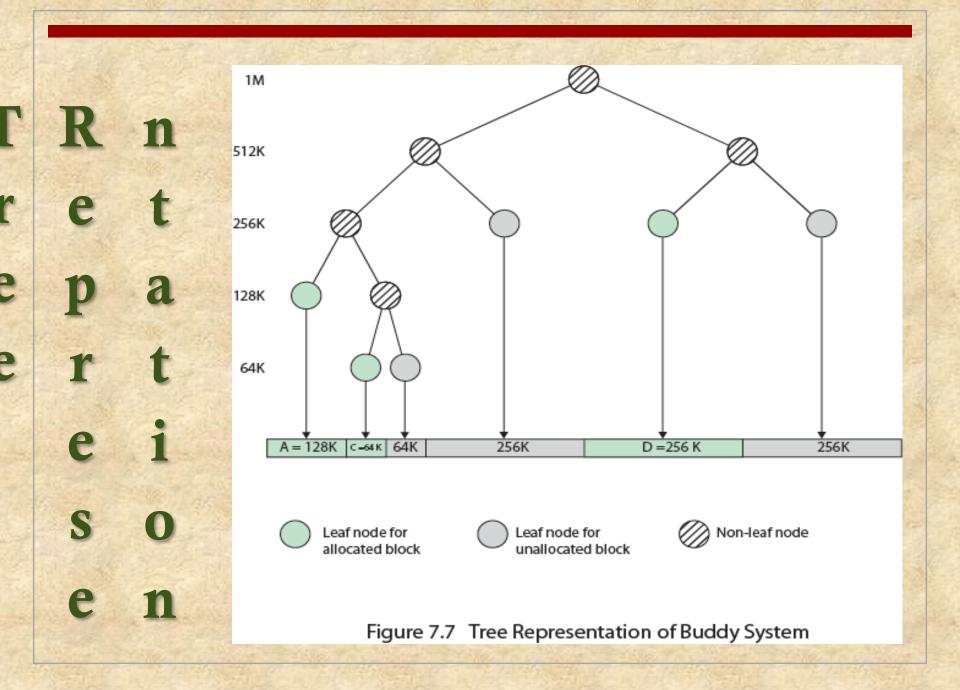
## **Buddy System**

- Comprised of fixed and dynamic partitioning schemes
- Space available for allocation is treated as a single block
- Memory blocks are available of size  $2^K$  words,  $L \le K \le U$ , where
  - $\blacksquare$  2<sup>L</sup> = smallest size block that is allocated
  - 2<sup>U</sup> = largest size block that is allocated; generally 2<sup>U</sup> is the size of the entire memory available for allocation

## Buddy System Example

1 Mbyte block			1	M	
Request 100 K	A = 128K	128K	256K	512K	
Request 240 K	A = 128K   128K		B = 256K	512K	
Request 64 K	A = 128K	C = 64K 64K	B = 256K	512K	
Request 256 K	A = 128K	C = 64K 64K	B = 256K	D = 256K	256K
Release B	A = 128K	C = 64K 64K	256K	D = 256K	256K
Release A	128K	C = 64K 64K	256K	D = 256K	256K
Request 75 K	E = 128K	C = 64K 64K	256K	D = 256K	256K
Release C	E = 128K	128K	256K	D = 256K	256K
Release E		51	2K	D = 256K	256K
Release D	1M				

Figure 7.6 Example of Buddy System



### Addresses

#### Logical

• reference to a memory location **independent** of the current assignment of data to memory

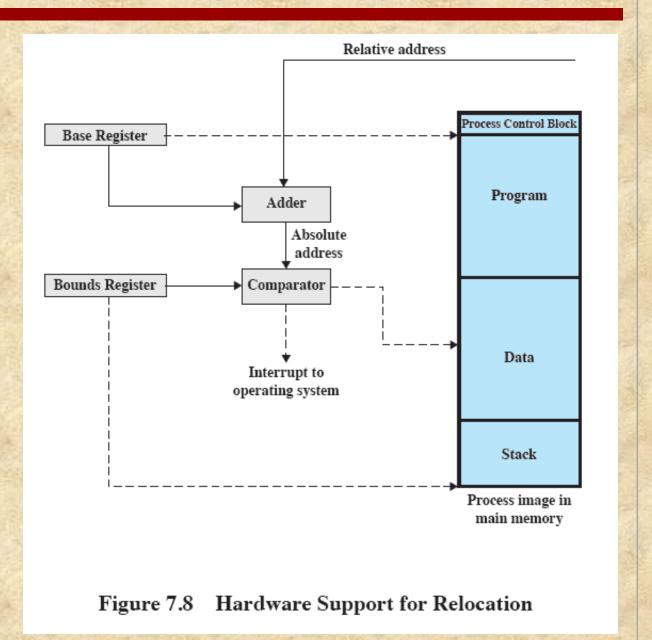
#### Relative

• address is expressed as a location relative to some known point (a particular example of logical address)

#### Physical or Absolute

actual location in main memory

#### Relocation



## Paging

- Partition memory into equal fixed-size chunks (termed frames) that are relatively small
- Process is also divided into small fixed-size chunks (termed pages)
   of the same size

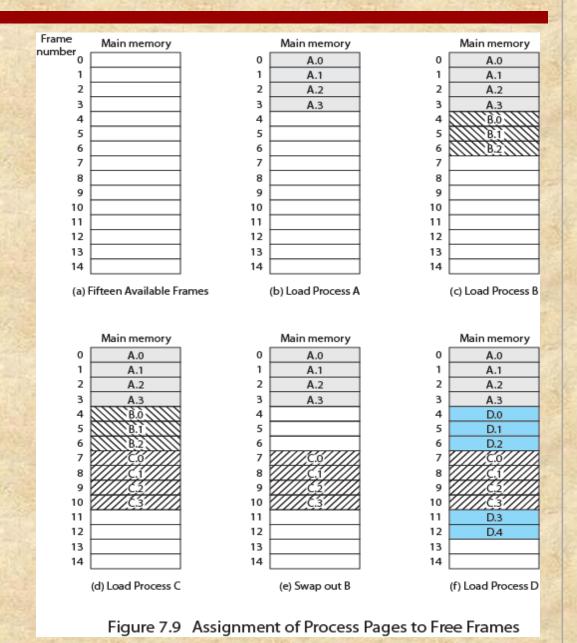
#### **Pages**

chunks of a process

#### Frames

 available chunks of memory

## Assignment of Process to Free Frames

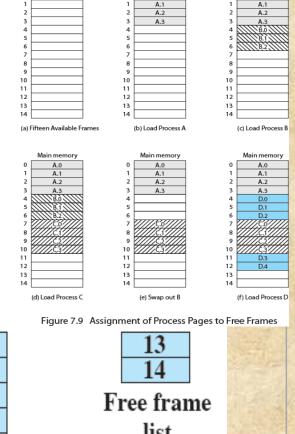


## Page Table

- Maintained by operating system for each process
- Contains the frame location for each page in the process
- Processor must know how to access for the current process
- Used by processor to produce a physical address



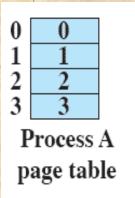
### Data Structures

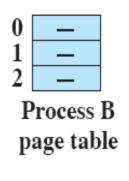


Main memory

A.0

Main memory





0	7					
1	8					
2	9					
3	10					
Process C						
page table						

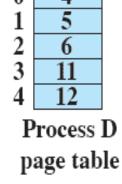
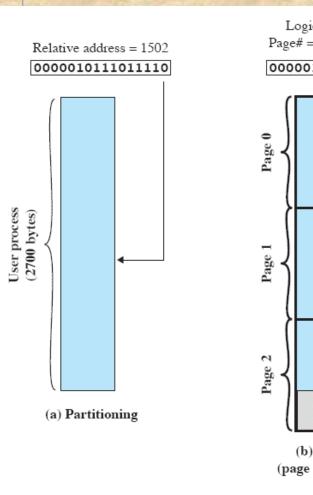
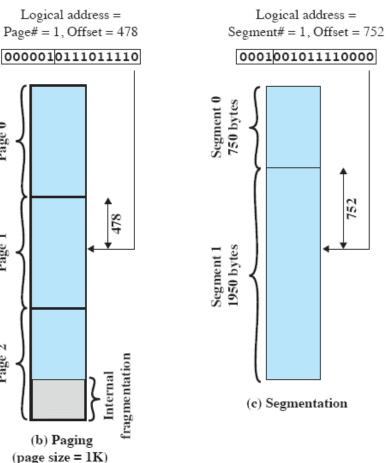




Figure 7.10 Data Structures for the Example of Figure 7.9 at Time Epoch (f)

#### Logical Addresses





**Logical address** 

= page # + offset within page

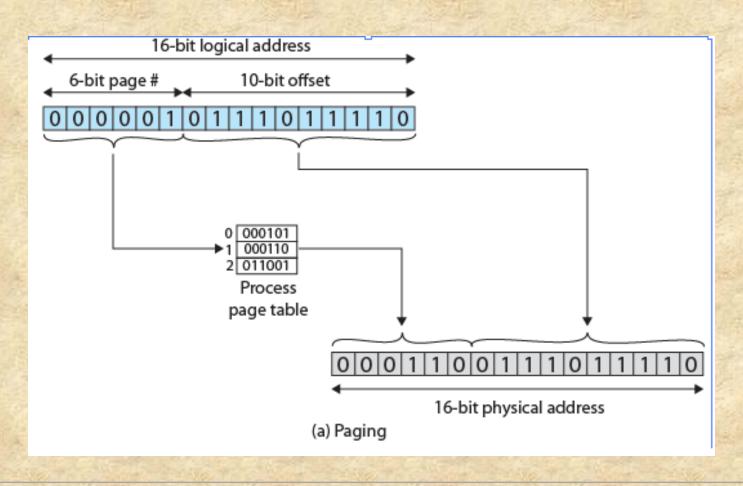
16-bit address

752

Page size = 1KB= 210 Bytes

Figure 7.11 Logical Addresses

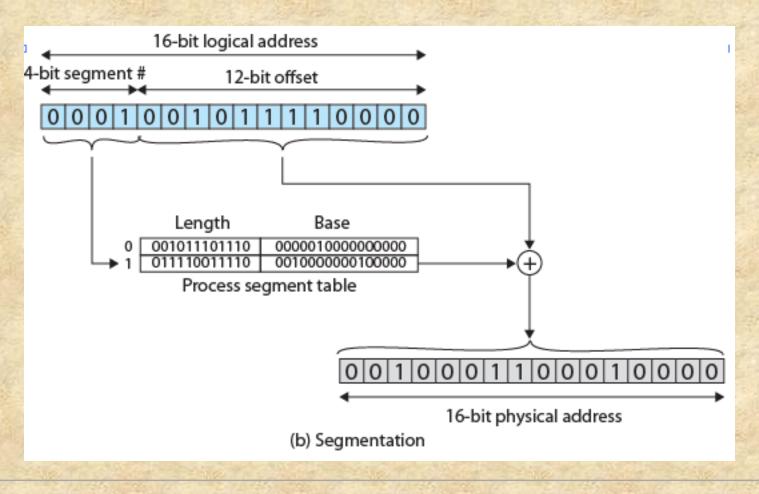
## Logical-to-Physical Address Translation - Paging



## Segmentation

- A program can be subdivided into segments
  - may vary in length
  - there is a maximum length
- Addressing consists of two parts:
  - segment number
  - an offset
- Similar to dynamic partitioning
- Eliminates internal fragmentation

### Logical-to-Physical Address Translation - Segmentation



## Summary

#### Memory Management

- one of the most important and complex tasks of an operating system
- needs to be treated as a resource to be allocated to and shared among a number of active processes
- desirable to maintain as many processes in main memory as possible
- desirable to free programmers from size restriction in program development
- basic tools are paging and segmentation (possible to combine)
  - paging small fixed-sized pages
  - segmentation pieces of varying size