

Name:-

Murad Waheed

Roll NO:-

18-CET-2024

Group:-

Computer Engineering Technology

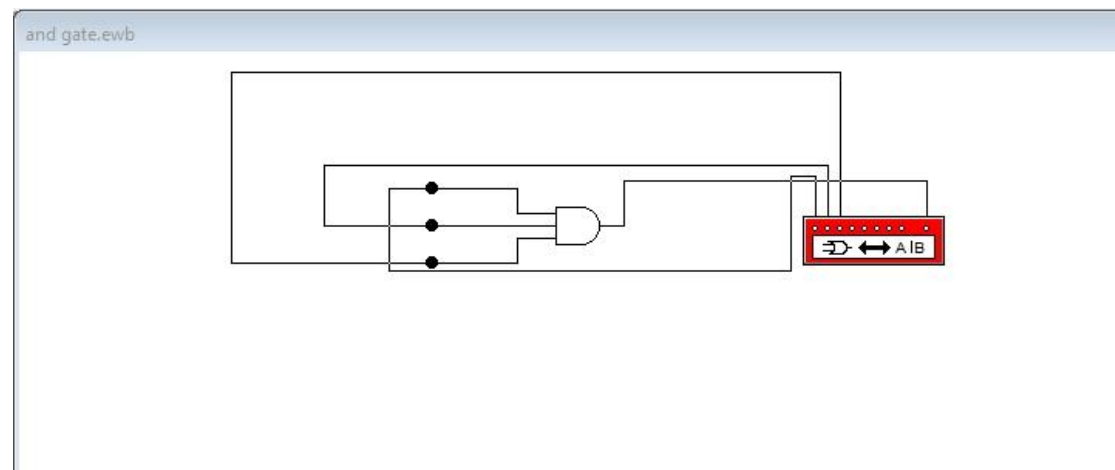
Subject:-

DLD

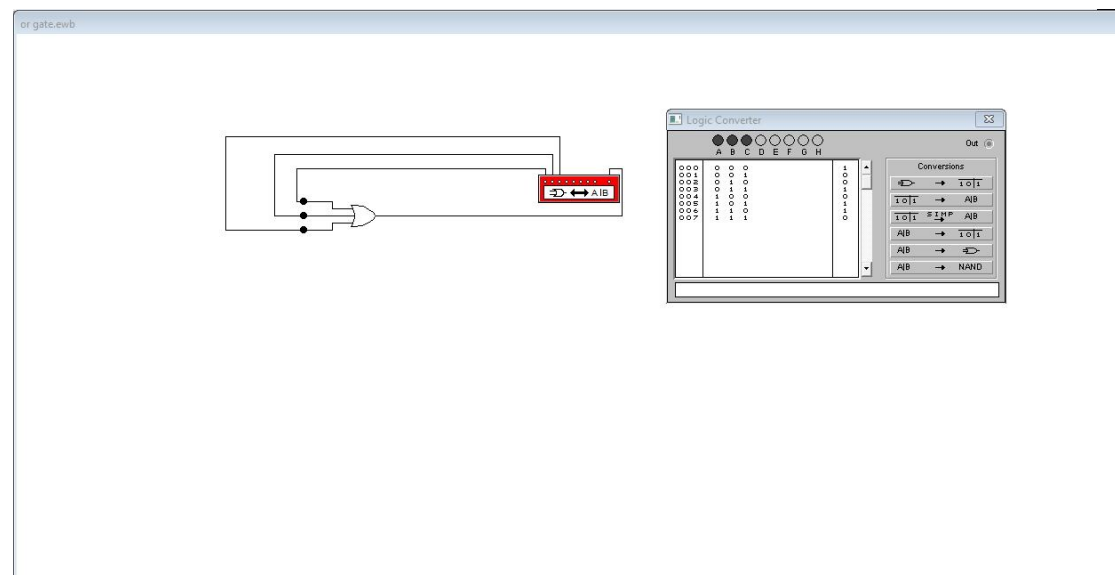
Semester:-

2nd Semester

AND Gate:

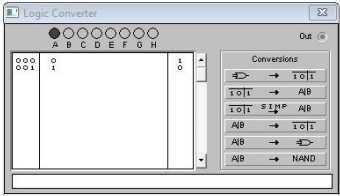



OR Gate:



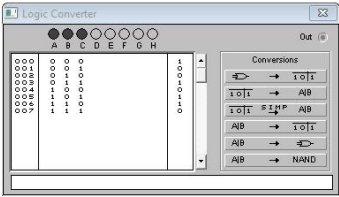
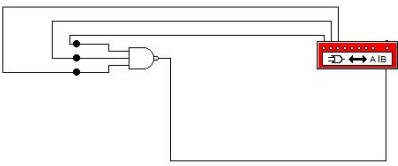
NOT Gate:

NOT Gate.EWB



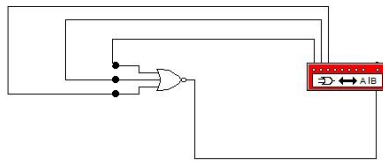
NAND Gate:

nand gate.ewb



NOR Gate:

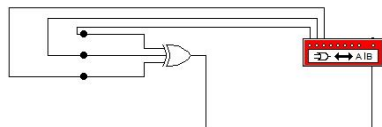
nor gate.ewb



Logic Converter							
A B C D E F G H							
000	0	0	0	0	0	0	0
001	0	0	0	1	0	0	0
002	0	0	0	1	1	0	0
003	0	0	0	1	1	1	0
004	0	0	1	0	0	0	0
005	0	0	1	0	0	1	0
006	0	0	1	0	1	0	0
007	0	0	1	1	0	0	0
008	0	1	0	0	0	0	0
009	0	1	0	0	0	1	0
010	0	1	0	1	0	0	0
011	0	1	0	1	0	1	0
012	0	1	1	0	0	0	0
013	0	1	1	0	0	1	0
014	0	1	1	1	0	0	0
015	0	1	1	1	0	1	0
016	1	0	0	0	0	0	0
017	1	0	0	0	0	1	0
018	1	0	0	1	0	0	0
019	1	0	0	1	0	1	0
020	1	0	1	0	0	0	0
021	1	0	1	0	0	1	0
022	1	0	1	1	0	0	0
023	1	0	1	1	0	1	0
024	1	1	0	0	0	0	0
025	1	1	0	0	0	1	0
026	1	1	0	1	0	0	0
027	1	1	0	1	0	1	0
028	1	1	1	0	0	0	0
029	1	1	1	0	0	1	0
030	1	1	1	1	0	0	0
031	1	1	1	1	0	1	0

XOR Gate:

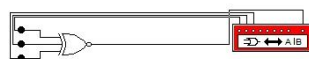
xor gate.ewb



Logic Converter							
A B C D E F G H							
000	0	0	0	0	0	0	0
001	0	0	0	1	0	0	0
002	0	0	0	1	1	0	0
003	0	0	0	1	1	1	0
004	0	0	1	0	0	0	0
005	0	0	1	0	0	1	0
006	0	0	1	0	1	0	0
007	0	0	1	1	0	0	0
008	0	1	0	0	0	0	0
009	0	1	0	0	0	1	0
010	0	1	0	1	0	0	0
011	0	1	0	1	0	1	0
012	0	1	1	0	0	0	0
013	0	1	1	0	0	1	0
014	0	1	1	1	0	0	0
015	0	1	1	1	0	1	0
016	1	0	0	0	0	0	0
017	1	0	0	0	0	1	0
018	1	0	0	1	0	0	0
019	1	0	0	1	0	1	0
020	1	0	1	0	0	0	0
021	1	0	1	0	0	1	0
022	1	0	1	1	0	0	0
023	1	0	1	1	0	1	0
024	1	1	0	0	0	0	0
025	1	1	0	0	0	1	0
026	1	1	0	1	0	0	0
027	1	1	0	1	0	1	0
028	1	1	1	0	0	0	0
029	1	1	1	0	0	1	0
030	1	1	1	1	0	0	0
031	1	1	1	1	0	1	0

XNOR Gate:

xnor gate.ewb



Logic Converter							
A B C D E F G H							
000	0	0	0	0	0	0	0
001	0	0	0	1	0	0	0
002	0	0	0	1	1	0	0
003	0	0	0	1	1	1	0
004	0	0	1	0	0	0	0
005	0	0	1	0	0	1	0
006	0	0	1	0	1	0	0
007	0	0	1	1	0	0	0
008	0	1	0	0	0	0	0
009	0	1	0	0	0	1	0
010	0	1	0	1	0	0	0
011	0	1	0	1	0	1	0
012	0	1	1	0	0	0	0
013	0	1	1	0	0	1	0
014	0	1	1	1	0	0	0
015	0	1	1	1	0	1	0
016	1	0	0	0	0	0	0
017	1	0	0	0	0	1	0
018	1	0	0	1	0	0	0
019	1	0	0	1	0	1	0
020	1	0	1	0	0	0	0
021	1	0	1	0	0	1	0
022	1	0	1	1	0	0	0
023	1	0	1	1	0	1	0
024	1	1	0	0	0	0	0
025	1	1	0	0	0	1	0
026	1	1	0	1	0	0	0
027	1	1	0	1	0	1	0
028	1	1	1	0	0	0	0
029	1	1	1	0	0	1	0
030	1	1	1	1	0	0	0
031	1	1	1	1	0	1	0