# Experiments Conducted:

* SPART:
  + Fully filled TX and RX buffer, and read till empty
  + Filled TX, Changed Baud Rate, and read RX till empty
  + Changed Baud Rate and fully filled TX and RX buffer, and read till empty
  + Unselected SPART and read/write all the registers, write to TX buffer and Read from RX buffer.
* Queue:
  + Fully fill the queue, try to send more
  + Read 1 value then write another
  + Empty completely and then keep trying to read
* MiniLab1:
  + High level tb so we could trace our program in modelsim
  + Sent dummy data in through the RX line and forced a CR into the RX line during run

# Problems Encountered and Solutions:

* Incorrectly waiting for the TX buffer to empty
  + We treated it as if it was checking how many filled entries
  + Changed it to check if all entries were empty
* Weird corruption of the “Hello …” when reset with the board
  + Include a null terminator when storing user input in memory
* Not printing the name at all
  + We used the wrong type of branch (bneq)
  + Changed it to beq
* Not fully qualified iocs\_n signal. Chip was selected even when memory map read/write was not asserted. This resulted in read of RX buffer even when there was a flush which changed the state.