Madushan Nishantha

Curriculum Vitae

Contact Info

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About Me

I'm a technology enthusiasts who always seeks for new and interesting technological advancements, which lets me to get in touch with new technologies as soon as they are available.

I love teamwork and I can always adapt to such a environment at ease.

My career goal is to work in the cyber security sector which I think will take the best out of me. I also have experiences in Mobile and Computer software development.

Education

2011–Now Bachelor of Engineering (B.Sc.), University of Peradeniya, Sri Lanka.

Computer Engineering, Undergraduate

Experience

10/2015– System Administrator at Kadira Inc. – DevOps and System reliability Engineering

Present

10/2014 – Short term technical training at Atrenta Lanka (PVT) LTD – Software Engineering

03/2015 Intern at GUI team(Qt)

Technical skills

Cloud Management (AWS, Google Cloud)

Software Defined Networking (OpenFlow Controllers/Switches)

Programming skills

Advanced C/C++, Java, Python, JavaScript

Intermediate Haskell, Common Lisp, Antlr, Perl, Assembly, Rust

Kadira Projects

Improving Organizational Security - with Arunoda Susiripala

Designing and implementing a system to centralize backend (aws/gcloud/etc...) authentication methods (ssh/passwords/etc...) used by Kadira's various projects.

Meteor Up – with Arunoda Susiripala, Muhammed Thanish

The production quality meteor deployment tool, Meteor Up, which enables developers to deploy their meteor applications to custom Linux servers. A maintainer of this open source project.

Personal Projects

Porting FirefoxOS (B2G) to HTC Desire HD

Solo project of Porting FirefoxOS aka Boot to Gecko (B2G), to HTC Desire HD smartphone

Academic Projects

SDN testbed for research and education – University Final Year Project

Low cost Software defined networking testbed built using Raspberry Pi devices for researches and undergraduates.

Contributing to Firefox for Android – fenek

Few minor code cleanups for Firefox for android

User level thread library - C

Designing and implementing of context switching library for user level C programs.

Virtual Memory Management System - OS161

Designing and implementing a Virtual Memory Management System for educational operating system OS161

Seven Segment Display Decoder for hexadecimal numbers – FPGA

Designing and implementing a decoder for displaying hexadecimal numbers in seven segment displays on Altera DE2 FPGA development board using VHDL.

Interests

- Operating Systems
- Virtualization
- Hiking

- Embedded Designing
- Programming Languages
- Creative Writing

Referees available upon request