Learn-in-depth

Unit3 Lesson2

assignment

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-Screen of source file :

1-app.c

app.c | uart.c | uart.h | startup.s | linker_script.ld

```
1  #include "uart.h"
2
3  unsigned char stringBuffer[100]="learn-in-depth : Magdy Adel ";
4  void main(void)
5  {
6      Uart_SendString(stringBuffer);
7  }
```

2-uart.h

app.c | uart.c | uart.h | startup.s | linker_script.ld

```
1  #ifndef _UART_H_
2  #define _UART_H_
3
4  void Uart_SendString(unsigned char * p_tx_string);
5
6  #endif
```

3-uart.c

app.c | uart.c | uart.h | startup.s | linker_script.ld

```
1  #include "uart.h"
2
3  #define UART0DR *((volatile unsigned int *) ((unsigned int*)0x101f1000))
4
5  void Uart_SendString(unsigned char * p_tx_string)
6  {
7      while(*p_tx_string!='\0')
8      {
9          UART0DR = (unsigned int)(*p_tx_string);
10         p_tx_string++;
11     }
12 }
```

4-starup.s

app.c | uart.c | uart.h | startup.s | linker_script.ld

```
1
2   .global reset
3   reset:
4       ldr sp, =stack_top
5       bl main
6   stop: b stop
```

5-linker_script.ld

app.c | uart.c | uart.h | startup.s | linker_script.ld

```
1   ENTRY(reset)
2
3   MEMORY
4   {
5       Mem (rwx) :ORIGIN = 0x00000000,LENGTH =64M
6   }
7
8   SECTIONS
9   {
10      .=0x10000;
11      .startup:
12      {
13          startup.o(.text)
14      }>Mem
15      .text:
16      {
17          *(.text) *(.rodata)
18      }>Mem
19      .data:
20      {
21          *(.data)
22      }>Mem
23      .bss:
24      {
25          *(.bss) *(COMMON)
26      }>Mem
27      . = . + 0x1000 ;
28      stack_top= . ;
29  }
```

-Object files:

1-app.o

```
 (main)
$ arm-none-eabi-objdump -h app.o

app.o:     file format elf32-littlearm

Sections:
Idx Name          Size      VMA       LMA       File off  Algn
  0 .text         00000018  00000000  00000000  00000034  2**2
                  CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data         00000064  00000000  00000000  0000004c  2**2
                  CONTENTS, ALLOC, LOAD, DATA
  2 .bss          00000000  00000000  00000000  000000b0  2**0
                  ALLOC
  3 .comment      00000012  00000000  00000000  000000b0  2**0
                  CONTENTS, READONLY
  4 .ARM.attributes 00000032  00000000  00000000  000000c2  2**0
                  CONTENTS, READONLY
```

2-uart.o

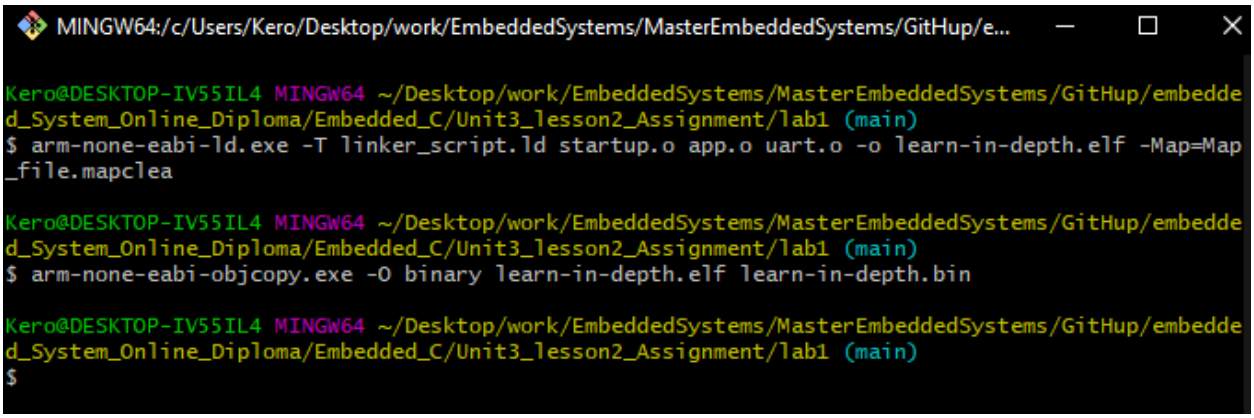
```
 (main)
$ arm-none-eabi-objdump -h uart.o

uart.o:     file format elf32-littlearm

Sections:
Idx Name          Size      VMA       LMA       File off  Algn
  0 .text         00000050  00000000  00000000  00000034  2**2
                  CONTENTS, ALLOC, LOAD, READONLY, CODE
  1 .data         00000000  00000000  00000000  00000084  2**0
                  CONTENTS, ALLOC, LOAD, DATA
  2 .bss          00000000  00000000  00000000  00000084  2**0
                  ALLOC
  3 .comment      00000012  00000000  00000000  00000084  2**0
                  CONTENTS, READONLY
  4 .ARM.attributes 00000032  00000000  00000000  00000096  2**0
                  CONTENTS, READONLY

Kero@DESKTOP-IV55IL4 MINGW64 ~/Desktop/work/EmbeddedSystems/MasterEr
```

3-startup.o

```
MINGW64:/c/Users/Kero/Desktop/work/EmbeddedSystems/MasterEmbedd...
ing not done

Kero@DESKTOP-IV55IL4 MINGW64 ~/Desktop/work/EmbeddedSystems/MasterEmbedde
s/GitHup/embedded_System_Online_Diploma/Embedded_C/Unit3_lesson2_Assignme
 (main)
$ arm-none-eabi-objdump -h startup.o

startup.o:     file format elf32-littlearm

Sections:
Idx Name          Size      VMA       LMA       File off  Algn
  0 .text         0000000c  00000000  00000000  00000034  2**2
                  CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data         00000000  00000000  00000000  00000040  2**0
                  CONTENTS, ALLOC, LOAD, DATA
  2 .bss          00000000  00000000  00000000  00000040  2**0
                  ALLOC
  3 .ARM.attributes 00000022  00000000  00000000  00000040  2**0
                  CONTENTS, READONLY

Kero@DESKTOP-IV55IL4 MINGW64 ~/Desktop/work/EmbeddedSystems/MasterEmbedde
```

Invoke the linker and pass the linker script:

Generate binary file:



-Output