芯片设计自动化与智能优化
Optimization and Machine Learning in VLSI Design Automation (VLSI CAD)
Homework
Timing Analysis

1. (Programming) Given a RC-tree, write a program to compute the Elmore delay at each node.
2. (Programming) Given a timing graph for *combinational logic* and the delay at each timing arc, write a program to compute the slack at each node.
3. (Writing) Write down the pseudo-code of your algorithms and analyze the time and space complexity in Problems 1-2.