SCHEMATIC CONTENT PAGE PAGE 0 COVER PAGE 1 PAGE1 SYSTEM DIAGRAM 2 PAGE2 DC5V to 3.3V,2.5V,1.8V,1.2V,1.0V 3 PAGE3 GW1N-LV9CLQ144C7I6 4 PAGE 4 Mic IN 5 PAGE5 USB2.0 6 PAGE 6 SPDIF 7 PAGE7 RAC, Horn 8 PAGE8 JTAG, GPIO 9 DK_UAC2_GW1N-LV9LQ144C7l6 Document Number COVER PAGE Date: Friday, November 04, 2022 Sheet 1 of 9

















