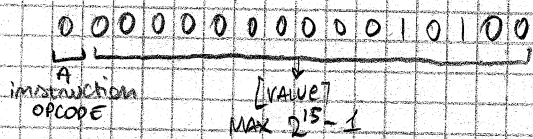


PROJECT ④ ML

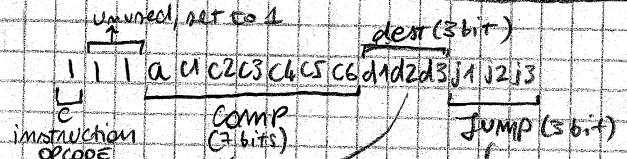
① INSTRUCTION

② [VALUE]



② INSTRUCTION

[optional] = [COMPUTATION]; [JUMP]



COMP (7 bit)

DEST (3 bit)

JUMP (3 bit)

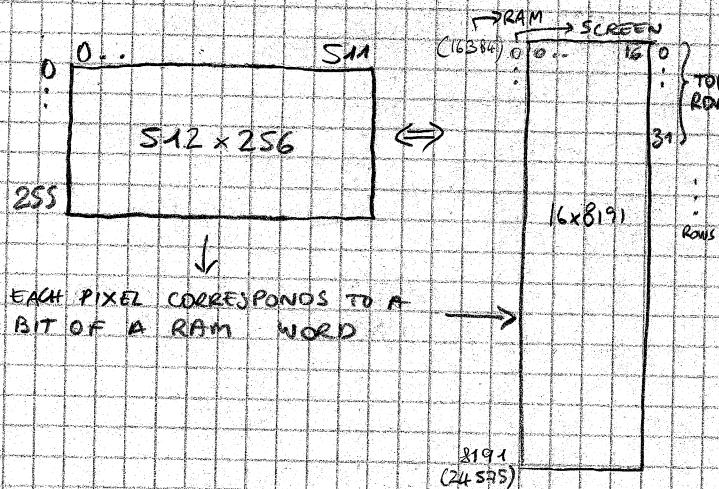
A=0	A=1	C ₁	C ₂	C ₃	C ₄	C ₅	C ₆	DEST	d ₁	d ₂	d ₃	Comment	JUMP	J ₁	J ₂	J ₃	Comment
0		1	0		0		0	NULL	0	0	0	NO VALUE STORED	NULL	0	0	0	NO JUMP
+1		1	1		1		1	M	0	0	1	TO M	JGT	0	0	1	IF COMP > 0
-1		1	1		0		0	D	0	1	0	TO D	JEQ	0	1	0	" " = 0
0		0	0		1		0	MD	0	1	1	TO BOTH M,D	JGE	0	1	1	" " ≥ 0
A	M	0	1		0		0	A	1	0	0	TO A	JLT	1	0	0	" " < 0
!D		0	0		1		1	AM	1	0	1	TO BOTH A,M	JNE	1	0	1	" " ≠ 0
!A	!M	1	1		0		0	AD	1	1	0	" " A,D	JLE	1	1	0	" " ≤ 0
-D		0	0		1		1	AMD	1	1	1	TO A,M,D	JMP	1	1	1	UNCONDITIONAL
-A	-M	1	1		0		1										
D+1		0	1		1		1										
A+1	M+1	1	1		0		1										
D-1		0	0		1		0										
A-1	M-1	1	1		0		0										
D+A	D+M	0	0		0		0										
D-A	D-M	0	1		0		1										
A-D	M-D	0	0		0		1										
D&A	D&M	0	0		0		0										
D!A	D!M	0	1		0		1										

CPU REGISTERS:

- ① → "Address register" its value is the current "pointed" RAM Register (A INSTRUCTION OR A=... SETS)
- ② → Its value is the CONTENT of the CURRENTLY "pointed" RAM Register
- ③ → General purpose register

SCREEN = 16384 (... 24575)

KBD = 24576



IN ORDER TO CHANGE PIXEL X (COL, ROW)
I HAVE TO MODIFY RAM[SCREEN+i] = SCREEN[i]

$$i = 32 * \text{ROW} + \frac{\text{COL}}{16}$$

AND INSIDE THIS RAM WORD THE BIT N°

$$\text{BIT N}^\circ = \text{COL} \% 16$$

Contains the scancode of the key currently being pressed. (SUBSET of ASCII 1)

Key	Scancode	Key	Scancode	Key	Scancode	Key	Scancode
0	48	A a	65 99	[space]	32	newLine	28
1	49	B b	66 98	"	33	backspace	129
2	50	C c	67 97	#	34	←	130
3	51	D d	68 96	\$	35	↑	131
4	52	E e	69 95	%	36	→	132
5	53	F f	70 94	&	37	↓	133
6	54	G g	71 93	'	38	home	134
7	55	H h	72 92	(39	end	135
8	56	I i	73 91)	40	pg ↑	136
9	57	J j	74 90	*	41	pg ↓	137
...	...	K k	75 89	+	42	insert	138
...	...	L l	76 88	=	43	del	139
...	...	M m	77 87	-	44	esc	40
...	...	N n	78 86	.	45		
...	...	O o	79 85	/	46		
...	...	P p	80 84		47		
...	...	Q q	81 83				
...	...	R r	82 82				
...	...	S s	83 81				
...	...	T t	84 80				
...	...	U u	85 79				
...	...	V v	86 78				
...	...	W w	87 77				
...	...	X x	88 76				
...	...	Y y	89 75				
...	...	Z z	90 74				

OTHER ACCEPTED SYMBOLS

SP	0
LCL	1
ARG	2
THIS	3
THAT	4

VIRTUAL REGISTERS

R0	0
R15	15