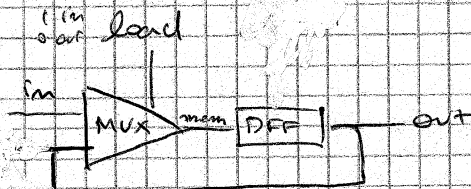
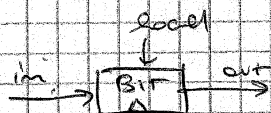


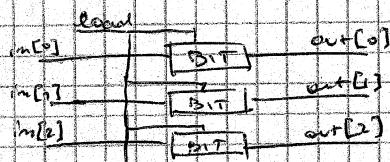
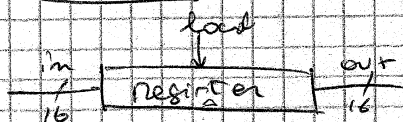
# PROJECT 3 DIAGRAMS

## BIT

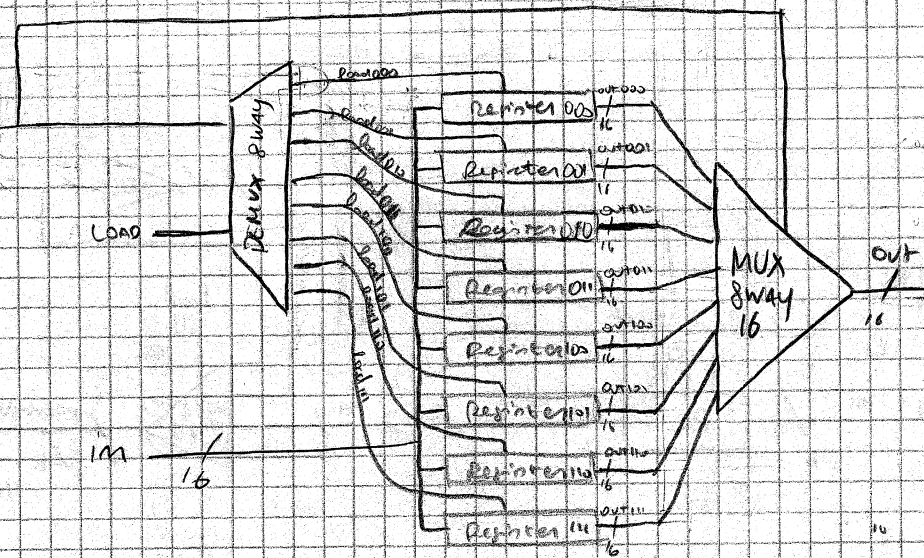
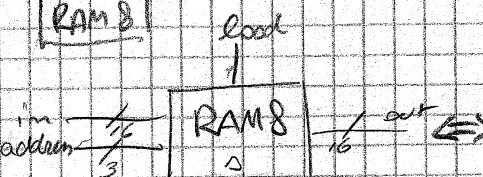
if load=1  
 $out[t+1] = in[t]$   
 else  $out[t+1] = out[t]$



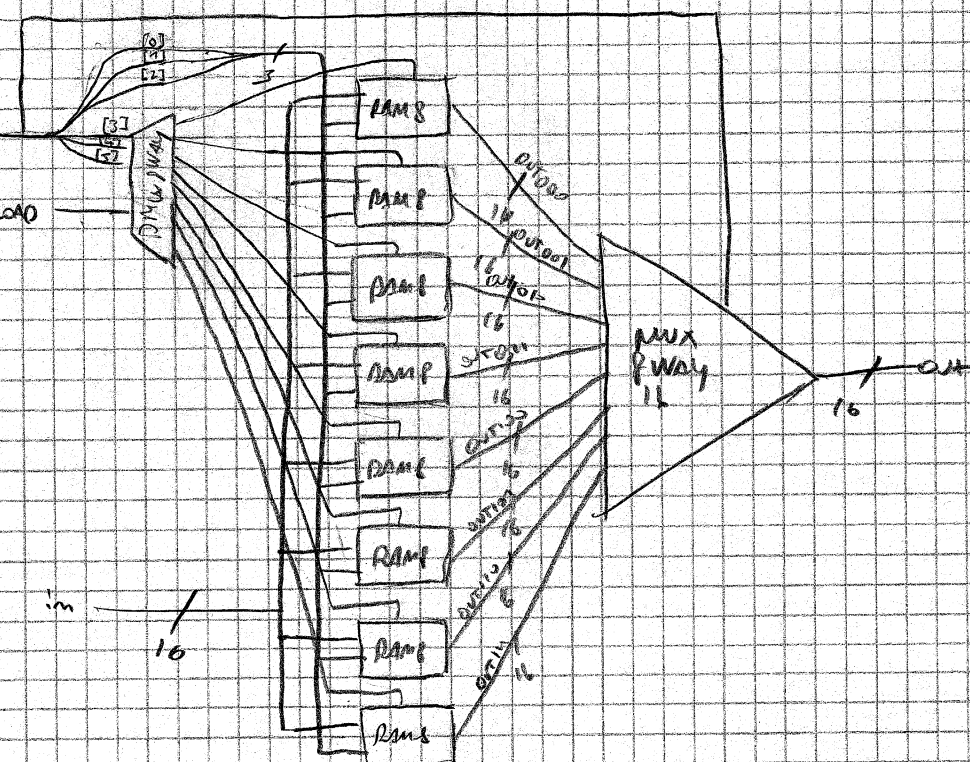
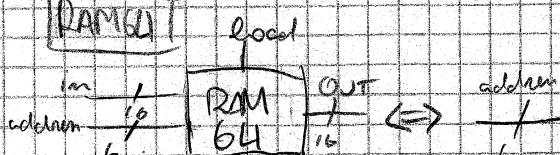
## REGISTER



## RAM8



## RAM64



## RAM32

See RAM64 substituting RAM8 with RAM64

## RAM4K

" " " " " RAM32

## RAM16K

" " " " " RAM4K (ONLY FOUR MODULES BECAUSE

MUX8WAY is substituted by MUX4WAY AND MUX16WAY by MUX4WAY16)

PC

