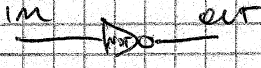


DIAGRAMS PROJECT ①

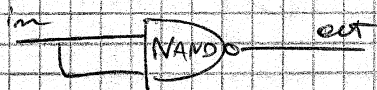
1/6

NOT

in	out
1	0
0	1



\Leftrightarrow

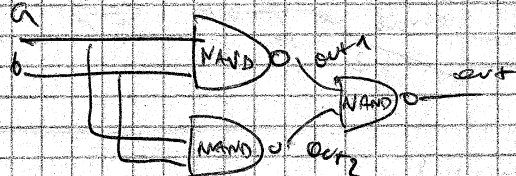


AND

a	b	out
0	0	0
0	1	0
1	0	0
1	1	1



\Leftrightarrow



OR

a	b	out
0	0	0
0	1	1
1	0	1
1	1	1



\Leftrightarrow

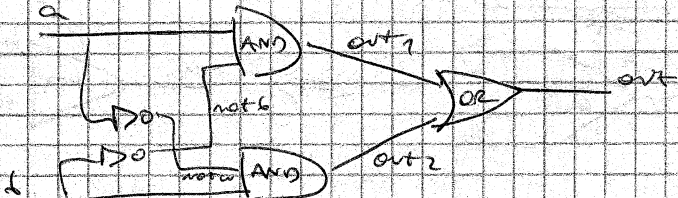


XOR

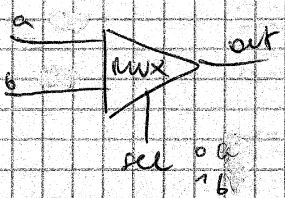
a	b	out
0	0	0
0	1	1
1	0	1
1	1	0



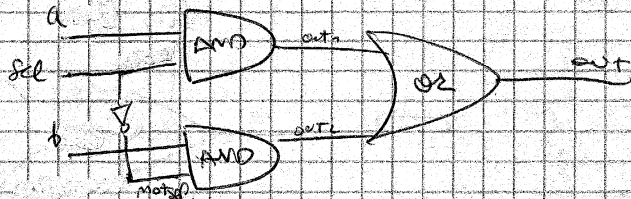
\Leftrightarrow



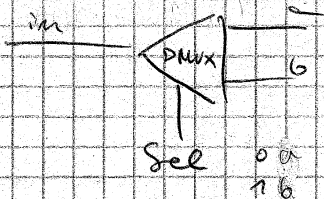
MUX



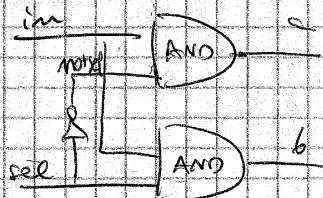
\Leftrightarrow



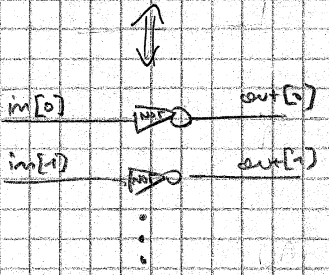
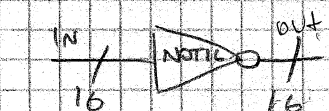
DMUX



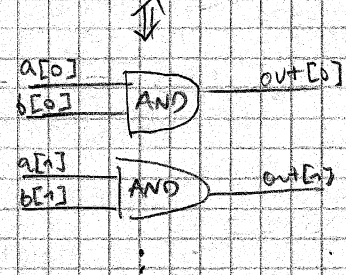
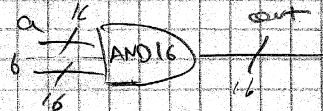
\Leftrightarrow



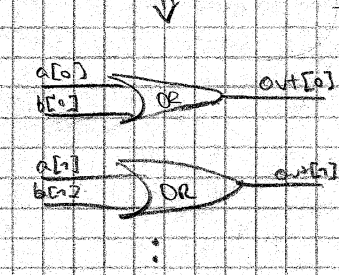
NOT 16



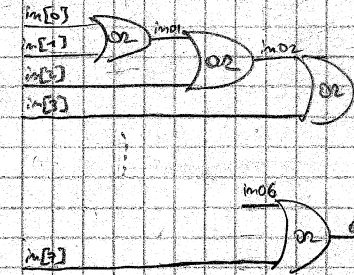
AND 16



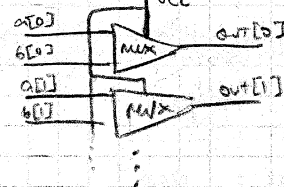
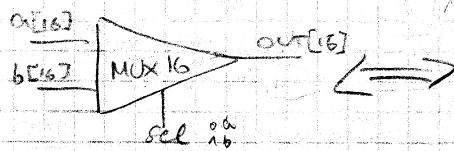
OR 16



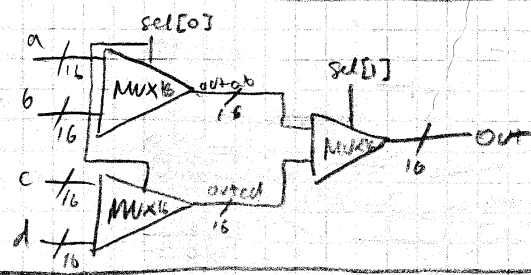
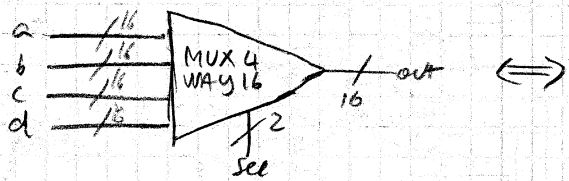
OR 8WAY



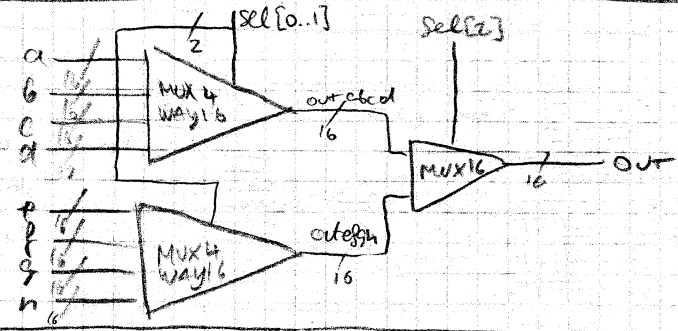
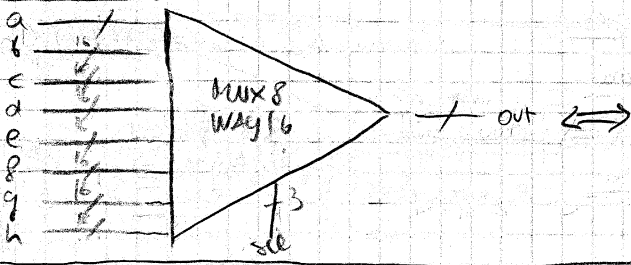
MUX 16



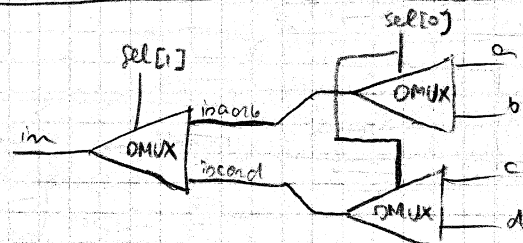
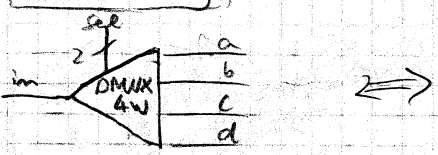
MUX 4WAY 16



MUX 8WAY 16



DMUX 4WAY



DMUX 8WAY

