

# Param Mahajan

Electronics & Telecommunication Engineering Student

Contact No.: - +91 9834587130

E-mail id:- [mahajanparam361@gmail.com](mailto:mahajanparam361@gmail.com)

Portfolio Website:- <https://mahajanparam-dev.github.io/param-mahajan-portfolio/>

Linked Profile:- <https://in.linkedin.com/in/param-mahajan-386982383>



---

## Professional Summary

Results-driven Electronics and Telecommunication Engineering student specializing in Embedded Systems, VLSI design, and Verilog HDL. Strong foundation in digital architecture and secure system design. Experienced in open-source ecosystems, Kubernetes fundamentals, and cybersecurity principles. Focused on building scalable, silicon-ready, and security-conscious engineering solutions.

## Technical Skills

- Embedded Systems
- VLSI Design
- Verilog HDL
- Digital Electronics
- Linux Fundamentals
- Kubernetes Basics
- AI-assisted DevOps Workflows
- npm Security Awareness
- Cybersecurity Fundamentals

## Recent Experience & Workshops

- Attended Red Hat Open Source Developers Conference at MIT-WPU.
- Integrated Kubectl AI to manage and optimize Kubernetes clusters in hands-on workshop.
- Analyzed attacker methodologies and detected npm package vulnerabilities in cybersecurity workshop.
- Applied secure software development lifecycle (SDLC) best practices in practical sessions.

## Projects

- Project Title 1 – Implemented technical solution demonstrating measurable impact.
- Project Title 2 – Designed and validated architecture using industry-standard tools.
- Project Title 3 – Optimized performance, security, or hardware efficiency metrics.

## Education

Bachelor of Engineering (B.E.) – Electronics & Telecommunication Engineering

Relevant Coursework: Digital Systems, Microprocessors, VLSI Design, Embedded Systems

## **Certifications / Workshops**

- Red Hat Open Source Developers Conference Participation
- Kubernetes & AI Integration Workshop
- Cybersecurity & Secure Software Development Workshop