

**American International University-Bangladesh**  
**Course: CSC 1101: Introduction to Computer Studies**

*Lab Exam: FINAL-TERM*

*Section: B7*

*Semester: Spring 24-25*

*Time: 30 mins*

*Total Marks: 15*

<b>Name:</b>	<b>ID:</b>	<b>Marks:</b>
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**CO4:** Explain a complex engineering problem using Logic gates and Boolean Algebra to make a solution.

<b>Evaluation Criteria</b>	<b>Evaluation Definition</b>	<b>Marks</b>	<b>T (15)</b>
Knowledge and proper use of Logic gates and Boolean algebra	Proper knowledge and concept of relevant topics are reflected.		
Mathematical Knowledge	Understanding mathematical terminologies and their application in the Computer Science area.		
Solution Design	Design the equivalent logic circuit.		

**Problem Scenario:**  $Y = (A + AB) \cdot (B + BC) \cdot (C + AB)$

From the above scenario, form the Boolean expression for the vault logic output Y.

- Draw the logic circuit using any one universal gate.
- Clearly label all gates and inputs in the diagram.

**Solution:**