



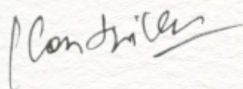
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**Mahendrarvarman R S**

For presenting the paper “ PCB Design of low noise Class D Amplifier ”  
in NCRTGT 2k24 which run by Electrical Drives and Automation Lab & Department of Electrical and Electronics  
Engineering on 12.04.2024



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