# RESUME

Flat No: 304,
ANCO HIEGHT Apartments,
Bandlaguda Jagir, RR Dist
Hyderabad.
**Email:** mahesh4a4@gmail.com
**Mobile:** 9912438444
          8897070499

## MAHESH ENUMULA

**OBJECTIVE:** To work for a professional organization**,** where I can use my Technical & Personal skills and expertise to help the organization to achieve its mission as well as my skills.

**Awards:**

1.Recieved **India Independence Awards-2019 for Young Scientist from** International Association of Research and Developed Organization (IARDO) on 18/08/2019 at Goa,India.

**Patents:**

1. Title of the Invention: A SYSTEM AND A METHOD FOR IMAGE FORGERY DETECTION, Application No : 201941036726, Date:12.09.2019.

2. Title of the Invention: EXCESS FOOD MANAGEMENT SYSTEM USING ANDROID/IOS PHONE APPLICATION, Application No : 201941041471, Date:14.10.2019.

**Ongoing Research Projects:**

1.Implementation of life Saver mechanism in the Smart Phones.

2.Maps based Social networking.

**TOTAL EXPIRIENCE**: 9 Years of Experience in Academic Research.

1.Working as an Asst Professor in **JBREC**, Hyderabad from July 2011 to till Date.
2.Worked as an Asst professor in **YPRCET**, Medak from November 2010 to July2011.

**RATIFICATION:** On 7th January 2013 Ratified as Assistant professor by JNTUH.

**EDUCATIONAL QUALIFICATIONS:**
1. Doing Research from KL University as a part time PhD scholar in the area of Digital image processing.
2. Completed M.Tech from Lords Institute of Engineering and Technology Affiliated to JNTU Hyderabad in the year of 2010 with specialization of Embedded systems and VLSI Design.
3. Completed B.Tech from P.V.P. Siddhartha Institute of Technology, Vijayawada Affiliated to JNTU in the year of 2008 in Electronics and Communication Engineering**.**

**TRAINING & CERTIFICATIONS:**
1.Completed PG Diploma in Embedded systems Design and Development from Kernel Masters, Hi-tech city, Hyderabad.
2. Trained from Vector India, Hyderabad in Embedded systems.

**TECHNICAL SKILLS:**
C & C++
Shell Programming
Python
Linux Basics
Embedded System Basics
Micro Controller Programming
RTOS
Linux System Programming
Linux Device Drivers
Internet of Things

**RESEARCH PROGRESS:**
   **Research Topic: An Implementation of image security for IOT.**
   **Research progress:** 1. completed course work.
                     2. Passed Pre-PhD Exams.
                     3. Attended Three Research progress reviews.
                     4. Published Two UGC approved Research papers in which one is
SCOPUS indexed and one is Thomson Reuters indexed with affiliation of University.
                     5.Presented research paper in international conference.

**ACADAMIC PROJECTS:**
1. INDUSTRIAL AUTOMATION using cell phone With Voice feed back (M.TECH)
2. RISC PROCESSOR USING VHDL(B.TECH)

**MINI PROJECTS**: 1.Micro Flasher
                 2.Earth Quake detector

**MEMBERSHIPS IN INSTITUTIONS:**

1. SENIOR MEMBER OF MEMBER OF INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS **(IEEE)** AND CASS.
**2.** LIFE MEMBER OF INDIAN SOCIETY FOR TECHNICAL EDUCATION **(ISTE)**
**3.** LIFE MEMBER OF INSTITUTION OF ELECTRONICS AND TELECOMMUNICATION ENGINEERS  **(IETE)**
4. LIFE MEMBER OF IAENG.
5. LIFE MEMBER OF IRED.

**Subjects Taught / Labs Handled:**

| Ac.Year | SEM | SUBJECT/B.Tech | LABS | SUBJECT/MTECH |
|---------|-----|----------------|------|---------------|
| 2011-12 | I | EDC | EDC LAB | CMOS AMS, HS CO DESIGN |
| | II | STLD | EC LAB(MULTISIM) | |
| 2012-13 | I | MPI | MPI LAB | |
| | II | ES | ES LAB | |
| 2013-14 | I | PTSP | EDC LAB | |
| | II | VLSI | EC LAB(MULTISIM) | |
| 2014-15 | I | PTSP | BS LAB | IMGAE AND VIDEO PROCESSING |
| | II | DD VERILOG | PDC LAB | WIRELESS COMMUNICATIONS |
| 2015-16 | I | PTSP | BS LAB | |
| | II | DD VERILOG, GS | EC PC LAB | |
| 2016-17 | I | PTSP | BS LAB | |
| | II | VLSI, GS | EC PC LAB | |
| 2017-18 | I | SSP | BS LAB | |
| | II | STLD, GS | CO LAB | |
| 2018-19 | I | DLD | BS LAB | |
| | II | PCCN | CO LAB | |

**Publications:**

| | TITLE | CITED BY | YEAR |
|---|---|---|---|
| ☐ | A Novel Method of Scale-Invariant Feature Transform Based Image Forgery Detection<br>E Mahesh, S Maloji, DN Rao | 1 | 2017 |
| ☐ | Security implementation for the Image Forgery Detection: A review<br>Mr.Mahesh Enumula Dr.D.N.Rao Dr.M.Suman<br>International Journal of Engineering Research in Electronics and ... | | 2017 |
| ☐ | A Study on security implementation for the prevention of Image Forgery<br>PDMS Mr. Mahesh Enumula, Prof.Dr.D.N.Rao<br>International Journal of Advanced Scientific Technologies in Engineering and ... | | 2017 |
| ☐ | Nonlinear Coordinated Steering and Braking Control of Vision-Based Autonomous Vehicles in Emergency Obstacle Avoidance<br>MSR K.Prashanth , E.Mahesh<br>International Journal of Engineering Science and Computing 7 (8), 14428-14431 | | 2017 |
| ☐ | DESIGN OF SRAM BIT CELL WITH OPTIMIZED REGISTER FILE<br>EM A.Murali , P.Thirapaiah<br>INTERNATIONAL JOURNAL OF ELECTRICAL ELECTRONICS AND COMMUNICATION 15 (25 ... | | 2015 |
| ☐ | AN IMPLEMENTATION OF CYBER SECURITY FOR THE PROTECTION OF WOMEN<br>PDMS Mr.Enumula Mahesh<br>International Journal of Latest Trends in Engineering and Technology 7 (4 ... | | 2015 |
| ☐ | Design of low voltage low power comparator using cmos technology<br>EM VADDE BHAGYA LAKSHMI<br>International Journal of Computer Science information and Engg 3 (4), 1-7 | | 2014 |
| ☐ | Low Power Analysis of Double Tail Comparator for ADC by Using Hspice<br>AMEMNV Babu<br>International Journal & Magazine of Engineering, Technology, Management and ... | | 2014 |
| ☐ | The Cave: A Dynamic Interface for Virtual Reality<br>EMM venkatesh<br>https://findingsofmahesh.blogspot.com/ | | 2006 |

| S.No | EVENT | TITLE | DATES | VENUE |
|---|---|---|---|---|
| 1 | FDP | TEACHING METHODOLOGY | 12TH AND 13TH JAN 2012 | JBIET |
| 2 | WORKSHOP | HIGH IMPACT TEACHING SKILL | 14TH AND 15TH FEBRUARY 2012 | JBREC |
| 3 | WORKSHOP | MISSION 10X | 14TH TO 18TH FEBRUARY 2012 | JBREC |
| 4 | WORKSHOP | AKASH FOR EDUCATION BY ISTE AND MHRD | 10TH AND 11TH NOVEMBER 2012 | JBREC |
| 5 | FDP | EFFECTIVE TEACHING METHOLOGY | 20TH AND 21ST JULY 2013 | JBIET |
| 6 | FDP | EFFECTIVE METHODS OF TEACHING | 14TH DEC 2013 | JBREC |
| 7 | WORKSHOP | DATBASE MANAGEMENT SYSTEMS | 21ST TO 31ST MAY 2013 | JBREC |
| 8 | WORKSHOP | ANALOG ELECTRONICS | 4TH TO 14TH MAY 2013 | JBREC |
| 9 | WORKSHOP | MAT LAB AND SIMULINK FOR ENGINEERING EDUCATION | 26TH JULY 2013 | JBREC |
| 10 | WORKSHOP | SIGNALS AND SYSTEMS | 2ND TO 12TH JANUARY 2014 | JBREC |
| 11 | WORKSHOP | MAT LAB AND SIMULINK FOR ENGINEERING EDUCATION | 11TH AUG 2014 | HYDERABAD |
| 12 | WORKSHOP | GENDER SENSITIZATION | 12TH FEB 2016 | JNTUH,HYDERABAD |
| 13 | WORKSHOP | EMBEDDED SYSTEMS | 23RD AND 24TH DECEMBER 2016 | JBREC |
| 14 | WORKSHOP | ADVANCE VLSI DESIGN AND SIMULATION | 6TH AND 7TH JAN 2017 | JBREC |
| 15 | INTERANATIONAL CONERENCE | INTERNATIONAL CONFERENCE ON APPLIED SCIENCES,ENGINEERING,TECHNOLOGY AND MANAGEMENT BY IFERP | 2ND TO 3RD NOVEMBER 2017 | DRKIST |
| 16 | INTERANATIONAL CONERENCE | 1ST INTERNATIONAL CONFERENCE IN ADVANCEMENTS AND INNOVATIONS IN ENGINEERING,TECHNOLOGY AND MANAGEMENT | 28TH TO 29TH DECEMBER 2017 | JBREC |
| 17 | WORKSHOP | INTERNET OF THINGS(IOT) BY E&ICT ACADEMY NIT WARANGAL | 9TH TO 14TH OCTOBER 2017 | JBREC |
| 18 | WORKSHOP | REAL TIME EMBEDDED SYSTEMS AND IOT,ITS APPLICATIONS BY E&ICT ACADEMY NIT WARANGAL | 9TH TO 14TH APRIL 2018 | JBREC |
| 19 | WORKSHOP | PARTIAL RECONFIGURATION FLOW USING XILINX TOOLS | 19TH TO 23RD SEPTEMBER 2017 | BVRIT,NARSAPUR |
| 20 | SEMINAR | SMART POWER OPTIMIZATION SYSTEM WITH STUDENTS ATTENDANCE MESSAGING FOR COLLEGES | 14TH JULY 2017 | JBREC |
| 21 | SEMINAR | BIT ERROR RATE MEASUREMENT ADD ON EXPERIMENT IN DC LAB | 19TH JULY 2017 | JBREC |

| S.No | EVENT | TITLE | DATES | VENUE |
|------|-------|-------|-------|-------|
| 22 | FDP | DOCTORAL RESEARCH SYMPOSIUM | 21ST JULY 2017 | GNITC |
| 23 | SEMINAR | POWER OPTIMIZATION SYSTEM FOR CLASS ROOM WITH AUTOMATIC STUDENTS ATTENDENDANCE | 28TH JULY 2017 | JBREC |
| 24 | WORKSHOP | RESEARCH WORKSHOP BY IEEE | 26TH TO 27TH AUGUST 2017 | IIIT HYDERABAD |
| 25 | SEMINAR | UPLIFTMENT OF HUMANITY | 21ST DECEMBER 2017 | JBREC |
| 26 | ORGANISING | FEMA | 8TH MARCH 2018 | JBREC |
| 27 | FDP | TWO WEEK SKILL DEVOLOPEMENT PROGRAM ON MICROSOFT OFFICE | 22ND NOVEMBER TO 5TH DECEMBER 2017 | JBREC |
| 28 | PUBLISHINGS | A Study on security implementation for the prevention of Image Forgery | Volume.3,Issue.11,November.2017 | (IJASTEMS-ISSN: 2454-356X |
| 29 | PUBLISHINGS | A Novel Method of Scale-Invariant Feature Transform Based Image Forgery Detection | Vol.11, No.2, 2018 | International Journal of Intelligent Engineering and Systems(SCOPUS) |
| 30 | SEMINAR | INCENTIVES AND SCHOLORSHIPS PROVIDED BY GOVT OF INDIA | 4TH JAN 2018 | JBREC |
| 31 | PUBLISHINGS | Security implementation for the Image Forgery Detection: A review | Vol 4, Issue 11, November 2017 | IJERECE-ISSN (Online) 2394-6849 |
| 32 | SEMINAR | How to build commandble character by minority cell | 22nd march 2018 | JBREC |
| 33 | PUBLISHINGS | Nonlinear Coordinated Steering and Braking Control of Vision-Based Autonomous Vehicles in Emergency Obstacle Avoidance | vol 7 issue 8,August 2017 | IJESC |
| 34 | SEMINAR | Smart visitors access & Monitoring system - MOU project | 24TH JULY 2018 | JBREC |
| 35 | SEMINAR | RELIGION AND RESPONSIBILITIES BY Minority cell | 10th July 2018 | JBREC |
| 36 | FDP | Effective Teaching And Learning Methodolgy | 3RD JULY 2018 TO 5TH JULY 2018 | JBREC |
| 37 | SEMINAR | Addon experiments beyond syllabus for DICA,AC,PDC & EDC Labs | 17th JULY 2018 | JBREC |
| 38 | SEMINAR | Green Energy intiative | 24-Aug-18 | JBREC |
| 39 | FDP101X | FOUNDATION PROGRAM IN ICT FOR EDUCATION By IIT Bombay | 13TH SEP 2018 TO 18TH OCT 2018 | |

| 40 | SEMINAR | ADVERSE EFFECT OF MODERN TECHNOLOGY IN RELATIONSHIPS BY MINORITY CELL | 13TH NOV 2018 | JBREC |
|---|---|---|---|---|
| 41 | FDP201x | Pedology for online and blender teaching learning process by IIT Bombay | 30TH OCT 2018 TO 13 DEC 2018 | JBREC |
| 42 | SEMINAR | QUALITY IMPROVEMENT IN TECHNICAL EDUCATION THROUGH OUTCOME BASED EDUCATION | 22ND DEC 2018 | JBREC |
| 43 | SEMINAR | KHUDAI DAAVA AND ITS UNPROPITOUS EFFECTS- HISTORICAL PROSPECTIVE | 30TH JAN 2019 | JBREC |
| 44 | WORKSHOP | PROPHETS AND THEIR SERMONS BY MINORITY CELL | 18TH FEB 2019 | JBREC |
| 45 | FDP | STUDENT INDUCTION BY AICTE | 09-05-2019 TO 11-05-2019 | IAE,HYDERABAD |

## CONFERENCES AND WORKSHOPS as student:

1. Organized Student national conference **IETET-2k14** in JBREC

2. Participated in Faculty National conference **AIETM-2k14** and presented my paper.

3. Participated National conference ON EMERGING TECHNOLOGIES INEMBEDDED SYSTEMS CONDUCTED by Vignan University.

4. Got selected Technical paper for One day National conference On MODREN COMMUNICATION SYSTEMS AT JNTU ANANTHAPUR

5. Attened Workshops Mobilebotics, Robomania and Ardinox

## PROJECTS:

**1.** Using VHDL language Guided IEEE based projects in the domain VLSI In Applications

1. Network Security 2.Digital Signal Processing 3.Digital Image processing
5. Microprocessor Peripherals

2. Using Embedded C language Guided IEEE based projects in domain Embedded systems

## EXTRA CURRICULAR ACTIVITIES

1. Selected for **National level Mathematics OLYMPIAD-02** at school level.

2. Won **UNESCO** prize in Quiz competitions. AND won many prizes in Quiz .

3. Published **Technical paper** in **THE CAVE** (Digital image processing) And got selected our paper in Emerge-08.

**4.** By being a member of **THE FELINES** (A club of Technical students) participated in SYNERERGY-**05** and **06**. And organized **SYNERGY-07.**

5. Exhibited projects in **SAFIRE08** and **SRUJANA08** and won prizes.

**PERSONAL PROFILE:**

| | | |
|---|---|---|
| **Name** | : | **E.Mahesh** |
| Date of Birth | : | 1$^{st}$ july1987 |
| Father's Name | : | Nehru |
| Mother's Name | : | Bhavani |
| Languages known | : | Telugu, English, Hindi |
| Hobbies | : collecting t-shirts, Browsing Internet | |
| | Playing computer games, | |

**PERSONAL SKILLS:**

- ✓ Excellent presentation skills with Flexible and adaptive nature
- ✓ Introvert, Punctual and Sincere
- ✓ Open to learning

**DECLARATION:**

I here declare that the information furnished by me is true to the best of my knowledge.

Date:

Place: Hyderabad                                                        **(E.Mahesh)**