



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE First Semester January 2013
Electronics & Communication Engineering



J 245774
13

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1RI12EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT11	Engineering Mathematics - I	100	35	37	25	-	21	125	50	58	Pass
2	10CHE12	Engineering Chemistry	100	35	40	25	-	18	125	50	58	Pass
3	10CCP13	Computer Concepts & C Programming	100	35	40	25	-	15	125	50	55	Pass
4	10CED14	Computer Aided Engineering Drawing	100	35	35	25	-	23	125	50	58	Pass
5	10ELN15	Basic Electronics	100	35	38	25	-	20	125	50	58	Pass
6	10CPL16	Computer Programming Lab	50	20	20	25	12	16	75	32	36	Pass
7	10CHE17	Engineering Chemistry Lab	50	20	32	25	12	20	75	32	52	Pass
8	10CIV18	Environmental Studies	50	18	27	25	-	20	75	30	47	Pass
GRAND TOTAL									775	314	375	

Total Marks Obtained (in words) : THREE HUNDRED AND SEVENTY FIVE ONLY

Result of the Semester : SECOND CLASS

DATE : MAY 16, 2013

06/10/CIP/CIV18 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Second Semester July 2013

Electronics & Communication Engineering



J 548136

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT21	Engineering Mathematics - II	100	35	29	25	-	15	125	50	44	Fail
2	10PHY22	Engineering Physics	100	35	39	25	-	15	125	50	54	Pass
3	10CIV23	Elements of Civil Engg. & Engg. Mechanics	100	35	37	25	-	18	125	50	55	Pass
4	10EME24	Elements of Mechanical Engineering	100	35	42	25	-	15	125	50	57	Pass
5	10ELE25	Basic Electrical Engineering	100	35	53	25	-	15	125	50	68	Pass
6	10WSL26	Workshop Practice	50	20	32	25	12	15	75	32	47	Pass
7	10PHYL27	Engineering Physics Lab	50	20	28	25	12	15	75	32	43	Pass
8	10CIP28	Constitution of India & Professional Ethics	50	18	25	25	-	16	75	30	41	Pass
GRAND TOTAL									775	314	368	

Total Marks Obtained (In words) : THREE HUNDRED AND SIXTY EIGHT ONLY

Result of the Semester : FAIL

DATE : OCT 10, 2013

08/10/CIP/CIV28 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible



[Signature]
Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Second Semester January 2014

Electronics & Communication Engineering



J 14 294773

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT21	Engineering Mathematics - II	100	35	35	25	-	15	125	50	50	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (In words) : *****

Result of the Semester : SECOND CLASS

DATE : APR 12, 2014

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2014

Electronics & Communication Engineering



J
14 158539

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1RI12EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT31	Engineering Mathematics-III	100	35	39	25	-	18	125	50	57	Pass
2	10ES32	Analog Electronic Circuits	100	35	46	25	-	17	125	50	63	Pass
3	10ES33	Logic Design	100	35	35	25	-	15	125	50	50	Pass
4	10ES34	Network Analysis	100	35	13	25	-	15	125	50	28	Fail
5	10IT35	Electronic Instrumentation	100	35	32	25	-	17	125	50	49	Fail
6	10ES36	Field Theory	100	35	07	25	-	15	125	50	22	Fail
7	10ESL37	Analog Electronics Lab	50	20	30	25	12	16	75	32	46	Pass
8	10ESL38	Logic Design Lab	50	20	22	25	12	15	75	32	37	Pass
GRAND TOTAL									900	364	352	

Total Marks Obtained (In words) : THREE HUNDRED AND FIFTY TWO ONLY

Result of the Semester : FAIL

DATE: APR 07, 2014

A - Absent NE - Not Eligible



[Signature]
Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester July 2014

Electronics & Communication Engineering



J 599834

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10ES34	Network Analysis	100	35	36	25	-	15	125	50	51	Pass
2	10IT35	Electronic Instrumentation	100	35	47	25	-	17	125	50	64	Pass
3	10ES36	Field Theory	100	35	35	25	-	15	125	50	50	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (In words) : *****

Result of the Semester : SECOND CLASS

DATE : SEP 25, 2014

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವವರಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
 KARNATAKA, INDIA



J
14 533100

STATEMENT OF MARKS

BE Fourth Semester July 2014
 Electronics & Communication Engineering

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT41	Engineering Mathematics - IV	100	35	18	25	-	15	125	50	33	Fail
2	10ES42	Microcontrollers	100	35	42	25	-	17	125	50	59	Pass
3	10ES43	Control Systems	100	35	29	25	-	15	125	50	44	Fail
4	10EC44	Signals & Systems	100	35	15	25	-	15	125	50	30	Fail
5	10EC45	Fundamentals of HDL	100	35	30	25	-	15	125	50	45	Fail
6	10EC46	Linear ICs & Applications	100	35	35	25	-	15	125	50	50	Pass
7	10ESL47	Microcontrollers Lab	50	20	25	25	12	16	75	32	41	Pass
8	10ECL48	HDL Lab	50	20	20	25	12	20	75	32	40	Pass
GRAND TOTAL									900	364	342	

Total Marks Obtained (In words) : THREE HUNDRED AND FORTY TWO ONLY

Result of the Semester : FAIL

DATE : SEP 22, 2014

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fourth Semester January 2015

Electronics & Communication Engineering



J 15 176548

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT41	Engineering Mathematics - IV	100	35	52	25	-	15	125	50	67	Pass
2	10ES43	Control Systems	100	35	36	25	-	15	125	50	51	Pass
3	10EC44	Signals & Systems	100	35	43	25	-	15	125	50	58	Pass
4	10EC45	Fundamentals of HDL	100	35	35	25	-	15	125	50	50	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (in words) : *****

Result of the Semester : SECOND CLASS

DATE: APR 17, 2015

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester January 2015

Electronics & Communication Engineering



J 094747
15

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No.: 1RI12EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10AL51	Management & Entrepreneurship	100	35	35	25	-	20	125	50	55	Pass
2	10EC52	Digital Signal Processing	100	35	29	25	-	22	125	50	51	Fail
3	10EC53	Analog Communication	100	35	25	25	-	18	125	50	43	Fail
4	10EC54	Microwaves & Radar	100	35	35	25	-	17	125	50	52	Pass
5	10EC55	Information Theory & Coding	100	35	21	25	-	22	125	50	43	Fail
6	10EC56	Fundamentals of CMOS VLSI	100	35	37	25	-	16	125	50	53	Pass
7	10ECL57	DSP Lab.	50	20	22	25	12	18	75	32	40	Pass
8	10ECL58	Analog Communication Lab. + LIC Lab.	50	20	05	25	12	15	75	32	20	Fail
GRAND TOTAL									900	364	357	

Total Marks Obtained (In words) : THREE HUNDRED AND FIFTY SEVEN ONLY

Result of the Semester : FAIL

DATE: APR 13, 2015

A - Absent NE - Not Eligible



[Signature]

Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester July 2015
 Electronics & Communication Engineering



J 727222
15

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC52	Digital Signal Processing	100	35	35	25	-	22	125	50	57	Pass
2	10EC53	Analog Communication	100	35	25	25	-	18	125	50	43	Fail
3	10EC55	Information Theory & Coding	100	35	21	25	-	22	125	50	43	Fail
4	10ECL58	Analog Communication Lab. + LIC Lab.	50	20	25	25	12	15	75	32	40	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (in words) : *****

Result of the Semester : FAIL

DATE: OCT 17, 2015

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
KARNATAKA, INDIA



J 128629
16

STATEMENT OF MARKS

BE Fifth Semester January 2016
Electronics & Communication Engineering

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC53	Analog Communication	100	35	A	25	-	18	125	50	18	Fail
2	10EC55	Information Theory & Coding	100	35	35	25	-	22	125	50	57	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (In words) : *****

Result of the Semester : FAIL

DATE : JUL 23, 2016

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA
STATEMENT OF MARKS
BE Fifth Semester July 2016
Electronics & Communication Engineering

J
16 523769

Name of the Student : **MAHESH B**

Father's / Mother's Name : **B.SANGASHETTY**

Seat No. : **1R112EC026**

Name of the College : **R R INSTITUTE OF TECHNOLOGY, BANGALORE**

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC53	Analog Communication	100	35	41	25	-	18	125	50	59	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (in words) : *********

Result of the Semester : **SECOND CLASS**

DATE : **OCT 24, 2016**

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Sixth Semester July 2015

Electronics & Communication Engineering



J 15 912266

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC61	Digital Communication	100	35	40	25	-	17	125	50	57	Pass
2	10EC62	Microprocessors	100	35	24	25	-	17	125	50	41	Fail
3	10EC63	Microelectronics Circuits	100	35	36	25	-	15	125	50	51	Pass
4	10EC64	Antennas & Propagation	100	35	31	25	-	16	125	50	47	Fail
5	10EC65	Operating Systems	100	35	35	25	-	15	125	50	50	Pass
6	10EC66	Satellite Communications	100	35	38	25	-	16	125	50	54	Pass
7	10ECL67	Advanced Communication Lab.	50	20	21	25	12	15	75	32	36	Pass
8	10ECL68	Microprocessor Lab.	50	20	20	25	12	20	75	32	40	Pass
GRAND TOTAL									900	364	376	

Total Marks Obtained (in words) : THREE HUNDRED AND SEVENTY SIX ONLY

Result of the Semester : FAIL

DATE: NOV 04, 2015

A - Absent NE - Not Eligible



[Signature]
Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS
BE Sixth Semester January 2016
Electronics & Communication Engineering

J
16 198394

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC62	Microprocessors	100	35	27	25	-	17	125	50	44	Fail
2	10EC64	Antennas & Propagation	100	35	40	25	-	16	125	50	56	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (In words) : *****

Result of the Semester : FAIL

DATE: JUL 25, 2016

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA
STATEMENT OF MARKS
BE Sixth Semester July 2016
Electronics & Communication Engineering



J
16 457468

Name of the Student : MAHESH B

Father's / Mother's Name : B.SANGASHETTY

Seat No. : 1R112EC026

Name of the College : R R INSTITUTE OF TECHNOLOGY, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC62	Microprocessors	100	35	36	25	-	17	125	50	53	Pass
GRAND TOTAL									***	***	***	

Total Marks Obtained (In words) : *****

Result of the Semester : SECOND CLASS

DATE : OCT 22, 2016

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA
STATEMENT OF MARKS
 BE Seventh Semester January 2016
 Electronics & Communication Engineering



J
16 073261

Name of the Student : **MAHESH B**

Father's / Mother's Name : **B.SANGASHETTY**

Seat No. : **1RI12EC028**

Name of the College : **R R INSTITUTE OF TECHNOLOGY, BANGALORE**

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC71	Computer Communication Networks	100	35	57	25	-	20	125	50	77	Pass
2	10EC72	Optical Fiber Communication	100	35	54	25	-	20	125	50	74	Pass
3	10EC73	Power Electronics	100	35	57	25	-	17	125	50	74	Pass
4	10EC74	Embedded System Design	100	35	47	25	-	17	125	50	64	Pass
5	10EC751	DSP Algorithms & Architecture	100	35	36	25	-	19	125	50	55	Pass
6	10EC763	Image Processing	100	35	35	25	-	19	125	50	54	Pass
7	10ECL77	VLSI Lab.	50	20	30	25	12	18	75	32	48	Pass
8	10ECL78	Power Electronics Lab.	50	20	31	25	12	18	75	32	49	Pass
GRAND TOTAL									900	364	495	

Total Marks Obtained (in words) : **FOUR HUNDRED AND NINETY FIVE ONLY**

Result of the Semester : **SECOND CLASS**

DATE : **JUL 23, 2016**

A - Absent NE - Not Eligible



Registrar (Evaluation)



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
 KARNATAKA, INDIA



F₁₆ 078219

STATEMENT OF MARKS
 BE Eighth Semester (Electronics & Communication Engineering)

Name of the Student : **MAHESH B**

Father's / Mother's Name : **B.SANGASHETTY**

Seat No. : **1R112EC026**

Name of the College : **R R INSTITUTE OF TECHNOLOGY, BANGALORE**

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Month & Year of Passing
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EC81	Wireless Communication	100	35	45	25	-	15	125	50	60	Jul 16
2	10EC82	Digital Switching Systems	100	35	54	25	-	21	125	50	75	Jul 16
3	10EC832	Network Security	100	35	41	25	-	20	125	50	61	Jul 16
4	10EC843	GSM	100	35	54	25	-	21	125	50	75	Apr 17
5	10EC85	Project Work	100	40	94	100	50	95	200	90	189	Jul 16
6	10EC86	Seminar	-	-	-	50	20	45	50	20	45	Jul 16

*First Attempt Total (In words) : **FOUR HUNDRED AND SEVENTY NINE ONLY**

GRAND TOTAL 750 310 479 *

Result of the Semester : **FIRST CLASS**

Results of V to VIII Semesters :

Semesters	V	VI	VII	VIII	V TO VIII
Marks at First Attempt	357	376	495	479	1707
Total Marks (Max.)	900	900	900	750	3450
Result	SC	SC	SC	FC	SC



First Attempt Total (V to VIII Sem) In words : **ONE THOUSAND SEVEN HUNDRED AND SEVEN ONLY**

Result of the Degree : **SECOND CLASS**

Date : **AUG 05, 2017**

Amigi
 Registrar (Evaluation)