Assignment 7

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1 question

The expression $\sim (p \leftrightarrow q)$ is equivalent to:
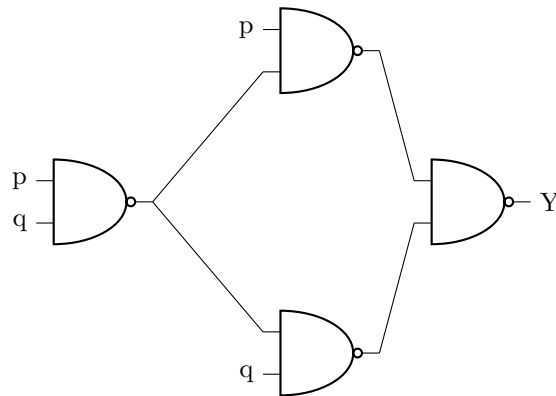

Figure 1: Logic Gate for NAND logic

$$Y = p.\bar{q} + q.\bar{p} \tag{1}$$