~ fetch address

idle

fetch address

Send = 1

Send
address

next = 1

next

address ready = 1

ready

read   waitrequest
address   read data
byte enable   read data valid

Pause

Wait

fetch address enable = 0

~pause

get address

fetch address enable = 1
read = 0

waitrequest

~waitrequest & address ready

waitrequest

address fetched

~waitrequest & address ready
fetch address enable = 0
read = 0

edge detect

~read data valid

read state

data enable = 1
read = 1
byteenable = 1111

edge detect

read data valid

get LSB data   edge detect   **get MSB data**

Counter 0

counter 1

diff states   data enable   Counter   byte enable   read   fetchaddressenable