

< Previous



Next >

Quiz Questions

[Bookmark this page](#)

Questions

0.0/2.0 points (graded)

Suppose an external device has requested the access of the system bus from 8086. Which of the following pins shows us whether 8086 accepted the request or not?

☐ S3☐ S4☒ S5☐ S6 ✓

✗

Suppose 8086 incremented SP and popped the first item from the stack. In this situation, what would be the value of the pins S4 and S3 respectively?

☒ 1 0☐ 0 1 ✓☐ 0 0☐ 1 1

✗

Submit

You have used 1 of 1 attempt

Show Answer

Answers are displayed within the problem

Question

2.0/2.0 points (graded)

Q. What is the precise value of A_0 and \overline{BHE} for the instruction: $MOV [70h], 436h$

☐ $A_0 = 0$ and $\overline{BHE} = 1$ ☐ $A_0 = 1$ and $\overline{BHE} = 0$ ☒ $A_0 = 0$ and $\overline{BHE} = 0$ ☐ $A_0 = 1$ and $\overline{BHE} = 1$ ☐ None of the other options

✓

Q. What is the precise value of A_0 and \overline{BHE} for the instruction: $MOV [43h], 869h$

☐ $A_0 = 0$ and $\overline{BHE} = 1$ ☐ $A_0 = 1$ and $\overline{BHE} = 0$

☐ $A_0 = 1$ and $BHE = 0$

☐ $A_0 = 0$ and $BHE = 0$

☐ $A_0 = 1$ and $BHE = 1$

☒ None of the other options



Submit

You have used 1 of 1 attempt

Show Answer

Answers are displayed within the problem

Question

1.25/2.0 points (graded)

Suppose you have an Intel 8086 Microprocessor which is running at a frequency of 5 Hz. Now what is the maximum number of times it can read a byte of data from the memory in 4 seconds?

☐ 5000000

☐ 50000

☐ 500

☒ 5



Suppose an Intel 8086 Microprocessor is operating at 9 MHz. Which of the following statements are true?

☒ The clock cycle is 111.11 ns *

☐ In each bus cycle, the clock remains high for 36.67 ns

☒ Data is supplied during the 222.22 ns to 333.33 ns period in a bus cycle. *

☐ The Status Bus bits become available before 222.22 ns in a bus cycle. ✓

☒ While reading, ALE stays high during the 111.11 ns to 222.22 ns period in a bus cycle.



Submit

You have used 1 of 1 attempt

Show Answer

Answers are displayed within the problem

Questions

2.0/2.0 points (graded)

Suppose, Interrupt Vector (IV) of an Interrupt Service Routine (ISR) is `ABDE2h` whose segment address is stored at memory location `00062h` and `00063h`. If the segment address of this IV is `ABCDh`, then, answer the following questions-

Q. The value at memory location `00061h` is-

☐ `CDh`

☐ `E2h`

☒ `01h`

☐ `12h`

☐ None of the above



Q. Interrupt type responsible for the above ISR is-

☐ TYPE 20

☒ TYPE 24

☐ TYPE 68

☐ None of the above



Submit

You have used 1 of 1 attempt

Show Answer

Answers are displayed within the problem

Question

1.25/2.0 points (graded)

Which of the following statements is true?

☒ For an 8259A controller the WR' pin is used to write data into the 8259A *

☐ The INT pin of the 8259A serves as an input and is connected to the INTR pin of the 8086

☒ For an 8259A controller the RD' pin is used by the 8259A to read data from the 8086

☐ The main idea behind using a pic is to increase to number of interrupts ✓

☒ If all interrupts are unmasked then the value of IMR will be 00000000 *



Suppose the interrupt flag is reset and the following 3 interrupts have occurred at the same time. So choose from the options which interrupt would be serviced first

☒ Divide Error Interrupt

☐ Breakpoint Interrupt

☐ Single Step Interrupt

☐ None of the above



Submit

You have used 1 of 1 attempt

Show Answer

Answers are displayed within the problem

< Previous

Next >

© All Rights Reserved



About Us

BracU Home

USIS

Course Catalog