CSF341 Assignment-02

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Section: 06

Answer to the Q. No 1

My id is 19101098.

MOV AX, 1910h MOV BX, 1098h ADD AX, BX

00010000100000

Value of status flag:

2F=0, SF=0, CF=0, PF=0, AF=0, OF=0

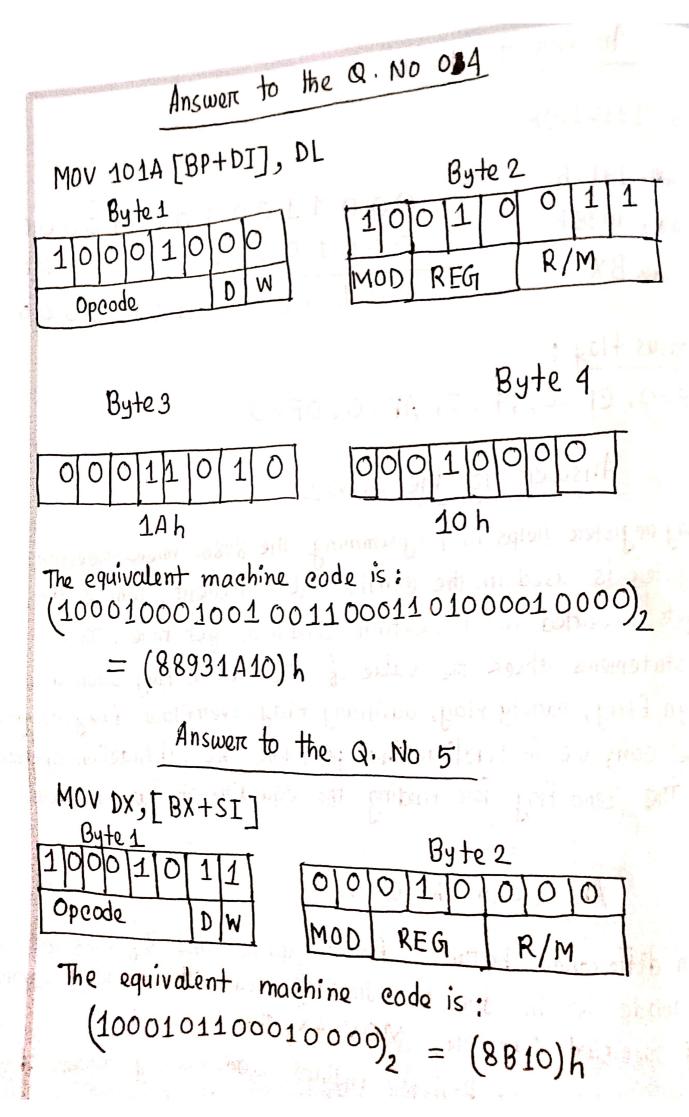
Answer to the Q. No2

The flag register helps in programming the 8086 microprocessor. The flag register is used in the conditional statement, where the code body gets executed when certain conditions get true. The conditional statements check the value of the various flag such as: Zerro flag, sign flag, parity flag, auxiliary flag, overflow flag, carry flag. Consider the compare instruction that performs the subtraction operation and checks the Zerro flag for finding the equality of two numbers.

Answer to the Q. No3

The main difference between Register direct and Register indirect addressing mode is in direct addressing mode the memory location is directly specified. Example: MOV AX, BX

On the other side, in Register indirect addressing mode, the address of the main memory location is specified. Example:





Answer to the Q. No 6

- a) MOV 101A [BP+DI], DL -> Base- πelative-plus-index addressing
- b) MOV DX, $[BX + SI] \rightarrow Base-plus-index addressing$