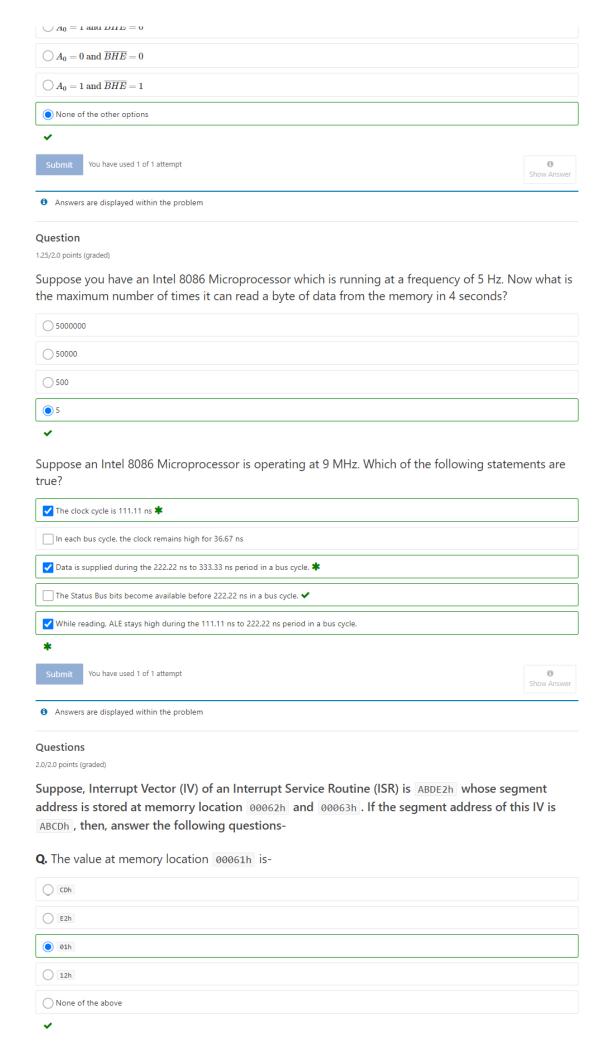
buX Courses How to Use buX FAQ About Us 19101098.mahmudul.hasan.emon MICROPROCESSORS Discussion Course Course > CSE341 Graded Quiz 3 Summer 2021 > Quiz 03 > Quiz Questions Previous Next > **Quiz Questions** ☐ Bookmark this page Questions 0.0/2.0 points (graded) Suppose an external device has requested the access of the system bus from 8086. Which of the following pins shows us whether 8086 accepted the request or not? O \$4 S5 S6

✓ Suppose 8086 incremented SP and popped the first item from the stack. In this situation, what would be the value of the pins S4 and S3 respectively? 1 0 ○ 0 1 \bigcirc 0 0 O11 You have used 1 of 1 attempt 0 1 Answers are displayed within the problem Question 2.0/2.0 points (graded) **Q.** What is the precise value of A_0 and BHE for the instruction: MOV [70h], $436\hbar$? $\bigcirc A_0 = 0$ and $\overline{BHE} = 1$ \bigcirc $A_0=1$ and $\overline{BHE}=0$ \bigcirc $A_0 = 0$ and $\overline{BHE} = 0$ $\bigcirc A_0 = 1$ and $\overline{BHE} = 1$ None of the other options **Q.** What is the precise value of A_0 and \overline{BHE} for the instruction: MOV [43h], 869h

> $\bigcirc A_0 = 0$ and $\overline{BHE} = 1$ ∩ 4 1 and DUD 0



Q. Interrupt type responsible for the above ISR is-○ TYPE 20 TYPE 24 ☐ TYPE 68 None of the above You have used 1 of 1 attempt 0 1 Answers are displayed within the problem Question 1.25/2.0 points (graded) Which of the following statements is true? ✓ For an 8259A controller the WR' pin is used to write data into the 8259A 🗱 $\hfill\Box$ The INT pin of the 8259A serves as an input and is connected to the INTR pin of the 8086 ✓ For an 8259A controller the RD' pin is used by the 8259A to read data from the 8086 ■ The main idea behind using a pic is to increase to number of interrupts ✓ If all interrupts are unmasked then the value of IMR will be 00000000 🗱 Suppose the interrupt flag is reset and the following 3 interrupts have occurred at the same time. So choose from the options which interrupt would be serviced first O Divide Error Interrupt Breakpoint Interrupt Single Step Interrupt None of the above You have used 1 of 1 attempt Answers are displayed within the problem Previous Next >

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