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### Answer to the 9. No 1

(1)

They can use a capacitive fingenprint sensor which is safer and accurate nathor than optical finger print sensor.

Mechanism: Everyone's finger has ridge and valley.

midges 3 Valley

Depending on ridge and valley, when anyone press the sensor; the sensor erreates a electrostatic field like ridge = 1 and valley = 0 and by using those value it create a digital image which is saved in a database.

Now, we can connect this arrduino Because, we know arduino requires a small processing unit. It will be connected through a analog pin of arduino. Here the data will come from sensor in non electrical form. Then arrduino will process the data. The arduino can be connected to computer

where database of fingenprint is stored. And it will help to match with previous record and only give access to the outhorised students. The arduino will provide a green LED if the student is authorised which will be connected \* with digital pin. As arduino is a open source microcontroller it can process and provide faster interfacing with such sensors with its Atmega 328 controller.

before switching the set line the high to low

Inductive proximity sensors are used for non-contact detection of metalic objects, on the other hand capacitive proximity sensors are used for non-contact detection of both metalic and non-metalic objects for example: paper, liquid etc.

#### Answer to the Q. No 2

help to motion with previous necond and only goes accept to the authorised students. The answer will provide a

John use SPI protocol and Jack use I2c priotocol.

be connected it with digital pin. As andumo is a copen source in connecess and

Here Jack use I20 protocol. For I20 protocol >

- 1. First, the moster sends the start condition to every connected slave by switching the SDA line from a high voltage level to a low voltage level before switching the SCL line from high to low.
- 2. Then, the master sends each slave the 7 or 10 bit address of the slave it wants to communicate with along with the read/write bit.
  - 3. Morreovers, each slave compares the address sent from the moster to its own address. If the address matches, the slave returns an ACK (acknowledment) bit by pulling the SDA line low

- for one bit. If the address from the master does not match the slave's own address, the slave leaves the SDA line high.
- 4. Furthermore, the moster sends on receives the data frame.
- 5. After each data frame has been transferrred, the receiving device returns another ACK bit to the sender to acknowledge successful receipt of the frame.
- 6. Lastly, to stop the data transmission, the master sends a stop condition to the slave by switching SCL high before switching SDA high.

welland of the difficult to impleme

John use SPI protocol:

## Advantages:

1) No start stop bits, so the data can be streamed continuously without interruption.

(11) High data transfer rate than I2C.

(1) Not limited to 8 bit worlds in case of bit transferring.

# Disadvantages;

1) It requires morre pins than I2C.

No harrdware flow contral.

No slave acknowledgement.

(iv) Multi Master difficult to implement.

# Answere to the 9. No 3

commond them want brownes I would profer SSD. Because, for magnetic, their file copy speed ranges from 50-120 MBs, while SSDs have a writing speed of morre than 200 MBs up to 550 MBs. As my priority is speed and portability, that's why I choose SSD.

#### <u>6</u>

I can retrive using TRIM concept. When we delete a file from windows on a typical hard drive, the file doesn't deleted immediately. Instead, the os tells the hand drive it can overwrite the physical area of the disk where that data was stored the next time it needs to pereform a write. This is why it's possible to undelete files, with a traditional HDD, the OS doesn't pe need to pay attention to whose data is being written

ore what trim command actually does, when we give modify command trim will mark some pages which are outdated fore errase. The TRIM command allows the operating system to tell the SSD it can skip rewriting certain data next time it personns a block errose.

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NAND flash memory is the key component of SSD.

Because all data is Storred here. It contains two transistors

(1) Control gate. (2) Floating transistors. Here the principal operation is based on MOSFETS.

Data storing: We have different configuration of storage like SLC, MLC, TLC, QLC. SCC storces one bit in a cell, MLC storres 2 bits, TLC 3 bits, QLC 4 bits in a cell. To write data here the data comes in voltage format and in floating gate resistor the change of the electron changes and our data is stored. We write data page wise. Since it's non-volatile the data is stored even after power gets off. So, according to SLC, MLC, TLC, QLC foremat a certain amount of data is stoned here QLC nequines procise amount of voltage since it can have 24=16 different combination of bits. This way, writing varise from one format to another.