

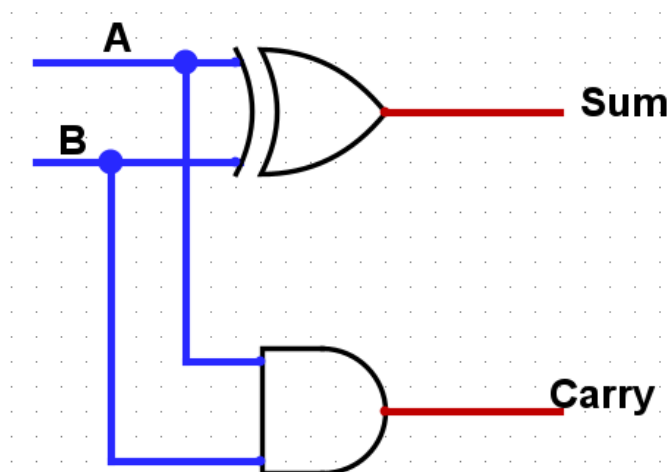
Week 3 – Hardware

Student number: 566780

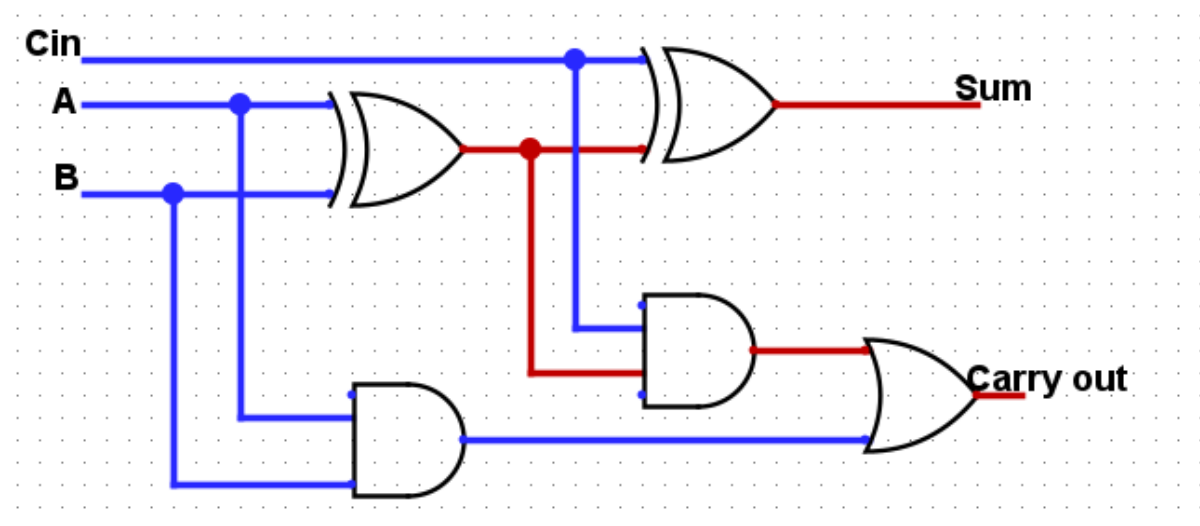
Bonus point assignment – week 3

Complete the **half adder**, **full adder** and **4-bit adder** assignment as described in the PowerPoint slides of week 3 in Logisim. Save the chip design and also export three PNG pictures of the separate finished designs. See the PowerPoint slides of week 3.

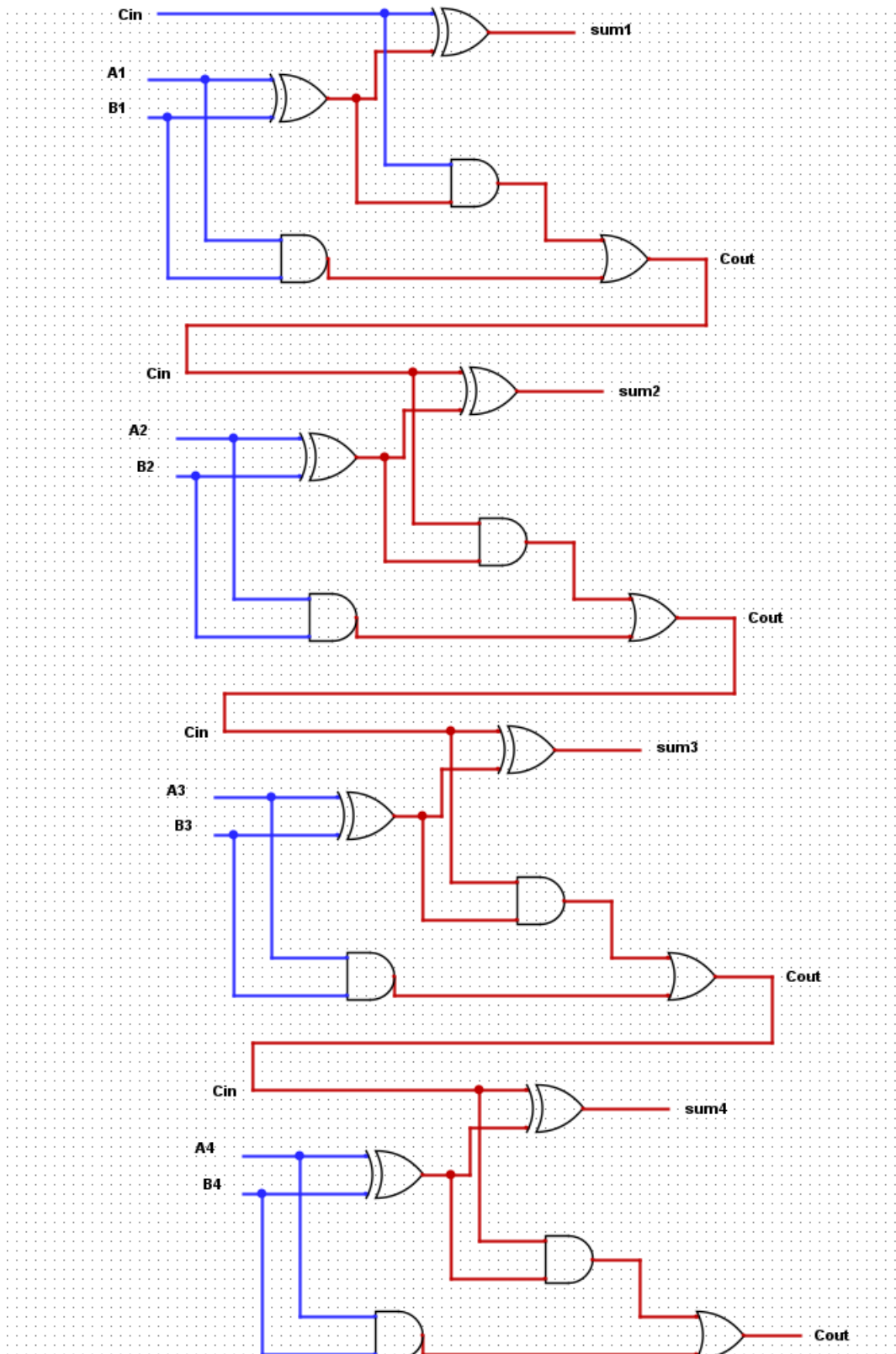
Paste the three exported PNG pictures in here.



This is the half adder logical circuit which sums up 2 single bit number and give a carry out.



This is a full adder logical circuit which adds 3 inputs.



The four bit adder takes two 4-bit input and gives a 4-bit output and a carry out