Liam Telenko

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Education

McMaster University | Hamilton, ON

Bachelor of Engineering in Computer Engineering, GPA 10.3

September 2019 – Present Expected Graduation, May 2024

Experience

Synopsys | Mississauga, ON

April 2022 – September 2023

Digital Verification Engineering Intern / Solutions Group

- Implemented and maintained constrained-random SystemVerilog UVM testbenches
- Debugged and documented mixed-signal simulation failures from daily regressions
- Wrote and implemented verification testplans

McMaster University | Hamilton, ON

July 2020 - April 2022

Web & Desktop Applications Assistant / Office of the Registrar

- Developed multiple full stack software solutions in Python for internal tasks resulting in improved efficiency of recurring processes
- Maintained technical and end-user documentation for support tickets and internally developed software
- Provided remote and in-person desktop and application support for users in a variety of production environments

Projects

Hardware Image Decompressor Digital Systems Design / 3DQ5

December 2021

The goal of the project was to decompress a proprietary image format using a hardware implementation. The project was teambased and completed in groups of two.

- Implemented colour space conversion, interpolation, and inverse-signal transformation on FPGA hardware in SystemVerilog
- Designed modules to efficiently compute matrix operations and manage data in embedded and external memory
- Completed unit tests by verifying the operation of individual modules in ModelSim

3D Visualization of Time-of-Flight Data Microprocessor Systems Project / 2DX4

April 2021

The goal of the project was to design a system which can collect and interpret data from a time-of-flight sensor. The project was completed individually.

- Developed embedded software for an ARM Cortex platform in C and assembly; wrote visualization software in Python
- Utilized the I²C and UART protocols to transfer data between the time-of-flight sensor, microcontroller, and data visualization software

Extra Curriculars

McMaster Formula Electric | Hamilton, ON

October 2020 – March 2022

Dashboard Team Lead

- Worked with previous team leads to design the structure of the software to meet hardware and competition constraints
- Utilized RTOS queue and task components to integrate the GUI with the CAN bus on the STM32 based platform