

Kailas Nath R

Software Engineer

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Forward-thinking Software Engineer with background working effectively in dynamic environments. Quickly learns and adapts to new technologies. Highly passionate about building scalable software.

Work History

2017-06 - Current

Associate

PwC, Bengaluru, Karnataka

- Worked with global team for implementing data migration tool from legacy system for leading global bank, in a project which was first of its kind.
- Lead the development for an ETL pipeline tool for a global client, which reduced system latency parameters by 30 percent.
- Worked on internal projects and POC developments which are used extensively by the company while pitching for projects.
- Gathered and defined customer requirements to develop clear specifications for project plans.

2016-06 - 2016-12

Intern

ST Microelectronics, Noida, Uttar Pradesh

- Implementation of chip to chip data transfer on Xilinx Virtex 7 FPGA for SOC R & D prototype unveiled during CES 2017.

Education

2013-06 - 2017-05

B.E. (Hons.): Electronics And Instrumentation

Birla Institute Of Technology And Science - Pilani

Coding Profiles

- LeetCode : <https://leetcode.com/skrill0/>
- Interviewbit : <https://www.interviewbit.com/profile/sKrill0>
- GitHub: <https://github.com/sKrill0>

Accomplishments

- Star & R Client Appreciation Award : PwC, 2020
- Got the highest rating for the performance : PwC, 2019
- Recommendation letter from Senior Design team : ST Microelectronics, 2016

Skills

- C++, Python, SQL
- Data Structures and Algorithms, System Design
- Spring Boot, Redis, Kafka