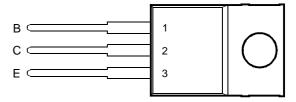
- Designed for Complementary Use with the BD241 Series
- 30 W at 25°C Case Temperature
- 2 A Continuous Collector Current
- 4 A Peak Collector Current
- Customer-Specified Selections Available

### TO-220 PACKAGE (TOP VIEW)



Pin 2 is in electrical contact with the mounting base.

MDTRACA

### absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT		
	BD240		-55		
Collector-emitter voltage ( $R_{BE} = 100 \Omega$ )	BD240A	\/	-70	V	
	BD240B	V <sub>CER</sub>	-90	v	
	BD240C		-115		
	BD240		-45		
Collector-emitter voltage (I <sub>C</sub> = -30 mA)	BD240A	\/	-60	V	
	BD240B	V <sub>CEO</sub>	-80		
	BD240C		-100		
Emitter-base voltage			-5	V	
Continuous collector current			-2	Α	
Peak collector current (see Note 1)			-4	Α	
Continuous base current			-0.6	Α	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			30	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			2	W	
Unclamped inductive load energy (see Note 4)			32	mJ	
Operating junction temperature range			-65 to +150	°C	
Storage temperature range			-65 to +150	°C	
Lead temperature 3.2 mm from case for 10 seconds			250	°C	

NOTES: 1. This value applies for  $t_p \le 0.3$  ms, duty cycle  $\le 10\%$ .

- 2. Derate linearly to 150°C case temperature at the rate of 0.24 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH,  $I_{B(on)}$  = -0.4 A,  $R_{BE}$  = 100  $\Omega$ ,  $V_{BE(off)}$  = 0,  $R_S$  = 0.1  $\Omega$ ,  $V_{CC}$  = -20 V.



# BD240, BD240A, BD240B, BD240C PNP SILICON POWER TRANSISTORS

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# electrical characteristics at 25°C case temperature

PARAMETER			TEST CONDITION	ONS	MIN	TYP	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> = -30 mA (see Note 5)	I <sub>B</sub> = 0	BD240 BD240A BD240B BD240C	-45 -60 -80 -100			V
I <sub>CES</sub>	Collector-emitter cut-off current	$V_{CE} = -55 \text{ V}$ $V_{CE} = -70 \text{ V}$ $V_{CE} = -90 \text{ V}$ $V_{CE} = -115 \text{ V}$	$V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$	BD240 BD240A BD240B BD240C			-0.2 -0.2 -0.2 -0.2	mA
I <sub>CEO</sub>	Collector cut-off current	$V_{CE} = -30 \text{ V}$ $V_{CE} = -60 \text{ V}$	$I_{B} = 0$ $I_{B} = 0$	BD240/240A BD240B/240C			-0.3 -0.3	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> = -5 V	I <sub>C</sub> = 0				-1	μА
h <sub>FE</sub>	Forward current transfer ratio	$V_{CE} = -4 V$ $V_{CE} = -4 V$	$I_{C} = -0.2 \text{ A}$ $I_{C} = -1 \text{ A}$	(see Notes 5 and 6)	40 15			
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>B</sub> = -0.2 A	I <sub>C</sub> = -1 A	(see Notes 5 and 6)			-0.7	V
V <sub>BE</sub>	Base-emitter voltage	V <sub>CE</sub> = -4 V	I <sub>C</sub> = -1 A	(see Notes 5 and 6)			-1.3	V
h <sub>fe</sub>	Small signal forward current transfer ratio	V <sub>CE</sub> = -10 V	$I_{C} = -0.2 \text{ A}$	f = 1 kHz	20			
h <sub>fe</sub>	Small signal forward current transfer ratio	V <sub>CE</sub> = -10 V	$I_{C} = -0.2 \text{ A}$	f = 1 MHz	3			

NOTES: 5. These parameters must be measured using pulse techniques,  $t_p$  = 300  $\mu$ s, duty cycle  $\leq$  2%.

### thermal characteristics

PARAMETER			TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			4.17	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

# resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t <sub>on</sub>	Turn-on time	I <sub>C</sub> = -200 mA	$I_{B(on)} = -20 \text{ mA}$	$I_{B(off)} = 20 \text{ mA}$		0.2		μs
t <sub>off</sub>	Turn-off time	$V_{BE(off)} = 3.4 V$	$R_L = 150 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		0.4		μs

 $<sup>^{\</sup>dagger} \ \ \mbox{Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.}$ 

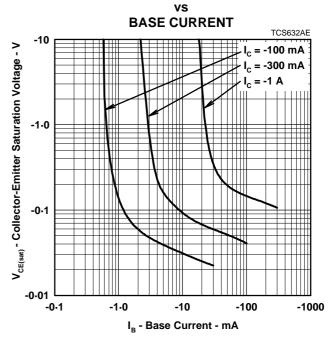
<sup>6.</sup> These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

# **TYPICAL CHARACTERISTICS**

# TYPICAL DC CURRENT GAIN VS COLLECTOR CURRENT $T_{CS632AG}$ $T_{CE} = -4 \text{ V}$ $T_{CE} = 300 \text{ µs, duty cycle} < 2\%$ $T_{CE} = 300 \text{ µs, duty cycle} < 2\%$ $T_{CE} = 300 \text{ µs, duty cycle}$ $T_{CE} = 300 \text{$

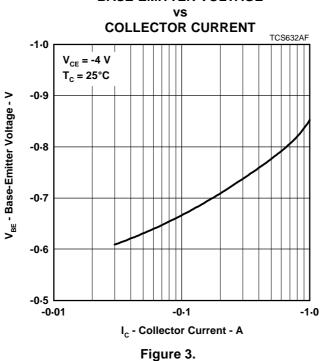
Figure 1.

# **COLLECTOR-EMITTER SATURATION VOLTAGE**



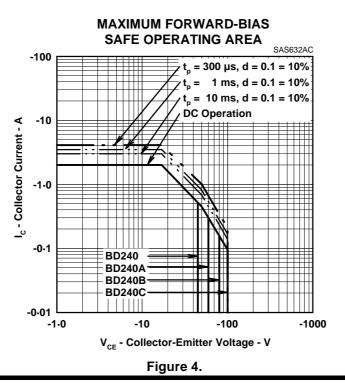
### Figure 2.

### **BASE-EMITTER VOLTAGE**



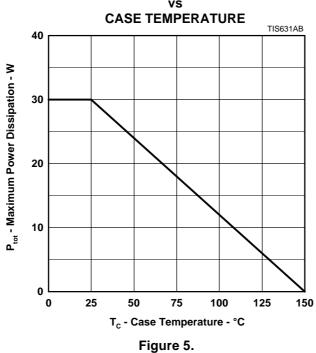
Power

### **MAXIMUM SAFE OPERATING REGIONS**



### THERMAL INFORMATION

# MAXIMUM POWER DISSIPATION

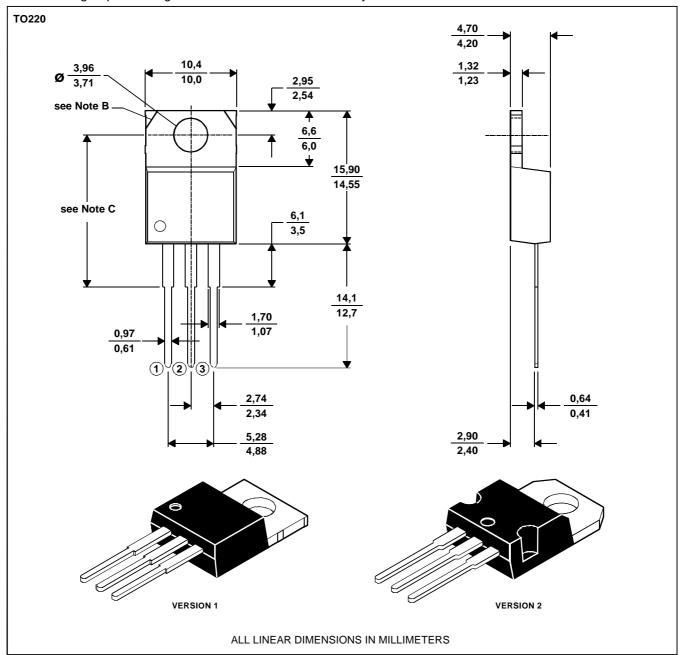


### **MECHANICAL DATA**

### **TO-220**

# 3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.

C. Typical fixing hole centre stand off height according to package version. Version 1, 18.0 mm. Version 2, 17.6 mm. MDXXBE



# BD240, BD240A, BD240B, BD240C PNP SILICON POWER TRANSISTORS

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