





40P-50M

JLCJLCJLCJLC

CLK IO

5V

3.3V

DB DEBUG V1.0

By Majianjia

GND
CLK
IO
3.3V

TX GND RX

Comment	Description	Designator	Footprint	LibRef	Quantity
Cap Semi	Capacitor (Semiconductor SIM Model)	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14	1608[0603]	Cap Semi	14
LED0	Typical INFRARED GaAs LED	D1, D2	INDC1608L	LED0	2
440247-2	USB 2.0, Right Angle, SMT, B Type, Receptacle, 5 Position, Black	J1	440247	440247-2	1
SWDIN	Header, 3-Pin	P1	HDR1X3	Header 3	1
Header 3	Header, 3-Pin	P2	HDR1X3	Header 3	1
Header 4	Header, 4-Pin	P3	HDR1X4	Header 4	1
Header 2	Header, 2-Pin	P4, P5	HDR1X2	Header 2	2
DEBUG_M	Header, 8-Pin	P6	FPC 8P 0.5mm	Header 8	1
DEBUG_M_R	Header, 8-Pin	P7	FPC 8P 0.5mm	Header 8	1
Res3	Resistor	R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19	J1-0603	Res3	19
STM32F103C8T6	STM32 ARM-based 32-bit MCU with 64 Kbytes Flash, 48-pin LQFP, Industrial Temperature	U1	LQFP48_N	STM32F103C8T6	1
FE 1.1		U2	SSOP28	FE 1.1	1
CP2102		U3	QFN-ML28_M	CP2102	1
CRYSTAL	CSCTE8M	X1	3213 CSTCE	Crystal	1
XTAL	Crystal Oscillator	Y1	HC-49U	XTAL	1