

1 2 3 4 5 6

Sheet: ESP32

Sheet: POWER

Sheet: DRIVE

File: POWER.sch

Sheet: USB

Sheet: SD

Sheet: ENDSTOP

Sheet: Laser&Spindle

Sheet: P-AUX

File: USB.sch

File: SD.sch

File: ENDSTOP.sch

File: Laser&Spindle.sch

File: P-AUX.sch

File: ESP32.sch

File: DRIVE.sch

FID1 FID2 FID3
Fiducial Fiducial Fiducial

(●)

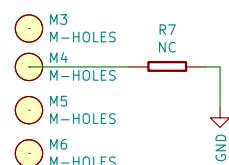
(●)

(●)

M1

M2

M7



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File: MKS DLC32 MAX.sch

Title: MKS DLC32 MAX

Size: A4 Date: 2024-08-22

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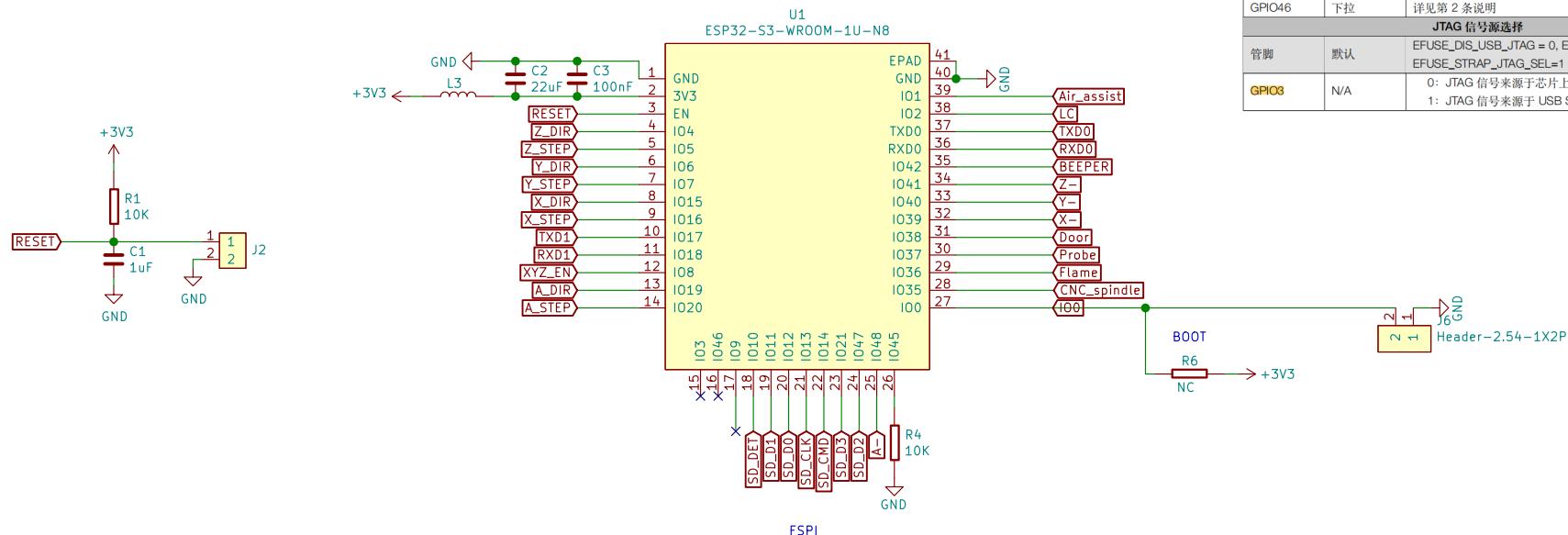
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表 5: Strapping 管脚

VDD_SPI 电压			
管脚	默认	3.3 V	1.8 V
GPIO45	下拉	0	1
系统启动模式 ¹			
管脚	默认	SPI 启动模式	下载启动模式
GPIO00	上拉	1	0
GPIO46	下拉	无关项	0
系统启动过程中, 控制 ROM Code 打印 ²			
管脚	默认	正常打印	上电不打印
GPIO46	下拉	详见第 2 条说明	详见第 2 条说明
JTAG 信号源选择			
管脚	默认	EFUSE_DIS_USB_JTAG = 0, EFUSE_DIS_PAD_JTAG = 0, EFUSE_STRAP_JTAG_SEL=1	
GPIO3	N/A	0: JTAG 信号来源于芯片上的 JTAG 管脚 1: JTAG 信号来源于 USB Serial/JTAG 控制器	



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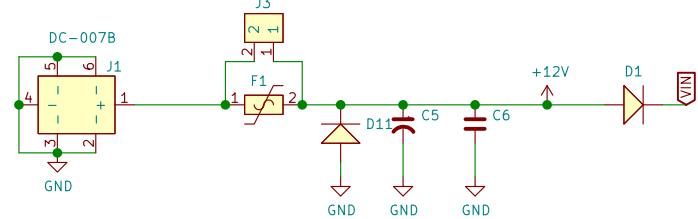


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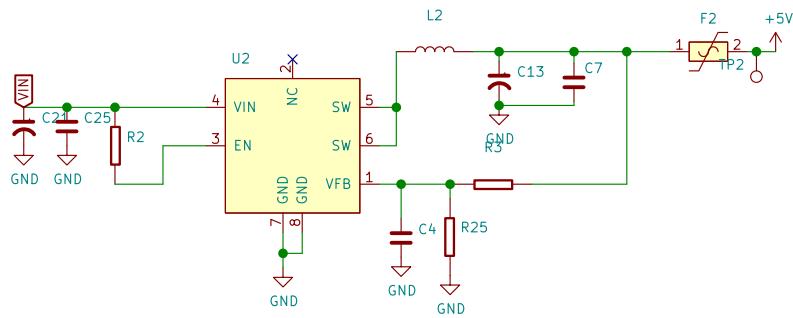
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A



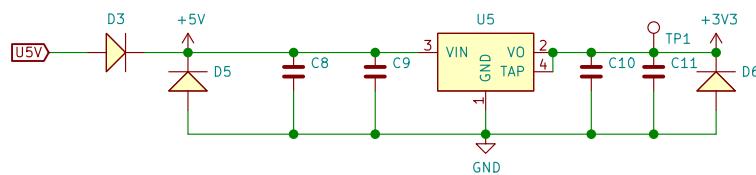
B



+12V
GND

XH2.54-2P
J13

C



+3V3
R5
D4 RED
GND

D

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A

A

B

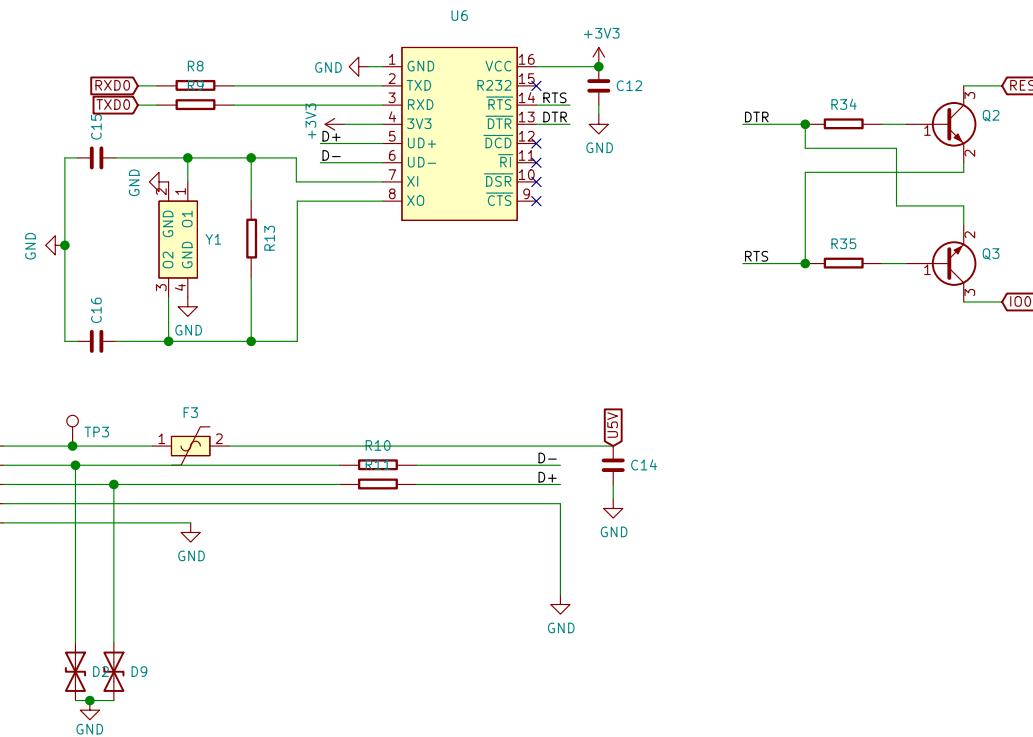
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C

C

D

D



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A

A

B

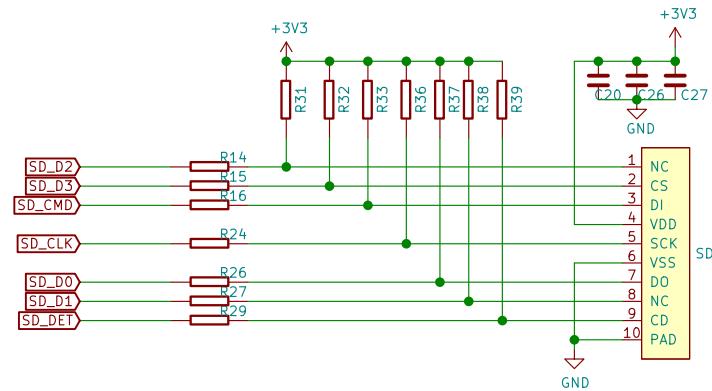
B

C

C

D

D



SD 3.0

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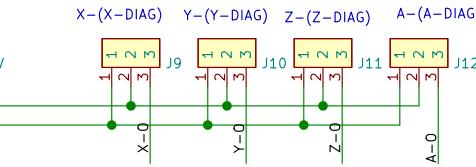
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1 2 3 4 5 6

1 2 3 4 5 6

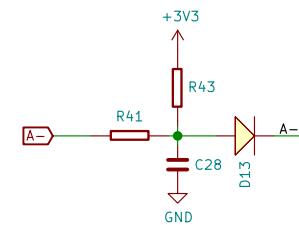
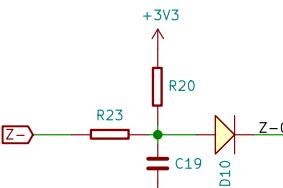
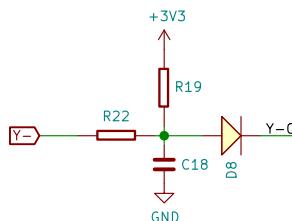
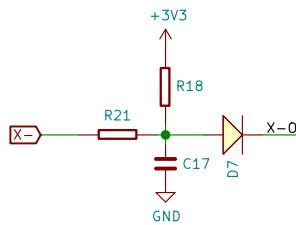
A

A



B

B



C

C

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Sheet: /ENDSTOP/
File: ENDSTOP.sch

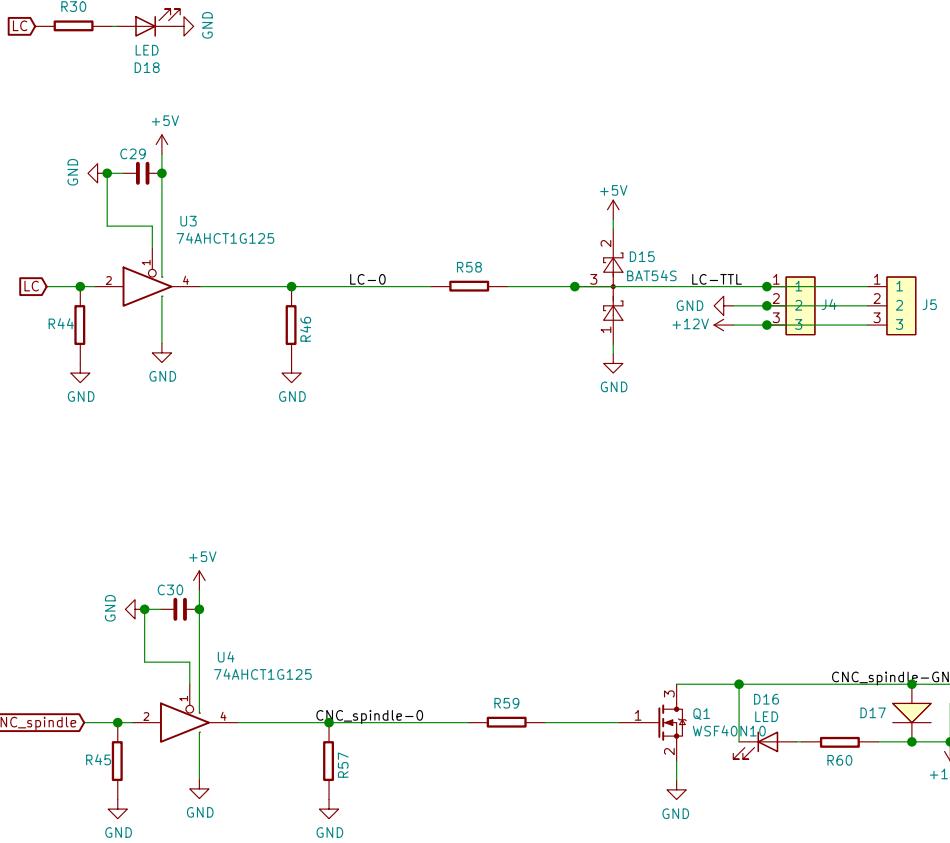
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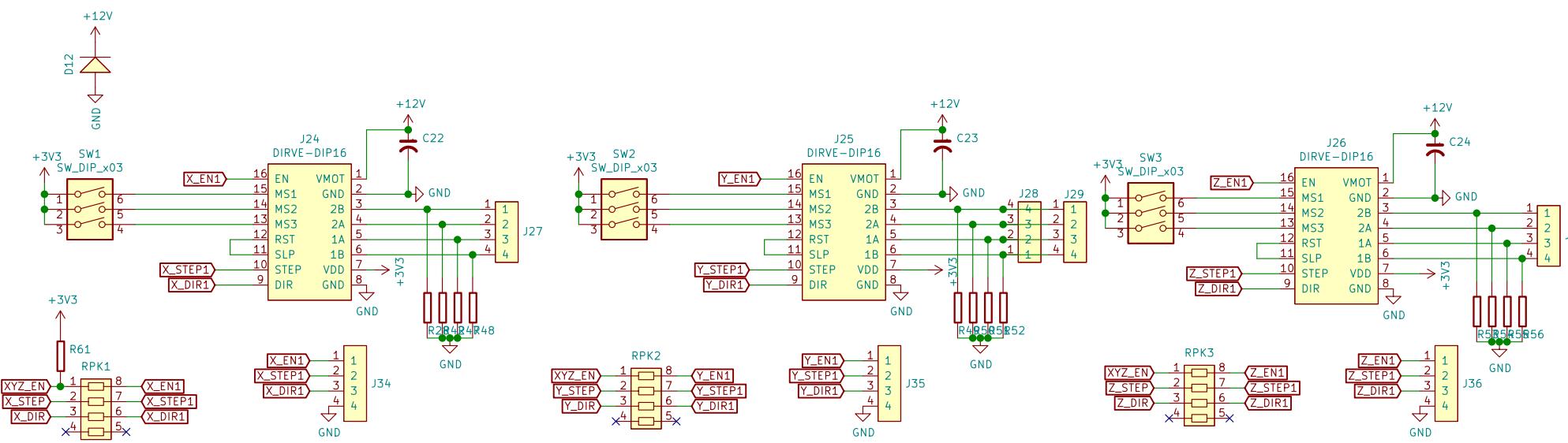
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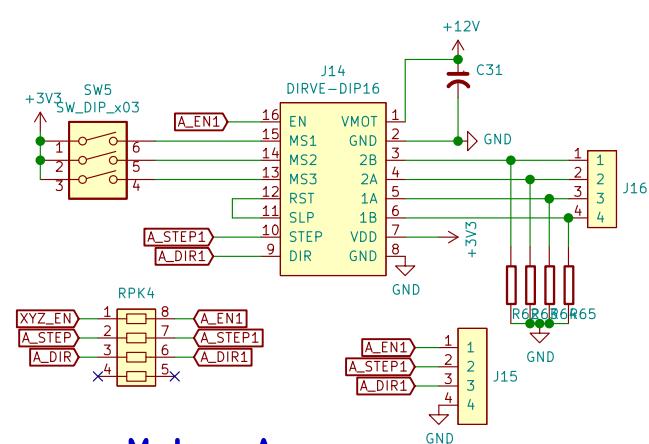
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Motor X

Motor Y

Motor Z



Motor A

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Rev: V1.0 001

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